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INSTITUTE OF ENGINEERING AND TECHNOLOGY  
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Hyderabad | PIN: 500068

## CENTRAL ACADEMIC COMMITTEE

CAC/CIR-1/2018-19

18.6.2018

### CIRCULAR

Dear Faculty,

This is to inform all the members of Central Academic Committee that there is a meeting going to be held on 21.06.2018 at 10.30AM in Principal Sirs room to discuss the following points for the academic year 2018-19, I semester.

#### AGENDA:

1. The committee-its rules and responsibilities.
2. Preparing of Academic Calendar
3. Teaching-learning process & monitoring
4. Time tables.
5. Attendance and Result analysis.
6. Stake holders feedback analysis.

Yours faithfully

Prof. *M. Abdul Kalam*

HOD-CE

Committee Co-ordinator

Copy to

1. Principal Sir
2. IQAC
3. All HoDs
4. All committee members

*Aswath*  
PRINCIPAL  
SREYAS INSTITUTE OF ENGG:TECH.  
9-39, Sy.No: 107, Tattiannaram (M),  
GSI, Bandlaguda, Nagole, Hvd-68.



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**CENTRAL ACADEMIC COMMITTEE**  
**MINUTES OF MEETING**

21.06.2018

**THEME OF THE MEETING**

- Preparing of Academic Calendar
- Teaching-learning process & monitoring
- Appointment of new committee members

**DATE OF THE MEETING** 21.06.2018  
**VENUE OF THE MEETING** Principal Sirs' chamber  
**TIME** 10.30 AM

**MINUTES OF MEETING**

The Coordinator has appointed new committee members and given guidance to carry out the responsibilities scrupulously.

1. The Academic Calendar and the academic activities were planned after detailed discussion with the HoDs.
2. Information was gathered from HoDs about the teaching-learning process and ensured the proper implementation. It is also suggested by the HoDs to introduce new teaching methods.
3. Also suggested to the members to monitor and follow-up of students about learning outcomes.
4. Attendance And Result Analysis to Improve in coming academic years  
Time tables of each class and years
5. Action suggested on Teacher, Student, Alumina, Employer, feedback report.(2017-18)

S.NO.	NAME OF THE MEMBER	DESIGNATION	SIGNATURE
1.	Mr. Y. Krishnaiah	Associate Professor	
2.	Dr. N. Muralikrishna	Professor	
3.	Dr. V.Gowtham	Associate Professor	
4.	Mr. Md.Naseeruddin	Associate Professor	
5.	Dr. J. Panduranga rao	Professor	

Prof. M. Abdul Kalam

HOD-CE

Committee Co-ordinator

Dr. Suresh Akella

Principal

**PRINCIPAL**  
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## CENTRAL ACADEMIC COMMITTEE

CAC/CIR-2/2018-19

12.11.2018

### CIRCULAR


Dear faculty

This is to inform all the members of Central Academic Committee that there is a meeting going to be held on 14.11.2018 at 10.45AM in Principal Sirs' room to discuss the following points for the academic year 2018-19 II Semester.

#### AGENDA:

1. Review of I semester academic performance.
2. Planning of Academic Activities.
3. Academic Rewards.
4. Attendance and Result analysis.
5. Annual day, Tech fest and sports fest.
6. Any other issues.

Yours faithfully

Prof.   
HOD-CE  
Committee Co-ordinator

Copy to

1. Principal Sir
2. IQAC
3. All HoDs
4. All committee members



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## CENTRAL ACADEMIC COMMITTEE

14.11.2018

### MINUTES OF MEETING

#### THEME OF THE MEETING

- Review of I semester academic performance.
- Planning of Academic Activities.
- Academic Rewards.

DATE OF THE MEETING 14.11.2018  
VENUE OF THE MEETING Principal Sirs' chamber  
TIME 10.45 AM

#### MINUTES OF MEETING

1. Information gathered from HoDs about the academic activities and the academic performance.
2. All members recommended announcing academic reward for the students.
3. Also suggested to the members to monitor and follow-up the students with poor performance.
4. Time tables of each class and years.
5. Annual day, Tech fest and sports fest

S.NO.	NAME OF THE MEMBER	DESIGNATION	SIGNATURE
1.	Mr. Y. Krishnaiah	Associate Professor	
2.	Dr. N. Muralikrishna	Professor	
3.	Dr. V.Gowtham	Associate Professor	
4.	Mr. Md.Naseeruddin	Associate Professor	
5.	Dr. J. Panduranga rao	Professor	

Prof. M. Abdul kalam  
HOD-CE  
Committee Co-ordinator

Dr. Suresh Akella  
Principal  
SREYAS INSTITUTE OF ENGG.&TECH.  
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# SREYAS Institute of Engineering & Technology

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## Department of Electronics and Communication Engineering

### ANNEXURE-1 : List of subjects

Sl.No	Course Code	R16 II YEAR ECE SEM-II SUBJECTS	
1.	EC302ES	Analog electronics	AE
2.	EC303ES	Electrical technology	ET
3.	EC304ES	Signals And Stochastic Process	SSP
4.	EC305ES	Network analysis	NA
5.	EC306ES	Electronic Devices And Circuits Lab	EDC Lab
6.	EC307ES	Basic Simulation Lab	BS Lab
7.	EC308ES	Basic Electrical Engineering lab	BEE Lab

Sl.No	Course Code	R15 III YEAR ECE SEM-I SUBJECTS	
1	A50217	Control Systems Engineering	CSE
2	A50516	Computer Organization and Operating Systems	COOS
3	A50418	Antenna and Wave Propagation	AWP
5	A50422	Electronic measurements and Instrumentation	EMI
6	A50408	Analog Communications	AC
7	A50425	Linear and Digital IC Applications	LDICA
8	A50482	Analog Communications Lab	AC Lab
9	A50488	IC Applications and HDL Simulation Lab	ICHDL Lab

Sl.No	Course Code	R13 IV YEAR ECE SEM-I SUBJECTS	
1.	A70436 A70443 A70505	Digital image processing	DIP MSC OOPS
		Multimedia and signal coding	
		Object oriented programming through java	
2.	A70442	Microwave Engineering	MWE
3.	A70447 A70444 A70440	Television engineering	TVE OC ESD
		Optical communications	
		Embedded system design	
4.	A70434	Cellular Mobile communications	CMP
5.	A70515	Computer Networks	EN
6.	A70499	Microwave & Digital Communication Lab	MWDC LAB

*Asuresh*  
PRINCIPAL  
SREYAS INSTITUTE OF ENGINEERING & TECHNOLOGY  
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## Department of Electronics and Communication Engineering

### Service Courses:

Sl.No	Course Code	R16 1 YEAR MECH/CIVIL/ CSE SEM-I SUBJECTS	
1.	CS304ES	Digital Logic Design	DLD
Sl.No	Course Code	R16 1 YEAR SUBJECTS	
2.	EE106ES	Basic Electrical And Electronic Engineering	BEEE

*Asuresh*

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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A.Y 2017-18

SEMESTER-I

12-06-2017

### Annexure -II

S.N O	Name of the Staff	Designation	SUBJECT-1	SUBJECT-2	SUBJECT-3	SIGN
1	Mr B SREENIVASU	Associate Professor & HOD	AC	DSD		<i>[Signature]</i>
2	Mr CH S V MARUTHI RAO	Associate Professor & Deputy-HOD	CSE	COOS	DLD	<i>[Signature]</i>
3	Dr S VENKATESWARLU	Professor		PG Faculty		<i>[Signature]</i>
4	Dr MD RASHID MAHMOOD	Professor	CMC	PTSP		<i>[Signature]</i>
5	Dr SHAIK SYED BASHA	Professor	ESD	DSD		<i>[Signature]</i>
6	Dr SANDEEP KUMAR	Professor	CS	DIP	ESD	<i>[Signature]</i>
7	Dr.M.SUSHANTH BABU	Professor	CMC	NATL		<i>[Signature]</i>
8	Dr A KISHORE REDDY	Associate Professor	DIP	SS		<i>[Signature]</i>
9	Dr.T.RAVICHANDRA BABU	Associate Professor	ESD	PTSP		<i>[Signature]</i>
10	Mr V A SANKAR	Associate Professor	MWE	AWP		<i>[Signature]</i>
11	Mr V TAJ KIRAN	Associate Professor	AE	EDG		<i>[Signature]</i>
12	Mr.B.AMARENDER REDDY	Associate Professor	SSP	CI		<i>[Signature]</i>
13	Mr J PANDU RANGA RAO	Associate Professor	SSP	AE		<i>[Signature]</i>
14	Mr N RAMESH	Associate Professor	E.T	N.A	BEEF	<i>[Signature]</i>
15	Mr P PRAVEEN RAJU	Associate Professor	EMI	PTSP		<i>[Signature]</i>
16	Mr Y SATISH KUMAR	Associate Professor	SSP	AWP	MWE	<i>[Signature]</i>
17	Mr G SRINIVAS RAO	Associate Professor	ET	BEG Lab		<i>[Signature]</i>
18	Mr G VUAY GOUD	Associate Professor	MWE	AC		<i>[Signature]</i>
19	Mrs HEMLATA DALMIA	Associate Professor	AE	LDICA	CN	<i>[Signature]</i>
20	Mr MAHESH KUSUMA	Associate Professor	CS	AMP	GLB	<i>[Signature]</i>
21	Mrs B SPANDANA	Assistant Professor	COOS	DLD	SD	<i>[Signature]</i>
22	Mr SHAIK BRAHMAIAH	Assistant Professor	EMI	SS		<i>[Signature]</i>
23	Mrs A SOWJANYA	Assistant Professor	CMC	COOS	SSP	<i>[Signature]</i>
24	Mrs A PRAVEENA	Assistant Professor	ESD	CMC	COOS	<i>[Signature]</i>

*ASUS*  
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25	Mr MOHAMMED RAFI SHAIK	Assistant Professor	LOICA	AWP	DSP	scribble
26	Mr G D BASAVARAJ	Assistant Professor	LOICA	AWA	MUP	scribble
27	Mr K NARSIMHA	Assistant Professor	CN	TVE	ESD	scribble
28	Mrs V IYOTHI	Assistant Professor	BEEE	DSD		scribble
29	Mr Y SRAVAN KUMAR	Assistant Professor	NA	ET	BEE	scribble
30	Mr G RAMACHANDRA KUMAR	Assistant Professor	LOICA	CM	CN	scribble
31	Mr S SUBRAHMANYAM	Assistant Professor	AE	CSE		scribble
32	Mrs S PALLAVI	Assistant Professor		LOICA	AE	scribble
33	Ms K SONALI SWAROOP	Assistant Professor	ESD	LOICA	SFP	K-band
34	Ms VUPPALA SWATHI	Assistant Professor	AE	CSE	DIP	scribble
35	Mrs RUKSANA SULTHANA	Assistant Professor		PG FACULTY		scribble
36	Ms M DIVYA	Assistant Professor	DIP	PTSP		scribble
37	Ms SANJANA MATHEW	Assistant Professor	AE	AC	CMC	scribble
38	Mrs D NIVEDITHA	Assistant Professor		AC Lab		scribble
39	Ms P SRI PALLAVI	Assistant Professor	AC Lab	CM		scribble
40	Mrs NEHA ANJUM	Assistant Professor	AC Lab	CSE		scribble
41	Mr T SUMAN	Assistant Professor	ESD	CN	MWED CL	scribble
42	Mrs CH BINDU	Assistant Professor		DSD		scribble
43	Mrs C YAMUNA	Assistant Professor	RDL Lab	Edc Lab		scribble
44	Mr K HARISH VARRMA	Assistant Professor	AC Lab	DSD Lab		scribble
45	Ms B SRAVANTHI	Assistant Professor	BS Lab	Edc Lab		scribble
46	Mrs SHAIK ASHA BEE	Assistant Professor	BEE Lab	BS Lab		scribble
47	Mr O CHIRANJEEVI	Assistant Professor			PG Faculty	Chiranjeevi
48	Mrs P SIREESHA	Assistant Professor	Cdc Lab	Dsd Lab		scribble
49	Mr R SURESH	Assistant Professor	AE	CSE		scribble
50	Mr B PARSHURAMULU	Assistant Professor	JCA Lab	Dsd Lab		scribble
51	Mrs M NAVANEETHA	Assistant Professor	BEE Lab	Edc Lab		scribble
52	Mrs K TEJASWI	Assistant Professor	ESD	LOICA		scribble
53	Mrs M. GAYATRI	Assistant Professor	BEEE	DSD		Asuresh
54	Mrs M SAI SRI	Assistant Professor	CN			scribble
55	Ms K PRASHANTHI	Assistant Professor	PG FACULTY			scribble
56	Ms M NEERAJA	Assistant Professor	NA	ET		scribble
57	Ms B SUSHANTHI	Assistant Professor	JCA Lab	CN		scribble
58	Ms T REVATHI	Assistant Professor	AE	AC		scribble

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 GSt. Bandlaguda, Nago

59	Mrs M JHANSI	Assistant Professor	BEEC	BS Lab			
60	Ms V SWATHI	Assistant Professor	EDC Lab	CS Lab			
61	Mr G ANIL KUMAR	Assistant Professor	AC Lab	EDC Lab			
62	Ms B UMASREE	Assistant Professor	Pier Lab	DSD Lab			
63	Ms Y RADHIKA	Assistant Professor	PC Faculty				
64	Mrs SEETHA YAMINI	Assistant Professor		BS Lab			
65	Mrs P CHAITANYA	Assistant Professor	DLD	COOS	CNI		
66	Ms R SIRISHA	Assistant Professor	BEEC	BS Lab			

*R. Kumar*

HOD

Head of the Department  
 Electronics & Communication Engineering  
 Sreyas Institute of Engineering and Technology  
 Beside Indu Aranya, Nellore, H. No. 100/101/102/103/104/105

*Asuresh*

PRINCIPAL

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 DS, Bandlaguda, Nagole, Hyd-68.



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Hyderabad, PIN: 500068

## Department of Electronics and Communication Engineering

### CIRCULAR

Date: 05/02/17

SIET/ECE/A.Y: 2017-18/I-Sem/Subject Allotment/02

Subject allotment for I semester

Ref. SIET/ECE/A.Y: 2017-18/I-Sem/Subject Allotment/01

Dear faculty,

With reference mentioned above it is to inform you that the following subjects (Theory & Labs) have been tentatively allotted for ECE faculty to handle subject for II, III & IV B.Tech. I-Semester students of ECE/CSE branches for the academic year: 2018-19.

\*faculty members are requested to make a note of the allotted subject and start their course file and submit the same to HOD-ECE. Please note that the contents of course file can be found in Annexure-2.

Encl : Annexure-1. Subject Allotment

Annexure-2. Course file contents

*Aswath*  
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001, Bandlaguda, Nagole, Hyd-68.

*R. Srinivas*  
HOD-EC  
Head of the Department  
Electronics & Communication Engineering  
Sreyas Institute of Engineering & Technology  
Nagole, Hyderabad, Telangana, India



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## Department of Electronics and Communication Engineering

### ANNEXURE-2: Course File Contents

#### Course file Index

1. Vision and Mission of Department
2. PEOs, POs and PSOs
3. Course Syllabus copy
4. Course learning objectives CLOs
5. Course Outcomes (COs)
6. COs and POs mapping matrix.
7. COs and PSOs mapping matrix
8. Academic Calendar.
9. Individual Time Table & Class Timetable.
10. Lesson Planner topic wise. (Incorporate relevant Textbooks, References and web reference)
11. Unit wise Lecture Notes
12. Topics beyond Syllabus (TBS)
13. Question Bank.....
  - a) Unit wise Question bank. - Include taxonomy Levels & Year in which question is asked
  - b) Previous Years Question papers
  - c) List of important Questions for Examinations
14. Tutorial Problems
15. Tutorial Evidence.
16. Assignment Questions
17. Assignment Evidence
18. Remedial classes for weak students
19. Guest lecturer from Eminent Professors / Scientist from reputed industry
20. Mid-1 & Mid-2 Question paper and Solution and Scheme of Evaluation.
21. Mid-1 & Mid-2 - 2 -Best, 2- Average and 1- poor Answer Scripts photocopy.
22. Result Analysis with evidence and Course Attainment calculation.

*Asuresh*

PRINCIPAL,  
SREYAS INSTITUTE OF ENGINEERING & TECHNOLOGY,  
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## Department of Electronics and Communication Engineering

A.Y 2017-18 I-SEMESTER

Date: 05/07/17

SJET/ECE/A.Y: 2017-18/I-Sem/Subject Allotment/03

To

The Head of the Department,

Humanities and Science,

S.I.E.T,

Sub: - Request to depute faculty to handle service course for ECE-Reg.

Sir as per the curriculum of Electronics and Communication engineering framed by JNTUH (R16&R13 regulations), the following courses are to be handled by Humanities and Science Department for Electronics and Communication Engineering for the academic year 2017-18, I Semester.

S No	Year & Sections	Course
1	II ECE A,B,C & D (R16)	Mathematics-IV
2	II ECE A,B,C & D (R16)	Environmental Science and Technology
3	IV ECE A,B,C & D (R13)	Management Science
4	IV ECE A,B,C & D (R13)	Advanced English Communication Skills Lab

In this regard I request you to depute <sup>A. Surech</sup> faculty to handle the mentioned subjects.

PRINCIPAL,  
SREYAS INSTITUTE OF ENG.&TECH.  
9-39, Sy.No: 107, Tolliannaram (V),  
GSI, Bandlaguda, Nagole, Hyd-50

*[Signature]*  
HOD-ECE  
Head of the Department  
Electronics & Communication Engineering  
Sreyas Institute of Engineering and Technology  
Beside Indu Aranya, Nagole, Hyderabad-500068



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## Department of Electronics and Communication Engineering

A.Y 2017-18 I-SEMESTER

Date: 05/07/2017

SIET/ECE/A.Y: 2017-18/I-Sem/Subject Allotment/04

To

The Head of the Department,

Humanities and Science,

S.I.E.

Sub: - Deputing faculty to teach service course for I Year I Sem  
-Reg.

Sir,

The following faculty are deputed from department of  
Electronics and Communication engineering to teach I Year I Sem  
course (BEE) as per JNTUH Curriculum, for the academic year  
2017-18, I Semester.

S No	Name of the Faculty
1	SHAIK BRAHMAIAH
2	R SIRISHA
3	V JYOTHI

*Asuresh*  
Principal,  
SREYAS INSTITUTE OF ENGINEERING & TECH.  
9-38, S.No: 107, Tallampalam (V),  
GSI, Bandlaguda, Nagole, Hyd-68.

*K. Suresh*  
HOD-ECE  
Head of the Department  
Electronics & Communication Engineering  
Sreyas Institute of Engineering & Technology  
Beside Indu Aranya, Tallampalam, Hyderabad-500068



# SREYAS Institute of Engineering & Technology

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## Department of Electronics and Communication Engineering

A.Y 2017-18 I-SEMESTER

Date:

05/07/2017

SIET/BCE/A.Y: 2017-18/I-Sem/Subject Allotment/05

To

The Head of the Department,

Computer Science and Engineering,

S.I.E.

Sub: - Deputing faculty to teach service course for II Year I Sem  
-Reg.

Sir,

The following faculty are deputed from department of  
Electronics and Communication engineering to teach II Year I Sem  
course as per JNTUH curriculum, for the academic year 2017-18, I  
Semester.

S No	Name of the Faculty-SUBJECT
1	P CHAITANYA-DLD

*Asuresh*

PRINCIPAL  
SREYAS INSTITUTE OF ENGINEERING & TECH.  
B-39, Sector: 107, Tolunnaram (V),  
GSI, Manoraguda, Nagole, Hyd-88.

*Pravin*  
Head of the Department  
Electronics & Communication Engineering  
Sreyas Institute of Engineering and Technology  
Beside Indu Aranya, Nagole, Hyderabad-500063



# SREYAS Institute of Engineering & Technology

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC)  
Hyderabad | PIN: 500062

Department of Humanities and Science  
AY 2017-18 I-SEMESTER

Date: 06/07/17

From,  
The Head of the Department,  
Humanities and Science,  
S.I.E.T.

To  
The Head of the Department,  
Electronics and Communication Engineering,  
S.I.E.T.

Sub: - Deputing faculty to handle service course for ECE-Reg.

Sir, with reference to your request letter SIET/ECE/A.Y: 2017-18/I-Sem/Subject Allotment/03 the following faculty are deputed to handle the concerned course as per the curriculum of R16 & R13 regulations of Electronics and Communication engineering framed by JNTUH, for the academic year 2017-18, I Semester.

S No	Year & Sections	Course	
1	II ECE (R16)	Mathematics-IV	Mr B Vidyasagar
			Mr R Santosh
2	II ECE (R16)	Environmental Science and Technology	Mr. MD Sohail Nizamuddin
			Mr. N Naveen Kumar
3	IV ECE (R13)	Management Science	Mr.T.Shiva Kumar
			Mr.P.Sriharsha
4		Advanced English Communication Skills Lab	Ms. Karuna/ Mrs. Sheetal Reddy/ Ms. Anuradha

PRINCIPAL

SREYAS INSTITUTE OF ENGINEERING & TECHNOLOGY  
9-30, Sector 107, T. Nagar, Hyderabad (V), Sreyas Institute of Engineering & Technology  
GGI, Gundlupeta, Ilgolt, Hyd-68, Thattal Annamangal, 172026



# SREYAS Institute of Engineering & Technology

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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING A.Y. 2017-18 SEMESTER-I

Sl. No.	Name of the Staff	Designation	Subject	Teach Semester	No of hours per week/week	Lab	No of Classes	No of hours per week/week	No of Theory hrs. per week	No of Practical hrs. per week	Total Marks per week
1	Mr. S. SURESH	Associate Professor & HOD	WEEEC 150	1	5				18	0	18
2	Mr. S. V. SURESH	Associate Professor & Deputy HOD	WEEEC 200	1	5	WEEEC 150 Lab C&C	2	3	12	0	12
3	Dr. Suresh Chandra	Professor	WEEEC 300	1	5				5	0	5
4	Dr. Suresh Chandra	Professor	WEEEC 400	2	8				12	0	12
5	Dr. Suresh Chandra	Professor	WEEEC 500	1	5				8	0	8
6	Dr. Suresh Chandra	Associate Professor	WEEEC 600	2	5				10	0	10
7	Dr. Suresh Chandra	Associate Professor	WEEEC 700	2	5				10	0	10
8	Mr. S. Suresh Chandra	Associate Professor	WEEEC 800	1	5	WEEEC 150 Lab C&C	2	3	12	0	12
9	Mr. S. Suresh Chandra	Associate Professor	WEEEC 900	2	5	WEEEC 150 Lab C&C	2	3	12	0	12
10	Mr. S. Suresh Chandra	Associate Professor	WEEEC 1000	2	5	WEEEC 150 Lab C&C	2	3	12	0	12
11	Mr. S. Suresh Chandra	Associate Professor	WEEEC 1100	2	5	WEEEC 150 Lab C&C	2	3	12	0	12
12	Mr. S. Suresh Chandra	Associate Professor	WEEEC 1200	2	5	WEEEC 150 Lab C&C	2	3	12	0	12
13	Mr. S. Suresh Chandra	Associate Professor	WEEEC 1300	2	5	WEEEC 150 Lab C&C	2	3	12	0	12
14	Mr. S. Suresh Chandra	Associate Professor	WEEEC 1400	2	5	WEEEC 150 Lab C&C	2	3	12	0	12
15	Mr. S. Suresh Chandra	Associate Professor	WEEEC 1500	2	5	WEEEC 150 Lab C&C	2	3	12	0	12

Asuresh

PRINCIPAL  
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9-39, Sy.No: 107, T. Jannaram (V).  
GSt, Bandlaguda, Nalgonda, Hyd-58

18	Dr. G. V. MOGHAN	Assistant Professor	ELECTIVE A & C	4	5	WIRELESS COMM. A & C	2	3	40	17	22
17	Dr. C. S. RAO (V)	Assistant Professor	WIRELESS C & D	3	5				10	4	10
16	Dr. G. S. RAO (V)	Assistant Professor	WIRELESS A & B	1	5	WIRELESS COMM. A & B	2	3	10	6	16
15	Dr. H. S. RAO (V)	Assistant Professor	WIRELESS C & D	1	5	WIRELESS COMM. C & D	1	1	40	6	16
20	Dr. A. S. RAO (V)	Assistant Professor	WIRELESS C	4	5				2	4	4
21	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS A & B	1	5	WIRELESS COMM. A & B	1	1	10	12	12
22	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS C & D	2	5				10	4	10
23	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS A & B	1	5				10	0	10
24	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS C & D	1	5				10	0	10
25	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS A & B	1	5	WIRELESS COMM. A & B	1	1	10	4	14
26	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS C & D	1	5				10	0	10
27	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS A & B	1	5	WIRELESS COMM. A & B	1	1	10	0	10
28	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS C & D	1	5				10	0	10
29	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS A & B	1	5	WIRELESS COMM. A & B	1	1	10	0	10
30	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS C & D	1	5				10	0	10
31	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS A & B	1	5	WIRELESS COMM. A & B	1	1	10	0	10
32	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS C & D	1	5				10	0	10
33	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS A & B	1	5	WIRELESS COMM. A & B	1	1	10	0	10
34	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS C & D	1	5				10	0	10
35	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS A & B	1	5	WIRELESS COMM. A & B	1	1	10	0	10
36	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS C & D	1	5				10	0	10
37	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS A & B	1	5	WIRELESS COMM. A & B	1	1	10	0	10
38	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS C & D	1	5				10	0	10
39	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS A & B	1	5	WIRELESS COMM. A & B	1	1	10	0	10
40	Dr. S. S. RAO (V)	Assistant Professor	WIRELESS C & D	1	5				10	0	10

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38	M.Sc. Engg.	Assistant Professor							
39	M.Sc. Information Science	Assistant Professor							
40	M.Sc. Information Science	Assistant Professor							
41	M.Sc. Information Science	Assistant Professor							
42	M.Sc. Information Science	Assistant Professor							
43	M.Sc. Information Science	Assistant Professor							
44	M.Sc. Information Science	Assistant Professor							
45	M.Sc. Information Science	Assistant Professor							
46	M.Sc. Information Science	Assistant Professor							
47	M.Sc. Information Science	Assistant Professor							
48	M.Sc. Information Science	Assistant Professor							
49	M.Sc. Information Science	Assistant Professor							
50	M.Sc. Information Science	Assistant Professor							
51	M.Sc. Information Science	Assistant Professor							
52	M.Sc. Information Science	Assistant Professor							
53	M.Sc. Information Science	Assistant Professor							
54	M.Sc. Information Science	Assistant Professor							
55	M.Sc. Information Science	Assistant Professor							

*Asuresh*

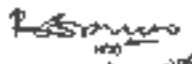
Head of the Department  
 Electronics & Communication Engineering  
 Sreyas Institute of Engineering and Technology  
 Bandlaguda Veerappa, Nagole, Hyderabad 500 080

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**SREYAS Institute of Engineering & Technology**  
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 Hyderabad | PIN: 500082

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

S.NO	Name of the Staff	Designation	Subject	No of Semesters	No of Hours per week/semester	Lab	No of Sections	No of Hours per week/semester	Max. Strength (No. per Week)	No. of Presently Available	Total No. of Staff
<b>VACANCY EXISTING TO M.A.S DEPARTMENT</b>											
1	Mr. S. Srinivas Kumar	Assistant Professor	ICE 18 B BCE	2	4				8	0	2
2	Mr. Y. Venkatesh	Assistant Professor	ICE 18EE A&D	4	4				16		4
3	Mr. B. Srinivas	Assistant Professor	ICE 18EE C&D	2	4				8	0	1

  
 Head of the Department  
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 Bandlaguda, Nayole, Hyderabad-500082

*Asuresh*

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**SREYAS Institute of Engineering & Technology**  
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 Hyderabad | PIN: 500048

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**PG FACULTY - EMBEDDED SYSTEMS**

S.NO	Name of the Staff	Designation	Subject	No of Sections	No of Hours per week/week	Job	No of Sections	No of Hours per session/week	No of Theory hrs per week	No of Practical hrs per week	Total hours load per week
1	Dr. V. SURESH KUMAR	Professor	MCSN CH 4 & 8	2	5				10	0	10
2	Mr. S. PRADHUMITHA	Associate Professor									
3	Mr. D. CHANNA KIRAN	Assistant Professor									
4	Ms. P. SURESH SATHISHA	Assistant Professor									

**PG FACULTY - DIGITAL SYSTEMS AND COMPUTER ELECTRONICS**

S.NO	Name of the Staff	Designation	Subject	No of Sections	No of Hours per session/week	Job	No of Sections	No of Hours per session/week	No of Theory hrs per week	No of Practical hrs per week	Total hours load per week
1	Dr. M. SURESH KUMAR MOOD	Professor	NEZ-CMCA & C	2	5				10	0	10
2	Mr. S. RAJESH	Assistant Professor									
3	Mr. S. VIKRAM	Assistant Professor									
4	Mr. S. SURESH	Assistant Professor									

*Asuresh*

Head of the Department  
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 Bandlaguda, Nagole, Hyderabad-500048

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**SREYAS Institute of Engineering & Technology**  
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 Hyderabad - 500082

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 A.Y 2017-18 SEMESTER-I

S.NO	Name of the Staff	Designation	Subject	No of Semesters	No of Hours per week/sem	Lab	No of Semesters	No of hours per week/sem	No of Theory hrs per week	No of Practical hrs per week	Total hours per week
1	Dr. B. SURESH KUMAR	Associate Professor & HOD	MATHEMATICS	2	5				10	0	10
2	Dr. G. V. VENKATESWARAN	Associate Professor & HOD	PHYSICS	4	5	MATHEMATICS	2	4	12	4	16
3	Dr. M. S. SURESH KUMAR	Professor	MATHEMATICS	2	5				10	0	10
4	Dr. S. S. SURESH KUMAR	Professor	PHYSICS	1	5				5	0	5
5	Dr. S. S. SURESH KUMAR	Professor	MATHEMATICS	2	4				12	0	12
6	Dr. M. S. SURESH KUMAR	Professor	MATHEMATICS	2	5				10	0	10
7	Dr. A. SURESH KUMAR	Associate Professor	PHYSICS	2	5				10	0	10
8	Dr. T. SURESH KUMAR	Associate Professor	PHYSICS	2	5				10	0	10
9	Dr. V. SURESH KUMAR	Associate Professor	MATHEMATICS	2	4	MATHEMATICS	2	6	10	11	21
10	Dr. V. SURESH KUMAR	Associate Professor	PHYSICS	2	5	MATHEMATICS	2	6	10	11	21
11	Dr. S. SURESH KUMAR	Associate Professor	MATHEMATICS	2	5	MATHEMATICS	2	5	10	4	14
12	Dr. S. SURESH KUMAR	Associate Professor	PHYSICS	2	4	PHYSICS	2	5	11	4	15
13	Dr. S. SURESH KUMAR	Associate Professor	PHYSICS	2	5	MATHEMATICS	2	6	11	11	22
14	Dr. P. SURESH KUMAR	Associate Professor	MATHEMATICS	2	5				10	0	10
15	Dr. P. SURESH KUMAR	Associate Professor	MATHEMATICS	2	5				10	0	10

*Asuresh*

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 9-39, Sy.No: 107, Tenali-Charam (V),  
 GSI, Basijaguda, Nellore, Ndy-68.



34	Ms D VEDU	Assistant Professor							
39	Ms C VIKASHA REDDY	Asst. Prof. Lecturer							
40	Ms S. Prashanthi	Assistant Professor							
41	Ms S. Lakshmi	Assistant Professor							
42	Ms S. Lakshmi	Assistant Professor							
43	Ms S. Lakshmi	Assistant Professor							
44	Ms S. Lakshmi	Assistant Professor							
45	Ms S. Lakshmi	Assistant Professor							
46	Ms S. Lakshmi	Assistant Professor							
47	Ms S. Lakshmi	Assistant Professor							
48	Ms S. Lakshmi	Assistant Professor							
49	Ms S. Lakshmi	Assistant Professor							
50	Ms S. Lakshmi	Assistant Professor							
51	Ms S. Lakshmi	Assistant Professor							
52	Ms S. Lakshmi	Assistant Professor							
53	Ms S. Lakshmi	Assistant Professor							
54	Ms S. Lakshmi	Assistant Professor							
55	Ms S. Lakshmi	Assistant Professor							
56	Ms S. Lakshmi	Assistant Professor							
57	Ms S. Lakshmi	Assistant Professor							
58	Ms S. Lakshmi	Assistant Professor							
59	Ms S. Lakshmi	Assistant Professor							
60	Ms S. Lakshmi	Assistant Professor							

**Asuresh**  
 Head of the Department  
 Electronics & Communication Engineering  
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 Bachelors, JNTU Anaparthi, Warangal, Hyderabad - 506 002

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**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

# **COURSE FILE**

**IV B. TECH I SEMESTER R15**

## **EMBEDDED SYSTEM DESIGN**

**VERSION – 1.0**



**2018 - 2019**

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**SREYAS INSTITUTE OF ENG. & TECH.**  
9-99, Sy.No. 107, Telcheru, ram (V),  
GSI, Bandlaguda, Nagole, Hyd-68.

**Prepared by: 1. Ch. S. V. Maruthi Rao**  
**2. K. Sonali Swaroop**

**Reviewed by:**  
( )

**Approved by:**  
**(IQAC)**

**Head of the Department - ECE**  
**B. Sreenivasu**



## Sreyas Institute of Engineering & Technology

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### INDEX

#### COURSE FILE

A.Y	: 2018 - 2019	Subject Name	: Embedded System Design
Class	: IV ECE	Semester	: I
Faculty Name	: 1. Ch. S. V. Maruthi Rao		2. K. Sonali Swaroop

S No.	Name of the Item	Page No.
1	Institute Vision and Mission., Dept. Vision and Mission, PEOs, POs and PSOs	
2	Academic Calendar	
3	JNTU Course Syllabus	
4	Text books and Reference Books	
5	Course objectives	
6	Course Outcomes	
7	COs mapping to POs	
8	Topics Beyond Syllabus, if any	
9	Plan for delivery of TBS	
10	Web references for self learning	
11	Preparation of course Outcome Assessment form	
12	Lesson Schedule	
13	Lesson Planner	
14	Lecture notes	
15	OHP/Power point presentations/ Web references	
16	University question papers	
17	Internal question paper with Taxonomy level key	
18	Assignments question paper with Taxonomy level	
19	Tutorial evidence	
20	Result Analysis, Remedial/Corrective Action	
21	Guest talks, field visits etc.,	
22	Add-on program	

*Agurush*

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## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

### ANNEXURE - I

#### VISION OF THE DEPARTMENT

*Striving for global recognition for excellence in Electronics and Communication Engineering Education, research and development, for the advancement of the society.*

#### MISSION OF THE DEPARTMENT

- *Providing quality education to the students and enhancing their skills to make them globally competent Electronics and Communication Engineers.*
- *Providing state of the art laboratory and research facilities with opportunities to innovate, design, develop and implement new systems and processes.*
- *Interact with different industries to integrate R&D activities along with academic learning such that applying engineering solutions in response to the change requirement needs of the society and industry.*

#### PROGRAM EDUCATIONAL OBJECTIVES

- *Graduates shall apply the fundamental, advanced and contemporary knowledge of Electronics, Communication and allied Engineering, to develop efficient solutions and systems, to meet the needs of the industries and society.*
- *Graduates will get employed or pursue higher studies or 0 research.*
- *Graduates will have team spirit, good communication skills and ethics with lifelong learning attitude.*

*Asuresh*

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## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

### ANNEXURE – II

#### (A) PROGRAM OUTCOMES

Engineering Graduates will be able to:

1. **Engineering Knowledge:** Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
2. **Problem analysis:** Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
3. **Design/development of solutions:** Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
4. **Conduct investigations of complex problems:** Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
5. **Modern tool usage:** Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.
6. **The engineer and society:** Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
7. **Environment and sustainability:** Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
8. **Ethics:** Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
9. **Individual and team work:** Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.

*Aswath*  
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- 10. **Communication:** Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
- 11. **Project management and finance:** Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
- 12. **Life-long learning:** Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

**(B) PROGRAM SPECIFIC OUTCOMES (PSOs)**

- 13. Proficiency on the contemporary skills towards development of innovative electronics and communication systems and solutions.
- 14. Capabilities to participate in the construction of large and complex electronics and/or communication systems of varying complexity.



*Asuresh*

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## ACADEMIC CALENDER

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD  
ACADEMIC CALENDAR (2018-19)  
FOR NON-AUTONOMOUS CONSTITUENT & AFFILIATED COLLEGES  
B. TECH. II, III & IV YEARS I & II SEMESTERS**

**I SEM**

S. No	EVENT	DATE	Duration
1.	Commencement of Instruction	9 <sup>th</sup> July 2018	--
2.	First Mid Term Examinations	4 <sup>th</sup> to 6 <sup>th</sup> Sept. 2018	--
3.	Submission of First Mid Term Exam Marks to University on or before	15 <sup>th</sup> Sept. 2018	--
4.	Parent-Teacher Meeting	13 <sup>th</sup> Oct. 2018	--
5.	Dissemination notes	15 <sup>th</sup> to 20 <sup>th</sup> Oct. 2018	1 week
6.	Last date of Instruction	10 <sup>th</sup> Nov. 2018	16 weeks
7.	Second Mid Term Examinations	12 <sup>th</sup> to 14 <sup>th</sup> Nov. 2018	--
8.	Preparation Holidays and Practical Examinations	15 <sup>th</sup> to 24 <sup>th</sup> Nov. 2018	1 week
9.	Submission of Second Mid Term Exam Marks to University on or before	24 <sup>th</sup> Nov. 2018	--
10.	End Semester / Supplementary Examinations	26 <sup>th</sup> Nov. to 3 <sup>rd</sup> Dec. 2018	3 weeks
11.	Semester Break	10 <sup>th</sup> to 15 <sup>th</sup> Dec. 2018	1 week

**II SEM**

S. No	EVENT	DATE	Duration
1.	Commencement of Instruction	17 <sup>th</sup> Dec. 2018	--
2.	First Mid Term Examinations	11 <sup>th</sup> to 23 <sup>rd</sup> Feb. 2019	--
3.	Submission of First Mid Term Exam Marks to University on or before	20 <sup>th</sup> Feb. 2019	--
4.	Parent-Teacher Meeting	9 <sup>th</sup> March 2019	--
5.	Last date of Instruction	13 <sup>th</sup> April 2019	16 weeks
6.	Second Mid Term Examinations	15 <sup>th</sup> to 17 <sup>th</sup> April 2019	--
7.	Preparation Holidays and Practical Examinations	18 <sup>th</sup> to 27 <sup>th</sup> April 2019	1 week
8.	Submission of Second Mid Term Exam Marks to University on or before	25 <sup>th</sup> April 2019	--
9.	End Semester / Supplementary Examinations	29 <sup>th</sup> April to 11 <sup>th</sup> May 2019	3 weeks
10.	Summer Vacation	11 <sup>th</sup> May to 6 <sup>th</sup> July 2019	8 weeks

*Subhasini*  
DIRECTOR

ACADEMIC & PLANNING, JNTU

*Asuresh*

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## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

L T/P/D C

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### (114AF) DIGITAL DESIGN USING VERILOG HDL

#### UNIT-I

**Introduction to Embedded Systems:** Definition of Embedded System, Embedded Systems Vs General Computing Systems, History of Embedded Systems, Classification, Major Application Areas, Purpose of Embedded Systems, Characteristics and Quality Attributes of Embedded Systems.

#### UNIT-II

**Typical Embedded System: Core of the Embedded System:** General Purpose and Domain Specific Processors, ASICs, PLDs, Commercial Off-The-Shelf Components (COTS), Memory: ROM, RAM, Memory according to the type of Interface, Memory Shadowing, Memory selection for Embedded Systems, Sensors and Actuators, Communication Interface: Onboard and External Communication Interfaces.

#### UNIT-III

**Embedded Firmware:** Reset Circuit, Brown-out Protection Circuit, Oscillator Unit, Real Time Clock, Watchdog Timer, Embedded Firmware Design Approaches and Development Languages.

#### UNIT-IV

**RTOS Based Embedded System Design:** Operating System Basics, Types of Operating Systems, Tasks, Process and Threads, Multiprocessing and Multitasking, Task Scheduling.

#### UNIT-V

**Task Communication:** Shared Memory, Message Passing, Remote Procedure Call and Sockets, Task Synchronization: Task Communication Synchronization Issues, Task Synchronization Techniques, Device Drivers, How to Choose an RTOS.

*Asured*

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9-39, Sy.No: 107, Tattlannaram (V),  
GST, Bandlaguda, Nagole Hyd-68.

**TEXT BOOKS:**

1. Introduction to Embedded Systems - Shibu K.V, Mc Graw Hill.

**REFERENCE BOOKS:**

1. Embedded Systems - Raj Kamal, TMH.
2. Embedded System Design - Frank Vahid, Tony Givargis, John Wiley.
3. Embedded Systems – Lyla, Pearson. 2013
4. An Embedded Software Primer - David E. Simon, Pearson Education.



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## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

### COURSE OBJECTIVES

Name of the Course	: B.Tech	Academic Year	: 2016-17
Batch	: 2015-2019	Regulation	: R-15
Class	: IV Year	Branch	: ECE
Semester	: I	Nature of Subject	: CORE
Title of the Subject	: ESD	Subject Code	: A70440

Unique Code	Course Objectives
1.	Define an embedded system and differentiate with a general purpose system. Classify the embedded systems and its application areas
2.	Learn about the various components that make the core of the embedded system
3.	Discuss the various functional units used in the hardware of an embedded system and understand the various embedded firmware approaches and the programming languages used
4.	To Understand the basic operating system concepts and various components of an RTOS
5.	Learn the various task communication techniques used in an RTOS

Faculty Signature

*Asuresh*

Head of the Department - ECE  
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Counselling  
Code  
SRYs

## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

### COURSE OUTCOMES

Name of the Course	: B.Tech	Academic Year	: 2018-19
Batch	: 2015-2019	Regulation	: R-15
Class	: IV Year	Branch	: ECE
Semester	: I	Nature of Subject	: CORE
Title of the Subject	: ESD	Subject Code	: A70440

Unique Code	Course Outcomes	Explanation for COs
ESD.CO1	Understand basic concepts and application areas of embedded system while learning its purpose and characteristics and differentiate with a general purpose system	Define & differentiate, classify and applications of embedded systems. understand the characteristics and purpose of embedded systems
ESD.CO2	Understand the various components / peripherals used to build the core of an embedded system	Learn about the various components which make the core of an embedded system
ESD.CO3	Understand and analyze the embedded firmware design approaches and development languages	Discuss about the various protection circuits used in embedded systems and learn about the various design approaches
ESD.CO4	Learn the basics of an operating system and Real-Time operating systems	Learn the basics of an RTOS. Define a task and its various states and types.
ESD.CO5	Understand the various task communication techniques used in the RTOS	Understand the various task communication techniques like RPC, sockets, shared memory and techniques to synchronize the

*Aswathy*

Faculty Signature

PRINCIPAL  
SREYAS INSTITUTE OF ENGG.&TECH.  
9-39, Sy No: 15, Juthigandhi (II),  
GSI, Bandlaguda, Nagole, Hyd-500089

Department of Electronics & Communication Engineering

Ch. S. V. Maruthi Rao, K. Sonali Swaroop



# Sreyas Institute of Engineering & Technology

( Approved by AICTE, New Delhi | Affiliated to JNTU, Hyderabad)

Campus Address : Beside Jada Amma, GSI, Bandlaguda, Nagole, Hyderabad, 500088 | Phone : 040-4591 4455



## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

### COURSE OUTCOMES- PROGRAM OUTCOMES MAPPING MATRIX

Name of the Course	: B.Tech	Academic Year	: 2018-19
Batch	: 2015-2019	Regulation	: R-15
Class	: IV Year	Branch	: ECE
Semester	: I	Nature of Subject	: CORE
Title of the Subject	: ESD	Subject Code	: A70440

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
ESD.CO1	3	2	-	1	-	2	-	-	-	-	-	-
ESD.CO2	3	3	1	-	3	2	1	-	-	-	-	-
ESD.CO3	-	3	3	3	2	-	-	2	1	-	1	-
ESD.CO4	3	-	-	-	3	-	-	-	-	1	-	-
ESD.CO5	2	2	1	-	3	3	-	-	-	-	-	1

Note:

Enter Correlation Levels as mentioned below:

1: Slight(Low)

2: Moderate (Medium)

3: Substantial (High)

If there is no correlation put “-”

Faculty Signature

Department of Electronics & Communication Engineering

*Asuresh*

**PRINCIPAL**  
SREYAS INSTITUTE OF ENGG.&TECH.  
9-39, Sy.No: 100, Tattannaram (V),  
GSI, Bandlaguda, Nagole, Hyd-50

**Head of the Department - ECE**

Ch. S. V. Manaji Rao, K. Sonali Swaroop



# Sreyas Institute of Engineering & Technology

(Approved by AICTE, New Delhi | Affiliated to JNTU, Hyderabad)

Campus Address : Beside Indo Amity, GSI, Bandlaguda, Nagole, Hyderabad, 500068 | Phone : 940-4381 4415



## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

### COURSE OUTCOMES- PROGRAM SPECIFIC OUTCOMES MAPPING MATRIX

Name of the Course	: B.Tech	Academic Year	: 2018-19
Batch	: 2015-2019	Regulation	: R-15
Class	: IV Year	Branch	: ECE
Semester	: I	Nature of Subject	: CORE
Title of the Subject	: ESD	Subject Code	: A70440

CO	PSO1	PSO2
ESD.CO1	3	1
ESD.CO2	3	2
ESD.CO3	3	3
ESD.CO4	3	1
ESD.CO5	3	2

Note:

Enter Correlation Levels as mentioned below

1: Slight(Low)

2: Moderate (Medium)

3: Substantial (High)

If there is no correlation put "\_\_\_"

Faculty Signature

*Asuresh*

PRINCIPAL  
SREYAS INSTITUTE OF ENGG.&TECH.  
5-39, Sy.No: 107, Tattaiyannaram (V),  
GSI, Bandlaguda, Nagole, Hyd-500068  
Head of the Department - ECE



# Sreyas Institute of Engineering & Technology

(Approved by AICTE, New Delhi | Affiliated to JNTU, Hyderabad)

Campus Address: Beside Indus Aaraya, GST, Bandlaguda, Nagola, Hyderabad, 500068 | Phone: 040-6583 4415



## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

### II ECE - A

DAY	PERIODS								
	I	II	B R	III	IV	L U	V	VI	VII
	09:00 To 10:00	10:00 To 10:50		11:00 To 11:50	11:50 To 12:40		01:20 To 02:10	02:10 To 03:00	03:00 To 03:50
MON									
TUE			E			N	ESD		
WED		ESD		ESD					
THU			A			C			
FRI							ESD	ESD	
SAT			K			H		ESD	

### II ECE - D

DAY	PERIODS								
	I	II	B R	III	IV	L U	V	VI	VII
	09:00 To 10:00	10:00 To 10:50		11:00 To 11:50	11:50 To 12:40		01:20 To 02:10	02:10 To 03:00	03:00 To 03:50
MON					ESD				
TUE			E		ESD	N			
WED					ESD				
THU			A			C			
FRI	ESD						<i>A. Sureth</i>		
SAT	ESD		K		ESD	H	<b>PRINCIPAL</b>		



# Sreyas Institute of Engineering & Technology

(Approved by AICTE, New Delhi | Affiliated to JNTU, Hyderabad)

Corporate Address: Beside Indu Aaraya, GSI, Bandlaguda, Nagole, Hyderabad, 500068 | Phone: 040-6501 4415



## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

### COURSE SCHEDULE

Name of the Course	: B.Tech	Academic Year	: 2018-19
Batch	: 2015-2019	Regulation	: R-15
Class	: IV Year	Branch	: ECE
Semester	: I	Nature of Subject	: CORE
Title of the Subject	: ESD	Subject Code	: A70440

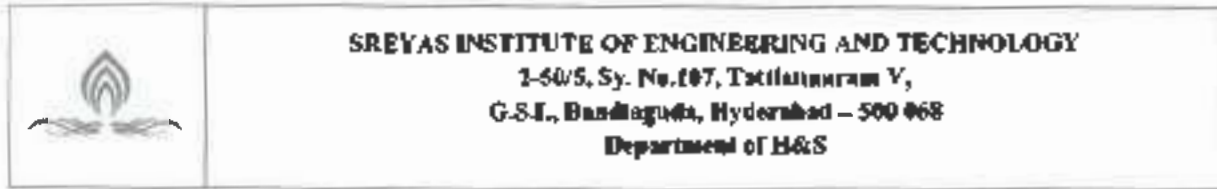
Expected date of completion of the Course:

S. No	Unit No	Title	Duration (Dates)		Total No. Periods
			From	To	
1	I	Introduction to Embedded Systems	10-07-18	20-7-18	10
2	II	Typical Embedded System	21-07-18	03-08-18	12
3	III	Embedded Firmware	04-08-18	17-08-18	9
4	IV	RTOS Based Embedded System Design	18-08-18	01-09-18	9
5	V	Task Communication	11-09-18	21-09-18	10
<b>Minimum Number of Classes Required</b>					<b>51</b>

Faculty Signature

*Asuresh*  
 PRINCIPAL  
 Head of the Department, ECE  
 9-39, Sy.No: 157, Tattannaram (H)  
 GSI, Bandlaguda, Nagole, Hyd-6

### 2.3.3 year wise list of number of students assigned to each mentor



No: 2019/DAC/M9

Date: 08.06.2019

#### CIRCULAR

This is to bring to your kind notice that the following members were allotted as mentors for the ACY 2019-20.


Mentoring In-charge

HOD

Circulate to,  
All staff of H&S.

CC to,  
Department.

  
SREYAS INSTITUTE OF ENGINEERING AND TECHNOLOGY  
G.S.I., Bandlaguda, Nagole, Hyderabad-500068

  
**PRINCIPAL**  
SREYAS INSTITUTE OF ENGG.&TECH.  
9-39, Sy.No: 107, Tattaiannaram V,  
GSI, Bandlaguda, Nagole, Hyderabad-500068



## SREYAS INSTITUTE OF ENGINEERING AND TECHNOLOGY

2-50/5, Sy. No.107, Tattianmaram V,

G.S.I., Bandlaguda, Hyderabad - 500 068

No: 2018/DAC/M8

Date: 03.07.2018

### CIRCULAR

This is to inform all the faculty that mentoring allocation is made for the academic year 2018-19 SEM-I as follows. For any further clarification, mentor can meet Mentoring in-charge Mrs. Mrs.R.Swathi/HOD-H&S.

SECTION	MENTOR NAME	ROLL NUMBERS	Total no. of Students Allotted
ICSE-A	NASEERUDDIN	18VE1A0501-18VE1A0520	20
	SIDDHANTH	18VE1A0521-18VE1A0540	20
	MANJUNATH/ S SUDHAKAR REDDY	18VE1A0541-18VE1A0560	20
ICSE-B	KAVITHA	18VE1A0561-18VE1A0580	20
	SUBRAMANYAM	18VE1A0581-18VE1A0600	20
	SOHAIL NEAMUDDIN	18VE1A0601-18VE1A0620	20
ICSE-C	C ANIL KUMAR	18VE1A0621-18VE1A0640	20
	BAMALAH	18VE1A0641-18VE1A0660	20
	J SANDEEP/ SANTHOSH	18VE1A0661-18VE1A0680	20
ICSE-D	P BANU	18VE1A0681-18VE1A0700	20
	RAHUL	18VE1A0701-18VE1A0720	20
	K SHIVARAM REDDY	18VE1A0721-18VE1A0740	20
IECE-A	VENKATAH	18VE1A0401-18VE1A0420	20
	RAMYADEEPIKA	18VE1A0421-18VE1A0440	20
	S RAMESH	18VE1A0441-18VE1A0460	20
IECE-B	C YASAVI	18VE1A0461-18VE1A0480	20
	S SUDHAKAR REDDY	18VE1A0481-18VE1A0500	20
	B RAMAKRISHNA	18VE1A0501-18VE1A0520	20
IECE-C	VIDYA SAGAR	18VE1A0421-18VE1A0440	20
	KIRAN KUMAR	18VE1A0441-18VE1A0460	20
	P RAJU	18VE1A0461-18VE1A0480	20
IECE-D	SUSHMA	18VE1A0481-18VE1A0500	20
	KA/ASHEKHAR/ Hythavathi	18VE1A0501-18VE1A0520	20
	S SRIKANTH	18VE1A0521-18VE1A0540	20
IMECH-A	A PRABHAKAR REDDY	18VE1A0301-18VE1A0320	20
	RAMAKRISHNA	18VE1A0321-18VE1A0340	20
IMECH-B	HARIPRIYA	18VE1A0341-18VE1A0360	20
	SHIVAKANTH/ YOSHNA	18VE1A0361-18VE1A0380	20
ICIVIL	R SWATHI	18VE1A0101, 18VE1A0120- 18VE1A0120, 17VE1A0140	20
	SHIKANTH REDDY	18VE1A0121, 17VE1A0140- 18VE1A0140, 17CE1A0140	20
	CELESTINA	18VE1A0141-18VE1A0160	20

Mentoring In-charge

H&S- HOD

*Asuresh*  
PRINCIPAL  
SREYAS INSTITUTE OF ENGG.&TECH.  
9-39, Sy.No: 107, Tattianmaram (V),  
GSI, Bandlaguda, Hyderabad, Hyd-500068

*Asuresh*  
SREYAS INSTITUTE OF ENGG. & TECH.  
D.A. 2018/07/03/18  
Bandlaguda, Tattianmaram, Hyderabad-500068



Circulated to,  
All staff of H&S



## SREYAS INSTITUTE OF ENGINEERING AND TECHNOLOGY

2-50/5, Sy. No.107, Tattiambaram V,

G.S.I., Bandlaguda, Hyderabad – 500 068

No: 2018/DAC/M8

Date: 02.01.2019

### CIRCULAR

This is to inform the faculty that mentoring allocation is made for the academic year 2018-19 sem-II as follows. For any further clarification, mentor can meet Mentoring in-charge Mrs. Mrs.R.Swathi/HOD-H&S.

BRANCH	Mentor Name	Roll Numbers	Total no. of Students Allotted
I CSE-A	Mr.O.Anil kumar	18VE1A0501-18VE1A0524	24
	Mr.G.Anil	18VE1A0525-18VE1A0547, 17VE1A05E1	24
I CSE-B	Mr.Abhilash	18VE1A0548-18VE1A0571	24
	Mrs.Navatha	18VE1A0572-18VE1A0595	24
I CSE-C	Mr.Phanindra	18VE1A0596-18VE1A0589	24
	Mrs.Anusha	18VE1A05C0-18VE1A05E4	24
I CSE-D	Mr.A.Prabhakar Reddy	18VE1A05E5-18VE1A05G8	24
	Mr.K.Shivaram Reddy	18VE1A05G9-18VE1A05K2	24
I CSE-E	Mrs. R.Swathi	18VE1A05K3-18VE1A05Q0	48
I ECE-A	Mrs.G.Kavitha	18VE1A0401-18VE1A0449	48
I ECE-B	Mrs.C.Vasavi Rao	18VE1A0450-18VE1A0497	48
I ECE-C	Mr.S.Subramanyam	18VE1A0498-18VE1A04C1	24
	Mr.G.Siddhanth	18VE1A04C2-18VE1A04E5	24
I ECE-D	Mr.B.Vidya Sagar	18VE1A04E6-18VE1A04K2	24
I ECE-E	Mr.Brahmaiah Shaik	18VE1A04K3-18VE1A04P9	24
I MECH-A	Ms.Jyoshna	18VE1A0301-18VE1A0342, 17VE1A0382	42
I MECH-B	Mr.Sohail Nizamuddin	18VE1A0343-18VE1A03E5	42
CIVIL	Mrs.G.Chrestina	18VE1A0101-18VE1A0132	32
	Mr.B.Rahul	18VE1A0133- 18VE1A0159, 17VE1A0140 17VE1A0150,18VE1A0115,18VE1A0129,	33

Mentoring In-charge

H&S- IOD

*Asuresh*  
PRINCIPAL  
SREYAS INSTITUTE OF ENGINEERING & TECH.  
9-39, Sy.No: 107, Tattiambaram (V),  
G.S.I., Bandlaguda, Nagole, Hyd-68.  
Bandlaguda, Tattiambaram, Hyderabad-68

Type of Mentoring / Counseling	Sample Summary			
	Student Name	Roll No	Efficacy achieved in	Improvement of Mentoring System
Co-Curricular Activities	Somala Parthasarathi Reddy	16VE1A04A6	Technical Development	Mentor Ms. P Teja encouraged the student to learn software developing.
	GVS Eshwar	16VE1A0414	Leadership qualities	Mentor Mr. N Ramesh counseled on leadership qualities, as result he successfully continued as Class Representative.
	SHRK Akhil	15VE1A04B0	Personality Development	He has best communication skills. Mentor Mrs. V Swathi encouraged him to participate in SMUN events; as a result, he became the secretary of TEDEX at SMUNI, SIET.
Professional guidance	B Sai Anirudh	15VE5A0467	Paper Presentations	Inspired by Mentor's Dr. Sandeep Kumar words, student presented in an IEEE conference on "A Comparative study on Paper Face spoofing Attacks"
Academic Guidance /	K.Vamshi	14VE1A04116	Improvement in studies	As the result of mentoring by Ms. Sonali Swaroop, he cleared his all backlogs in his engineering.
		14VE5A0467	Usage of attending	As per interest of the student

Asureth

PRINCIPAL  
SREYAS INSTITUTE OF ENGG & TECH  
9-39, Sy.No: 107, Tattannaram (V)  
GSI, Bandlaguda, Nagole, Hyd-50

*[Signature]*  
Principal  
Sreyas Institute of Engineering & Technology  
Bandlaguda, Nagole, Hyderabad-500082

Career Guidance	Allsagar Hussain		Workshops.	in Embedded systems suggested him to present paper on "Various Health Parameters Monitoring using PPG sensor".
All-round development	Uppu Aravind	16VE1A04A9	Regularity in attendance	Mentor Ms. P Teja spoke to student as well as to parents, and guided him to come out from the fears on studies. As a result, he improved his attendance from 55% to 75% and decreased his backlogs.
	Musku Shivan	16VE1A04K7	Importance of CRT classes	By mentor suggestions she attended career training classes
	Rahul	14VE1A04D2	Choosing career	Student was interested in technical field, Mentor motivated and encouraged him.

### Summary of mentoring system :

Parameters	Description
Type of Mentoring System	Professional Guidance/ Academic Guidance/ Laboratory Specific/ Personal Development/ All Round Development
Number of Students assigned/Mentor	15-20
Frequency of Meeting	Once in a week during mentoring hour/ whenever required

*Asuresh*  
**PRINCIPAL**  
 GREYAS INST. OF ENGG.&TECH  
 9-39, Sy.No: 1, Tattannaram (V)  
 GI, Bandlaguda, Nagole, Hyd-50

*Asuresh*  
**PRINCIPAL**  
 GREYAS INST. OF ENGG.&TECH  
 9-39, Sy.No: 1, Tattannaram (V)  
 GI, Bandlaguda, Nagole, Hyd-50



# SREYAS INSTITUTE OF ENGINEERING & TECHNOLOGY

Approved by AICTE, New Delhi & Affiliated to JNTU  
Thota, Jammour, Bandlaguda, Nagole, Hyderabad - 500088



Ref. No: CSE/ DAC /2018-19/FA1

Di: 04.09.2018

## REPORT ON FEEDBACK ANALYSIS SEM-I 2018-19

Sl. No	Name	Year & Sem	Feedback %	Reward/ Corrective Measures
1.	K.Navatha	II-I	4.7	Reward- Appreciation Letter
2.	Shilpa Rani	II-I	4.7	Reward- Appreciation Letter
3.	A.Swathi	II-I	4.6	Reward- Appreciation Letter
4.	K.Varalakshmi	IV-I	4.7	Reward- Appreciation Letter
5.	R.Satheesh	IV-I	2.3	Corrective Action

Signature of DAC Coordinator

SREYAS INSTITUTE OF ENGINEERING & TECHNOLOGY  
Jesu's Indu Aranya, Nagole, Hyd-068

Asuresh

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SREYAS INSTITUTE OF ENGINEERING & TECHNOLOGY  
9-39, Sy.No: 107, Battlannaram (V),  
GSI, Bandlaguda, Nagole, Hyd-66.

Asuresh  
PRINCIPAL

SREYAS INSTITUTE OF ENGINEERING & TECHNOLOGY  
Jesu's Indu Aranya, Nagole, Hyd-068



**FACULTY PERFORMANCE EVALUATION SHEET**

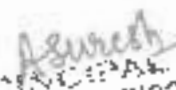
Branch & Semester : B.Tech 2/4 Semester-IE, Computer Science Engineering, A  
 Academic Year : 2019 - 2020

Name of the Staff	Subject Taught	1	2	3	4	5	6	7	8	Overall Percentage	Result
		Does the teacher come prepared to class and explain properly?	Does the teacher speak with the clear voice and effective body language?	Is the teacher able to keep the class under discipline and control?	Does the teacher command students' attention and answer all doubts?	Is the teacher regular and punctual?	Do you find the teacher impartial and honest in paper valuation and personal remark making?	Do you find the teacher keeping in the class as well as outside?	Overall rating of the teacher		
1	S.PALLAVI	Computer Organization	84	87	88	88	84	79	70	86	Average
2	ANIL KUMAR	Database Management Systems	79	84	81	80	80	80	68	80	Good
3	PADMA JOSHI	Operating Systems	79	82	86	83	86	87	76	81	Good
4	RAJATOL K.	Formal Languages And Automata Theory	84	84	81	84	84	87	78	83	Good
5	POORNIMATHI	Business Economics And Financial Analysis	88	89	86	87	72	86	77	83	Good
6	S.PALLAVI	Computer Organization Lab	67	67	67	68	86	78	67	67	Average
7	CHIDHDEVAN MURUK	Computer Organization Lab	65	63	64	65	83	79	62	66	Average
8	ANIL KUMAR	Database Management Systems Lab	85	86	85	88	80	86	75	83	Good
9	PADMA JOSHI	Operating Systems Lab	83	82	82	83	86	88	77	82	Good
10	SAHAGRETA PRIMA	Senior Scripting Lab	89	81	88	91	88	82	81	86	Good
11	A. SRINIVAS	OOP Coding	87	86	80	84	86	87	73	86	Good

\*Excellent (4) : >=90%      \*Good (3) : >=78 & <90%  
 \*Average (2) : >=60 & <78%      \*Poor (1) : Below 60%

Formula: Total Obtained Points / (No. Forms) (Coefficient \* 4) \* No. Of Students \* No. Of Questions

*Asuresh*

  
 PRINCIPAL  
 SREYAS INSTITUTE OF ENGG. & TECH  
 5-39, Sy. No. 157, Taltunnaram (V),  
 GSI, Bandlaguda, Nagole, Hyd-68

Sample Feedback Form

Charge Feedback

**FEEDBACK**

Faculty: \_\_\_\_\_ Dept: \_\_\_\_\_ Section: \_\_\_\_\_

1. Does the teacher follow syllabus in class and explain properly?	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2. Does the teacher starts with the class with interesting and effective way?	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3. Is the teacher able to take the class with discipline and control?	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4. Does the teacher encourage students' attention and inspire all students?	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5. Is the teacher well-prepared?	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6. Do you find the teacher interested and spend to solve students' and answer students' queries?	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7. Do you find the teacher keeping in the class as well as outside?	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8. General rating of the teacher	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

1. Conduct of examinations	<input type="checkbox"/>
2. College library	<input type="checkbox"/>
3. Office	<input type="checkbox"/>
4. Classes/Hours	<input type="checkbox"/>
5. Laboratories	<input type="checkbox"/>
6. Transport	<input type="checkbox"/>
7. Openness (Department matter)	<input type="checkbox"/>
8. Any other	<input type="checkbox"/>

*Asuresh*

PRINCIPAL  
 SREYAS INSTITUTE OF ENGINEERING & TECHNOLOGY  
 9-39, Sy.No. 107, Vallabhannaram (V)  
 GSI, Bandlaguda, Nagole, Hyd-68

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 9-39, Sy. No. 107, Vallabhannaram (V)  
 GSI, Bandlaguda, Nagole, Hyd 68



# SREYAS INSTITUTE OF ENGINEERING & TECHNOLOGY

Approved by: AKT, New Delhi & affiliated to JNTU  
Hyderabad, K.J. Somaiya, Vignana, Vignana, JNTU, Hyderabad, India

## SUMMARY REPORT ON STUDENT FEEDBACK ON FACULTY FOR THE ACADEMIC YEAR 2015-16

Department: ESE

Year & Semester: 2-2

Feedback is collected based on the following points

- Syllabus Completion: AS per timetable or not
- Over all faculty presentation in the class room: Audio-visual, Board presentation, Body Language, Class Control
- Clarity on concepts
- Teaching technique: Assignments, PPTs, lecture Videos etc.
- Punctuality & Regularity

SUBJECTS	SEC: A	COMMENTS	SEC: B	COMMENTS	SEC: C	COMMENTS	SEC: D	COMMENTS
MATHEMATICS	MAR.K.SANJOSH	Good	MAR.K.SANJOSH	Good	MRS.MEELA KREITHA	Good	MRS.MEELA KREITHA	Good
D5 THROUGH D4	DR.D.V.S.SUBB RAO	Good	DR.VENKATA REDDY	Good	MRS.ARCHANA KANI	Good	MRS.ARCHANA KANI	Good
MPCS	MRS.M.DEEPIKA	Good	MRS.DEEPIKA	Good	MRS.SHILPA KANI	Good	MRS.SHILPA KANI	Good
DIGITAL LOGICAL DESIGN	MIRLA.VETURABADU	Good	MIRLA.VETURABADU	Good	MRS.VARSHA AGARWAL	Good	MRS.VARSHA AGARWAL	Good
DOORS THROUGH AAVA	MRS.VARALAKSHMI	Good	MRS.VARALAKSHMI	Good	MRS.A.SWATHI	Very Good	MRS.A.SWATHI	Very Good
D5 THROUGH C-4 LAB	DR.D.V.S.SUBB RAO	Good	DR.VENKATA REDDY	Good	MRS.ARCHANA KANI	Good	MRS.ARCHANA KANI	Good
IT WORKSHOP ENVIRONMENT SCIENCE	MAR.AZHAR MITHAL ANOOSHAC SETHI	Good	MAR.AZHAR MITHAL ANOOSHAC SETHI	Good	MRS.A.SWATHI	Good	MRS.A.SWATHI	Good
	MAR.AZHAR MITHAL ANOOSHAC SETHI	Good	MAR.AZHAR MITHAL ANOOSHAC SETHI	Good	MRS.A.SWATHI	Good	MRS.A.SWATHI	Good
	MRS.VARALAKSHMI	Good	MRS.VARALAKSHMI	Good	MRS.A.SWATHI	Good	MRS.A.SWATHI	Good
	MRS.VARALAKSHMI	Good	MRS.VARALAKSHMI	Good	MRS.A.SWATHI	Good	MRS.A.SWATHI	Good

Supportive to Faculty in Subject related Real applications.

Indicate to concern faculty, i.e. attend FDP, overall as subject expert in the Department, etc., Deliverables & service faculty members

Approved

Principal, Sreyas Institute of Engineering & Technology, Hyderabad

Principal, Sreyas Institute of Engineering & Technology



# Sreyas Institute of Engineering & Technology

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Campus Address: Beside Tech Arena, GSK, Bandlaguda, Nellore, Hyderabad, 500068 | phone : 0840-65814317



### Subject Wise FeedBack Reports

A Year: 2016-17  
 Course: B.Tech  
 Dept: CSE  
 Sem: II

GO

#### CSE I A (B.Tech - 2016-17)

EE Lab	DS Lab	PS	EDC	MFC8	DS	BEE	DLD	CRT									
4.25	65.34	3.77	76.38	4.78	95.78	4.35	88.96	4.28	85.06	3.88	77.29	4.28	85.87	4.25	85.51	3.72	74.34

#### CSE I B (B.Tech - 2016-17)

EE Lab	DS Lab	PS	EDC	MFC8	DS	BEE	DLD	CRT									
4.32	86.44	4.13	82.83	4.84	96.81	4.43	88.89	3.76	75.28	4.25	85.03	4.57	91.40	4.25	84.86	3.85	78.95

#### CSE I C (B.Tech - 2016-17)

EE Lab	DS Lab	PS	EDC	MFC8	DS	BEE	DLD	CRT									
4.34	88.76	3.95	78.06	4.28	85.18	4.22	84.50	3.88	77.58	4.18	83.74	4.80	91.85	4.44	88.83	4.32	86.38

#### CSE I D (B.Tech - 2016-17)

EE Lab	DS Lab	PS	EDC	MFC8	DS	BEE	DLD	CRT									
4.28	85.86	4.48	89.22	4.28	85.58	4.32	86.34	4.01	80.21	4.53	90.78	4.32	88.38	4.28	85.25	3.30	65.98

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Asuresh

EE Lab: Ramesh  
 DS Lab: Madhava  
 P: ...  
 C: Vasika  
 M: Deepika  
 PS: Madhava  
 BEE: ...  
 DLD: ...

do-  
 Hdo- C. Vasavi  
 R. Vasavi  
 do- G. Sravan Kumar  
 do- ...  
 do- ...  
 do- ...

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Asuresh

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5-36 St. No. 107, Tadlambram (V), GSI, Bandlaguda, Nagole, Hyderabad, 500082 | Phone: 0846021 4613



Form#	ALL	Admin Office	Transport	Library	Hosts	Subjects	Time Tables	Attendance
ExamCell	3186	Results	Reports					

A. Satyavardhini

### Subject Wise Feedback Report

A Year: 2016-17    v    Column: B.Tech    f    Dept: CSE    i    Sem: 4B    r

GO

#### I CSE IIA (B.Tech - 2016-17)

DEMS Lab	JAVA Lab	PLAT	CO	DEMS	DATA	DS	JAVA	OS							
5.68	71.78	4.56	87.54	4.48	89.22	9.78	73.97	4.31	82.24	4.74	84.76	4.43	88.20	4.48	89.20

#### I CSE IIB (B.Tech - 2016-17)

DEMS Lab	JAVA Lab	PLAT	CO	DEMS	DATA	DS	JAVA	OS							
4.17	83.89	4.06	83.78	3.89	77.18	4.38	87.75	3.68	73.16	3.70	75.56	4.78	85.87	4.77	86.47

#### I CSE IIC (B.Tech - 2016-17)

DEMS Lab	JAVA Lab	PLAT	CO	DEMS	DATA	DS	JAVA	OS							
4.04	80.74	2.75	85.15	4.50	84.01	3.70	74.04	4.06	81.58	4.83	80.89	4.87	97.42	1.83	80.89

#### I CSE IIV (B.Tech - 2016-17)

DEMS Lab	JAVA Lab	PLAT	CO	DEMS	DATA	DS	JAVA	OS							
4.15	83.82	2.08	41.36	3.87	77.32	3.33	88.82	4.59	82.08	3.77	76.69	4.81	92.11	1.78	88.27

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Asuresh

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Date: 21/12/2018

## Department of Electronics and Communications Engineering


The following are the list of weak students identified based on the previous semester results for the Academic year 2018-19 II-year II semester Section A.

S.No	H.T.No	Name of the Student	No of Backlogs in Previous semester
1	16VE1A0417	Kakulavaram Anilketh Reddy	4
2	17VE1A0405	Bogadi Nithil	3
3	17VE1A0409	Dandu Sumanth Reddy	4
4	17VE1A0414	G.Kalyan	3
5	17VE1A0416	Gajjishorak Kumar Yadav	3
6	17VE1A0419	Gundlapally Akshay Reddy	5
7	17VE1A0419	Lenkala Pransy Kumar Reddy	3
8	17VE1A0430	M P S Priyadarshan	4
9	17VE1A0431	M.Praneeth Reddy	4
10	17VE1A0432	Madiveni Lokesh	5
11	17VE1A0434	Mundelapalli Haralal Krishna	5
12	17VE1A0438	Margouni Abhishek	4
13	17VE1A0440	Nakala Sneekana Manish	3
14	17VE1A0446	Peeanna Ramesh	5
15	17VE1A0449	Sarla Ashok	5
16	17VE1A0455	Tbota palli Srikanth Phani Pavan Kumar	4
17	17VE1A0459	Vallapu Nithish Reddy	3
18	18VE5A0401	Adepu Hemant	3

*Aswath*

  
Year Incharge

  
Head of Department  
Department of Electronics and Communications Engineering  
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Dr. M. Manoj Krishna  
Professor & Head of Department  
Electronics and Communications Engineering  
Sreyas Institute of Engineering and Technology  
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Date: 21/12/2018

### Department of Electronics and Communications Engineering

The following are the list of weak students identified based on the previous semester results for the Academic year 2018-19 II-year II semester Section B.

S.No	H.T.No	Name of the Student	No. of backlogs in Previous Semester
1	17VE1A0463	Akoju Mahesh	3
2	17VE1A0465	Ananthula Bharat Bhaskar Reddy	4
3	17VE1A0467	Gandi Rajesh Kalyan	4
4	17VE1A0474	Dwararam Manjunath Reddy	2
5	17VE1A0476	Gangnapally Vinay Kumar Reddy	2
6	17VE1A0479	Jasani Gokul	5
7	17VE1A0487	Kornagala Sai Kiran	4
8	17VE1A04A1	Narisetti Parthiv Praneeth Sai	3
9	17VE1A04A2	Pallela Datta Naga Sai	4
10	17VE1A04A3	Pocham Prithvi Kumar	4
10	17VE1A04A6	Ranglaeni Vikramaditya	5
11	17VE1A04A7	S Anvesh	3
12	17VE1A04B1	Sangareddy Vivek Varadhan Reddy	2
13	17VE1A04B9	Vineth Yadav Gaji	3
14	18VES00409	Sripathi Mikhil Metha	5
15	18VES00416	Kistadi Shiva Kumar	3
16	18VES00417	Kothagadi Mounika	4

*Asuresh*

*[Signature]*  
Year Incharge

*Asuresh*  
Principal  
Sreyas Institute of Engineering and Technology  
Bodlaguda, Bandlaguda, Hyderabad

*[Signature]*  
Dr. N. Murali Krishna  
Professor & Head of the Department  
Electronics & Communication Engg  
Sreyas Institute of Engineering and Technology  
Bodlaguda, Bandlaguda, Hyderabad

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Date: 21/1/2019

## Department of Electronics and Communications Engineering

The following are the students, who have attended the remedial class on "Analog Communications" for II Year II Sem Section B.

S.No	Roll No	Name of the Student	Signature
1	17VE1A0479	Gokul	Gokul
2	17VE1A0463	Nikhilesh	Nikhilesh
3	17VE1A0465	A. Bhavath Bhusan	Bhavath
4	17VE1A0476	Vinay	Vinay
5	18VESAD 417	K. Mounika	Mounika
6	17VE1A0487	K. Sai Kiran	Sai Kiran
7	17VE1A0496	R. Vikram	Vikram
8	17VE1A0467	B. Rajesh Kalyan	Rajesh
9	17VE1A04A2	Datta Naga Sai	DNS
10	18VE1A04A1	Praneeth	Praneeth
10	18VE1A0409	Nikhil	Nikhil
11	18VESAD 016	Shiva	Shiva
12			
13			

Year In Charge

Dr. N. M. ...  
Professor & Head of Department  
Electronics & Communications Engineering  
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Bandlaguda, Hyderabad, India

Dr. N. M. ...  
Professor & Head of Department  
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Bandlaguda, Hyderabad, India

HOD

Date: 25/1/2019

## Department of Electronics and Communications Engineering

The following are the students, who have attended the remedial class on\* Analog Communications\* for II Year II Sem Section C.

S.No	Roll No	Name of the Student	Signature
1	18VE5A0434	Shikharth	<i>Shikharth</i>
2	18VE5A0429	Manisha	<i>Mani</i>
3	17VE1A0400	Yashwanth	<i>Yash</i>
4	17VE3A04E0	Shashank	<i>Shashank</i>
5	17VE1A0401	Vinay	<i>Vinay</i>
6	17VE1A0402	Dheeraj	<i>chana</i>
7	17VELA04E6	Abhishek	<i>Abh.</i>
8	17VE1A0409	Rishita Reddy	<i>Rishita Reddy</i>
9	18VE5A0425	Lakshika	<i>Lakshika</i>
10	18VE5A0428	Sangeetha	<i>Sangeetha</i>
10	18VE5A0431	Nandi Vardhan	<i>Vardhan</i>
11			
12			
13			
14			

*Aswath*  
Year In charge

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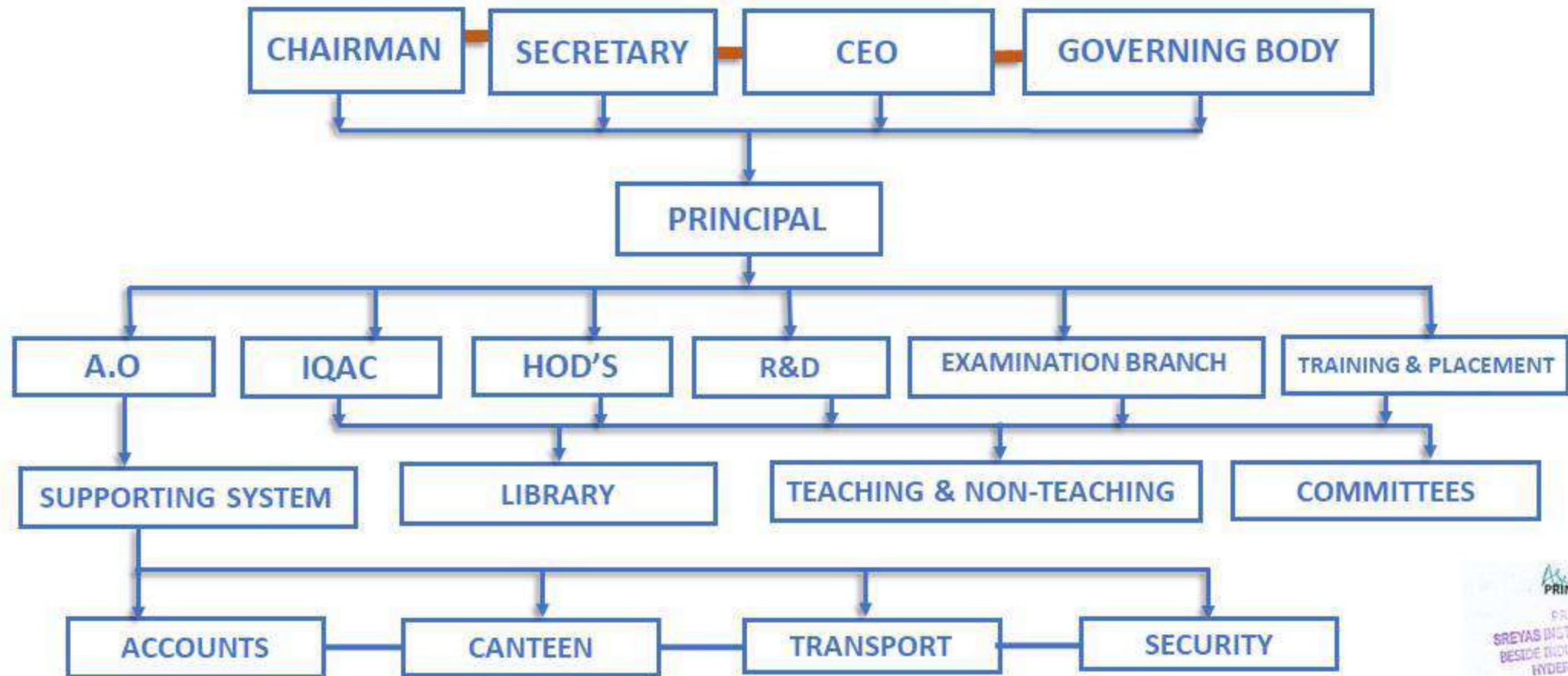
*Aswath*  
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*Dr. N. Murali K...*

Dr. N. Murali K...  
Head of the Department  
Department of Electronics and Communications Engineering  
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