



INSTITUTE OF ENGINEERING AND TECHNOLOGY
(Approved by AICTE, New Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC)Hyderabad | PIN: 500068

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
SCHEME OF EVALUATION FOR MAJOR PROJECTS**

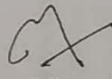
The overall project work shall be evaluated for 200 marks

Internal marks: 50

External marks: 150

Internal Marks scheme of evaluation:

S.No.	Rubric	Marks	Relevance to COs
1	Problem definition	5 marks	CO1
2	Literature Survey	5marks	CO2
3	Design and Implementation	10 marks	CO3
4	Result and Conclusion	10 marks	CO4
5	Presentation Skill and Time management	5 marks	CO5
6	Team work	5 marks	
7	Guide marks for PRC I	5 marks	
8	Guide marks for PRC II	5 marks	
	TOTAL	50 marks	


Dr. N. Murali Krishna
Head of the Department
Electronics & Communication Engineering
Sreyas Institute of Engineering and Technologies
Beside Indu Aranya, Nagole, Hyderabad-500 068.



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION

Rubrics for Project Assessment

Topic	Unacceptable / Incomplete (1)	Marginal / Progressing (2)	Acceptable / Satisfactory (3)	Exceptional / Exemplary (4-5)	Points
Problem Definition	Little or no grasp of problem or methodology hence incapable of producing	Some understanding of problem. Major deficiencies that impacted the quality of	Overall sound understanding of the	Clear and complete understanding of the problem	5
Literature search and use of reference	No proper Literature review and No referencing system are used.	Inadequate references in the text. Inconsistent or illogical referencing system.	Sufficient references are reviewed and Consistent referencing system.	Literature section is complete and comprehensive. Consistent and logical referencing system.	5
Design Flow	No proper understanding of design methodology.	Inconsistent in design flow.	Appropriate design methodology usage. Tools used with	Clear and Complete knowledge to design project. Proper tools are used	5
Implementation	Serious deficiencies in understanding the correct selection and/or use of tools.	Minimal application and use of appropriate tools.	moderate effectiveness to develop project	Proper tools are used effectively to develop and analyze project.	5
Results and Conclusions	The project conducted is irrelevant & No or erroneous conclusions based on achieved results.	Serious deficiencies in support for stated conclusions.	Sound conclusions reached based on achieved results.	Insightful, supported conclusions and recommendations.	10
Presentation, Time Management Skill and Team Work (10)					
Presentation Skill	Significant mumbling and incorrect pronunciation of terms. In -audible voice modulation, Monotonous, no eye contact, rate of speech is too fast or too slow	Occasional mispronunciation of terms. Little eye contact, uneven rate and only little expression	Voice is clear and at a proper level. Most words are pronounced correctly. Some eye contact, steady rate, excessively rehearsed	Clear voice and correct, precise pronunciation of terms. Good eye contact, steady rate, enthusiasm, confidence	5
Teamwork	Little or no distribution of work efforts and responsibilities. Little or no ability to work together in a professional and productive manner adversely affecting end result.	Minimal organization and planning with limited contributions of most team members. Significant deficiencies in leadership, cooperation and / or	Adequate organization and planning with contributions from all members of the team.	reat organization and planning with full participation and technical contributions from all members. Utilizes technical	5

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING DAAC

Date: 28-12-2018

TO

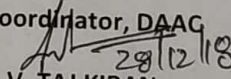
The HOD,
E.C.E,
S.I.E.T,
NAGOLE, HYDERABAD-68.

SUB: Assessment of project abstracts.

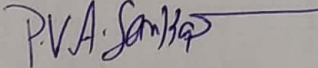

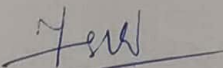
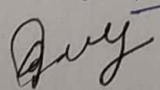
Sir,

The project titles and abstracts are assessed by the DAAC and Approved. Improvement suggested for Student compilation of abstract for 4 projects and conveyed the same to project coordinator. The Document is attached with this letter.

Coordinator, DAAC


28/12/18
V. TAJ KIRAN

Members

- 1) Mr. Dr. V.A. SANKAR PONNAPALLI 
- 2) Mr. Dr. SANDEEP KUMAR 
- 3) Mr. Y. SATISH KUMAR 
- 4) Mr. B. AMARENDER REDDY 

Approved
 28/12/18



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 DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
 IV B.Tech Section-A Project Titles and Guides (AY 2018-19)

Forwarded to DAAS
 28.12.2018

S.No.	Batch No.	Roll No.	Name of the Student	Title of the Project	Category	Location of the Project	Internal Guide	Remarks
1	A-1	15VE1A0401	A Sony	Generation and Authentication of Digital Certificate using Matlab	Digital Image Processing	In-House	Dr. Sandeep Kumar	
2		15VE1A0450	S Abhilash Goud					
3		15VE1A0447	Pothuganti Divya Jhansi					
4		15VE1A0448	R S P Shanmukhanath					
5		15VE1A0402	Archana Bathula					
6		15VE1A0406	Ch Akash					
7	A-2	15VE1A0452	Shravya Mandhapuram	Aperture Coupled Microstrip Antenna Design and Analysis	Antennas	In-House	Y. Satish Kumar	
8		15VE1A0405	B Krishna Mohan Goud					
9		15VE1A0432	Malyala Srinija					
10	A-3	15VE1A0424	K Revanth Yadav	Design Implementation of 32 bit RISC CPU in verilog	VLSI	In-House	Ch. S. V. Maruthi Rao	
11		16VES1A0402	Andhe Shivakumar					
12	A-4	16VES1A0411	Boga Dileep	A Low Power Incremental Delta-Sigma ADC for CMOS image Sensors	Analog Devices	In-House	V. Taj Kiran	
13		15VE1A0409	Ch Varshitha Reddy					
14		16VES1A0403	Anipindi Durgaprasad					
15		15VE1A0456	Veena Chitkuri					
16		16VES1A0410	Bejgum Sai Phanindra					
17	A-5	15VE1A0440	Neela Sai Rohith	Design and simulation of Internet of Things for Smart City	Networking	In-House	K. Narsimha	
18		15VE1A0454	S Sandeep Goud					
19		15VE1A0441	Nelanti Mounika					
20	A-6	15VE1A0444	Pasham Likhitha	Advanced Security System using HCI	Embedded Systems	In-House	Dr. N. Murali Krishna	
21		15VE1A0418	Jabishetti Shrivani					
22		15VE1A0411	Dasadi Akhiladeshwari					
23		15VE1A0413	Didugu Bhavani					
24	A-7	15VE1A0445	Pasunoori Ganesh	Implementation of Floating Point Butterfly Architecture based on Binary Signed Digit Representation	VLSI	In-House	G. Ramachandra Kumar	
25		15VE1A0434	Maragani Nagaraju					
26		15VE1A0428	Koppolu Kayasri					
27	A-8	15VE1A0459	Voruganti Sai Kumar	Point-wise shape adaptive DCT for High-Quality Denoising and Deblocking of Grayscale and Color Images	Digital Image Processing	In-House	G. Vijay Goud	
28		15VE1A0442	Oruganti Kusuma Priya					
29		15VE1A0458	Vinayak					
30		15VE1A0433	Mamidi Sanghavi					



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Department of Electronics & Communication Engineering

CIRCULAR

Dear Faculty members,

For the following list of students you are appointed as **INTERNAL GUIDE** for their IV-II Major projects for the academic year 2016-17. Please co-operate and guide the students.

Date : 30.01.2017

Section: ECE-A

Sl. No	Batch No	Roll No	Title Of The Project	Organization	Domain	Internal guide	Signature
1	A1	13VE1A0446	Simulation of Payloads microcontroller using PIC	BDL	Embedded systems	Mr. B. Amarendra Reddy Associate professor	
		13VE1A0404					
		13VE1A0435					
		13VE1A0451					
2	A2	13VE1A0422	Agro sense: A design for efficient farming system using sensors	In-House	Embedded systems	Mr. Govardhan Naidu Assistant professor	
		13VE1A0412					
		13VE1A0443					
		13VE1A0445					
3	A3	13VE1A0402	Design And Testing Of 155 Mbps Link Between Two Srm-16 SDH Systems Along With Protection	BSNL	Networking	Dr.T.Ravichandra Babu Professor	
		13VE1A0413					
		13VE1A0411					
4	A4	13VE1A0431	Development Of Monitoring And Altering Systems For Real Time Core Network	BSNL	Networking	Dr.T.Ravichandra Babu Professor	
		13VE1A0426					
		13VE1A0405					
		14VE5A0409					

Accepted by
 DAAC
 2/2/17

Forwarded to
 DAAC



Department of Electronics & Communication Engineering

CIRCULAR

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Date : 30.01.2017

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Section: ECE-A

Sl. No	Batch No	Roll No	Title Of The Project	Organization	Domain	Internal guide	Signature
1	A1	13VE1A0446	Simulation of Payloads microcontroller using PIC	BDL	Embedded systems	Mr. B. Amarendra Reddy Associate professor	
		13VE1A0404					
		13VE1A0435					
		13VE1A0451					
2	A2	13VE1A0422	Agro sense: A design for efficient farming system using sensors	In-House	Embedded systems	Mr. Govardhan Naidu Assistant professor	
		13VE1A0412					
		13VE1A0443					
		13VE1A0445					
3	A3	13VE1A0402	Design And Testing Of 155 Mbps Link Between Two Srm-16 SDH Systems Along With Protection	BSNL	Networking	Dr. T. Ravichandra Babu Professor	
		13VE1A0413					
		13VE1A0411					
4	A4	13VE1A0431	Development Of Monitoring And Altering Systems For Real Time Core Network	BSNL	Networking	Dr. T. Ravichandra Babu Professor	
		13VE1A0426					
		13VE1A0405					
		14VE5A0409					



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5	A5	13VE1A0425	Migration Of Corporate Networks From Ipv4 To New Generation Protocol Ipv6 Using Tunneling Mechanisms	BSNL	Networking	Dr. Kishore Reddy Professor	
		13VE1A0410					
		13VE1A0417					
		13VE1A0424					
6	A6	13VE1A0409	Measurement Of KPIS in GSM Radio Network In Gachibowli Area	BSNL	Networking	Dr. Kishore Reddy Professor	
		13VE1A0415					
		13VE1A0450					
		13VE1A0418					
7	A7	13VE1A0421	Use of mobile robot in coalmine disasters	In-House	Embedded Systems	Praveena.A Assistant professor	
		13VE1A0427					
		13VE1A0447					
		13VE1A0449					
8	A8	13VE1A0407	Design And implementatn of ISP MPLS Back Bone Network On Ipv6 Using 6pe Configuration	BSNL	Networking	Dr. T. Ravichandra Babu Professor	
		13VE1A0433					
		13VE1A0405					
		13VE1A0423					
9	A9	13VE1A0430	GSM Radio Network Optimization	BSNL	Networking	Mr. V. Taj Kiran Associate professor	
		13VE1A0428					
		13VE1A0438					
		13VE1A0406					
10	A10	14VE5A0419	Design And Testing Of 2.048 Mbps Link Between Two Stm-4 SDH Systems	BSNL	Networking	Mr. V. Taj Kiran Associate professor	
		13VE1A0436					
		13VE1A0429					
11	A11	13VE1A0434	Design And Implementation Of A LAN Using VLAN, VTP And STP Along With Configuration Of DHCP HTTP DNS Servers	BSNL	Networking	Mr. B. Sreenivasu HOD	



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12	A12	13VE1A0444	Vehicle Theft Intimation To The Owner On His Cell Phone By GSM	In-House	Embedded Systems	Praveena.A Assistant professor	
		13VE1A0442					
		13VE1A0414					
		13621A0449					
13	A13	13VE1A0432	Measurement of performance parameters of DWDM network elements and ROADM concept of 40 Channel 10 Gb/s) DWDM system (Huawei)	BSNL	Networking	Mr.B.Amarender Reddy Associate professor	
		13VE1A0441					
		13VE1A0448					
14	A14	13VE1A0420	Network Monitoring System Android based hand gesture controlled tablet Using MEMS Technology	(RC) Inhouse	(Networking) Embedded systems	Mrs. P. Chaitanya Assistant professor	
		13VE1A0452					
		13J81A0405					
15	A15	13VE1A0416	Automatic Toll e ticketing system	INHOUSE	Embedded systems	Mr. J. Pandu Ranga Rao Associate professor	
		13VE1A0407					



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING IV ECE - B MAJOR-PROJECTS TITLES AND GUIDES

S.No.	Batch No.	Roll Number	Name of the Student	Title Of The Project	Domain	Organization	Internal guide details	Remarks
1	B1	13VE1A0492	Peravelli Venkata Krishna Karthik	IPV6 WITH ADVANCED CONCEPTS	NETWORKING	BSNL	Mrs.B. SPANDANA, ASSISTANT PROFESSOR	
2		13VE1A0456	Baireddy Meghana Reddy					
3		13VE1A0472	Kathi Eshwar Reddy					
4		13VE1A0484	Mohammed Lubna Valiya					
5	B2	14VESAO404	Janagama Rajasamardhan	DESIGN OF 16FFT AND IFFT FOR OFDM APPLICATIONS	DSP	CITD	DR. KISHORE REDDY, PROFESSOR	
6		13VE1A0477	Kukatla Karunakar					
7		14VE1A0406	Usakoyala Rajender					
8		13VE1A0461	Bommidi Rajendra Prasad					
9	B3	13VE1A04A2	Yada Ayushman	DEVELOPMENT OF DIGITAL GLUE LOGIC FOR INTRA-PULSE ANALYSIS	RADAR SYSTEMS	DLRL	Mr.G.D. BASAVA RAJ. ASSISTANT PROFESSOR	
10		13VE1A0482	Mandal Dharshith					
11		13VE1A0458	Bathula Vamshi Krishna					
12		13VE1A0475	Kondabathin Kalyan Kumar					
13	B4	13VE1A04A0	V B S V N S Kiran	IMPLEMENTATION OF ADVANCED CONCEPTS OF EIGRP USING IPV6	NETWORKING	BSNL	Mrs.NEHA ANJUM, ASSISTANT PROFESSOR	
14		13VE1A0483	Merugu Santosh Kumar					
15		13VE1A04A4	Yarkala Naveen					
16		13VE1A0485	Mulinti Pravallika					
17	B5	13VE1A0463	Burgula Laxmi Manasa	DESIGN ,DEVELOPMENT AND TESTING OF 6-18GHZ WIDEBAND	MICROWAVE	DLRL	Mr.G. VIJAY GOUD, ASSISTANT PROFESSOR	
18		13VE1A0453	Aavula Srujana Reddy					
19		13VE1A0471	Kandibanda Venkatesh					
20		13VE1A0474	Kodumuri Sainath					
21	B6	13VE1A0473	Kethanapally Mahendhar Reddy	IMPLEMENTING MOBILE VOIP USING DIFFERENT PROTOCOLS AND ITS IN DEPTH ANALYSIS OF	MOBILE COMMUNICATION	BSNL	Mr.B. SREENIVASU, ASSOCIATE PROFESSOR	
22		13VE1A0496	Sankapuram Sai Teja Reddy					
23		13VE1A0478	Kunduru Lokesh Reddy					
24		13VE1A04A3	akkala Venkata Anantha Murali Krishi					
25	B7	13VE1A0481	Malreddy Sandhya	SECURITY LUGGAGE SCANNER WITH CONVEYER BELT ARRANGEMENT	EMBEDDED SYSTEMS	CITD	Mr.CH.V. MARUTHI RAO, ASSOCIATE PROFESSOR	
26		13VE1A0495	Puchakayalla Nikitha Reddy					
27		13VE1A0479	Kutmulge Manisha					
28		13VE1A0464	Chelliboyina Vineetha Sowjanya					
29	B8	13VE1A0497	Shaik Rahamath Pasha	GESTURE CONTROLLED ROBOT WITH SPY CAMERA USING ARDUINO	ROBOTICS	CITD	Mr.B. AMARENDER REDDY, ASSOCIATE PROFESSOR	
30		13VE1A0467	K Kanishka Samrat					

31	13VE1A0476	Kongara Hari Krishna	DESIGN AND IMPLEMENTATIONS OF ADVANCED FEATURES IN INTER DOMAIN ROUTING	NETWORKING	BSNL	Mrs.R. UTHIRA DEVI, ASSISTANT PROFESSOR	
32	13VE1A0499	Syed Farees Mehdi	DESIGN OF POLY PHASE CODED SEQUENCES FOR MICRO CONTROLLER BASED 3D RADAR PROTOTYPE USING DESIGN OF LOW ASP SEQUENCES IN RADAR	RADAR SYSTEMS	RCI	Mr.J. PANDU RANGA RAO, ASSOCIATE PROFESSOR	
33	13VE1A0466	Ega Shashank	PORTABLE CAMERA BASED ASSISTIVE TEXT AND PRODUCT LABEL OPTIMIZING FOUR PHASE CODES FOR RADARS USING GENETIC ALGORITHM	EMBEDDED SYSTEMS	INHOUSE	Mrs. PRAVEENA. A, ASSISTANT PROFESSOR	
34	13VE1A0470	K Pranay Kumar Reddy				Mr.J. PANDU RANGA RAO, ASSOCIATE PROFESSOR	
35	13VE1A04A1	Valaboju Kranthi Kumar				Mr.Y.SATISH KUMAR, ASSOCIATE PROFESSOR	
36	13VE1A0488	P Hemanth Sagar				Mr.J. PANDU RANGA RAO, ASSOCIATE PROFESSOR	
37	13VE1A0462	Bondugula Dharani				Mr.P. PRAVEEN RAJU, ASSOCIATE PROFESSOR	
38	13VE1A0480	Macherla Swamy Himabindu					
39	13VE1A0457	Banoori Pooja Sri					
40	13VE1A0454	Ambati Priyanka					
41	13VE1A0486	Nannuri Koushik Reddy					
42	13VE1A0490	Padma Navneeth					
43	13VE1A0491	Peddavoora Hareesh					
44	14VES A0402	Artham Nikilesh Kumar					
45	14VES A0403	Diviti Saipreetham					
46	14VES A0401	Akki Shivakumar					
47	13VE1A0498	Singamaneni Srisaichand					
48	13VE1A0493	Pinikeshi Ravi Kiran Reddy					
49	13VE1A0455	Andam Yashwanth Reddy					
50	13VE1A0465	Dasari Apoorva Dhana Santoshi					
51	13VE1A0469	K Mounika					
52	13VE1A0459	Bellam Chandana					

Projects Coordinator

Head of the Department

B. Sreenivasu
HEAD OF THE DEPARTMENT
Electronics & Communication Engg.
JYAS INSTITUTE OF ENGG. & TECH.
Siddes Indu Aranya, Nagole, Hyd-t
e-mail: ece.hod@sreyas.ac.in



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Department of Electronics & Communication Engineering

Section: ECE-C

A.Y: 2016-2017

Sl. No	Batch No	Roll No	Title of the project	Organization	Domain	Internal guide details	Signature
1	C1	13VE1A04D8	E/J BAND Field Signal Generator	BEL	Embedded systems	Sowjanya.A Assistant professor	
		13VE1A04A7					
		13VE1A04D3					
		13VE1A04C4					
2	C2	13VE1A04F3	Design and implementation of secure communication between two branches of company using IPSEC based VPN	BSNL	Networking	Hemalatha Dalmia Assistant professor	
		13VE1A04B1					
		13VE1A04B5					
		13VE1A04F0					
3	C3	13VE1A04C7	Flex Sensor Controlled Wheel Chair	IN-HOUSE	Embedded systems	K.Sonali Swaroop Assistant professor	
		13VE1A04C6					
		13VE1A04E7					
		13VE1A04B2					
4	C4	13VE1A04D4	Reliable Low-Power Multiplier Design Using Fixed Width Replica Redundancy Block	CITD	VLSI and	G.Ramachandra Kumar Assistant professor	
		13VE1A04E3					
		13VE1A04C1					
		13VE1A04C0					
5	C5	13VE1A04B6	A vehicle to vehicle communication protocol for cooperative collision warning	CITD	Embedded systems	S.Subramanyam Assistant professor	
		13VE1A04E5					
		13VE1A04B3					
		13VE1A04A6					



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Sl. No	Batch No	Roll No	Title of the project	Organization	Domain	Internal guide details	Signature
6	C6	13VE1A04E9	BLOB controlled robot through webcam using MATLAB	IN-HOUSE	Embedded systems	K.Sonal Swaroop Assistant professor	
		13VE1A04D5					
		13VE1A04B4					
		13VE1A04C8					
		13VE1A04E2					
7	C7	13VE1A04C2	Development of coal mine safety system using wireless sensor network	IN-HOUSE	Embedded systems	K.Narasimha Assistant professor	
		13VE1A04B8					
		13VE1A04E1					
		13VE1A04A8					
8	C8	13VE1A04F4	Integrated missed call alert server implementation based on SIP platform	BSNL	Networking	Ch.V. Maruthi Rao, Associate Professor	
		13VE1A04C9					
		13VE1A04A9					
		14VE5A0408					
9	C9	13VE1A04B0	Aerial Surveillance Using Quadcopter	CITD	Embedded systems	V.Taj Kiran Associate Professor	
		13VE1A04E4					
		13VE1A04E0					
10	C10	13VE1A04C3	Development Of Programmable Embedded System Using ZYNQ FPGA In Xilinx Vivado	RCI	VLSI & FPGA	Hemalatha Dalmia Assistant professor	
		13VE1A04D1					
11	C11	13VE1A04D0	Data Acquisition And Control System Using FPGA For Avionic Systems	RCI	Embedded on FPGA	N.Naveen Kumar Assistant professor	
		13VE1A04E8					
		13VE1A04D2					
12	C12	13VE1A04A5		MTS	VLSI	Y.Satish Kumar, Associate Professor	



Section: ECE-D

S.No	Batch No	Roll No	Title of the Project	Organization	Domain (platform)	Internal Guide	Signature
1	D1	13VE1A04G1	Design and implementation of Four Phase Code using Modified Genetic Algorithm(MGA)	RCI	VLSI	Mr.P.Praveen Raju Associate Professor	
2		13VE1A04G2					
3		13VE1A04H0					
4		13VE1A04J0					
5	D2	13VE1A04F8	Design and implementation of Ultrasonic sensor in Object detection	IN-HOUSE	Embedded Systems	Mr.G Vijay Goud Assistant professor	
6		13VE1A04G9					
7		13VE1A04H1					
8		13VE1A04L5					
9	D3	13VE1A04G0	Embedded system for position acquisition using GPS	RCI	Embedded Systems	Mr.P.Praveen Raju Associate Professor	
10		13VE1A04H2					
11		13VE1A04H3					
12		13VE1A04K7					
13	D4	13VE1A04G3	Design and Development of 6DOF using PIC Microcontroller	BDL	Embedded Systems	Mr.S.Govardhan Naidu Assistant professor	
14		13VE1A04H4					
15		13VE1A04H6					
16		13VE1A04J9					
17	D5	13VE1A04F5	IOT Based Secure Health care system using BSN	NI-MSME	Embedded Systems	Mr. G Vijay Goud Assistant professor	
18		13VE1A04K0					
19		13VE1A04L0					
20		13VE1A04L3					
21	D6	13VE1A04H7	Implementation of 2D Discrete Fourier Transform on sliding windows	DRDL	Image Processing	Mr. S.Govardhan Naidu Assistant professor	
22		13VE1A04J1					
23		13VE1A04K5					
24		13VE1A04L1					

A.Y: 2016-2017



Sreyas Institute of Engineering & Technology

(Approved by AICTE, New Delhi | Affiliated to JNTU, Hyderabad)

Campus Address : Beside Indu Aranya, GSI, Bandlaguda, Nagole, Hyderabad, 500068 | Phone : 040-6581 4415



Sl. No.	Roll No.	Project Title	Category	Subject	Supervisor
25	13VE1A04F6	Development of IOT Based smart security and monitoring devices for agriculture	NI-MSME	Embedded Systems	Mrs.P.Chaitanya Assistant professor
26	13VE1A04H8				
27	13VE1A04J3				
28	13VE1A04J6				
29	13VE1A04G7	PC-PC Communication using Ethernet	RCI	Embedded Systems	Mrs.B. Spandana Assistant professor
30	13VE1A04G8				
31	13VE1A04J2				
32	13VE1A04J4				
33	13VE1A04K3	IOT based robot monitoring system using Raspberry pi	IN-HOUSE	Embedded Systems	Mr.S.Govardhan Naidu Assistant professor
34	13VE1A04K1				
35	13VE1A04K8				
36	13VE1A04K9				
37	13VE1A04F7	Data Updation system using GSM	RCI	Embedded Systems	Mrs. A.Sowjanya Assistant professor
38	13VE1A04K2				
39	13VE1A04K6				
40	13VE1A04L2				
41	13VE1A04G4	Design of multi-channel data acquisition system based on SOPC	RCI	Embedded Systems	Mr. Ramachandra kumar Assistant professor
42	13VE1A04H9				
43	13VE1A04J7				
44	13VE1A04J8				

Project Coordinator

(S. Paudu Rang Red)

HOD
30/12/2017



SREYAS Institute of Engineering & Technology

(Approved by AICTE, New Delhi | Affiliated to JNTUH, Hyderabad)
Hyderabad | PIN: 500068

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

DAAC

Date: 22-12-2015

TO

The HOD,
E.C.E,
S.I.E.T,
NAGOLE, HYDERABAD-68.

SUB: FINAL YEAR ECE Project assessment.

Sir,

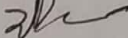
The project abstracts are assessed by the DAAC found to be good. Out of the projects abstracts submitted some are study projects, the committee suggested to modify, the correction is done and new project list has been approved by members of DAAC.

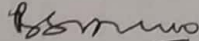
Coordinator, DAAC

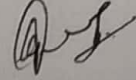
V. TAJ KIRAN

22/12/15

Members

1) J. PANDU RANGA RAO 

2) B. SREENIVASU 

3) B. AMARENDER REDDY 



GUIDLINES TO BE FOLLOWED WHILE PREPARING FINAL PROJECT DOCUMENT

All the students of **ECE** final year are hereby notified that while preparing the final project report, they should take care of the following instructions and prepare their document report in the format specified below:

The project report to be submitted to the college is to be made in 4 bound copies (apart from the personal copies), one for the Internal Guide, one for the department, one for the College Library and one for the Principal's Desk along with the softcopy including both Source Code and Documentation in a CD for all the "**n+3 copies**". n → number of students in each batch.

The bound copies have to be made in **NAVY BLUE** color.

Other specifications required are as follows:

Thesis Paper size	: A4
Font Metrics	
Chapters Headings	: 16pt, Bold ,
Headings	: 14pt, Bold , Bookman old style.
Sub headings	: 12pt, Bold , Bookman old style.
Description	: 12pt, Normal, Bookman old
style. Figures/Graphs/Charts/Tables headings:	10 pt, Bold , Bookman old style.
Line Spacing	: 1.5

A detailed Major project report should be made with a minimum of 80 pages spanning up to 100 pages, which includes all the following details in page wise order. Likewise for Technical seminar 20 – 25 Pages and for mini project 50-60 pages.

Each chapter should be separated by a separator (White Paper with chapter name in the center of the page)



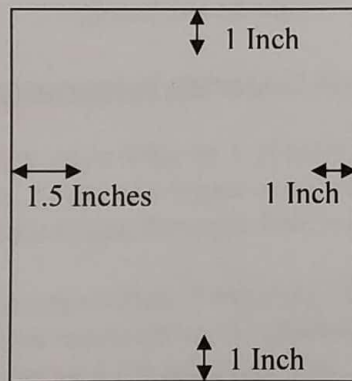


Order of pages before Contents:

Bound Cover Page (Format Enclosed)

1 st Page.	Title Page (Format Enclosed)
2 nd page.	Company Certificate
3 rd Page.	College Certificate (Format Enclosed)
4 th Page.	Acknowledgement (Format Enclosed)
5 th Page.	Abstract
6 th Page.	Contents

Margins for every page in the Report:



Justification: Regular matter ---- Both sides Indent (ctrl + j)

Side headings: - Left indent (Ctrl + l)

Any page heading ----- Center Indent (Ctrl + e)

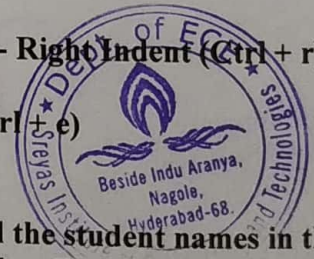
Names of Team Members in Acknowledgements ----- Right Indent (Ctrl + r)

Whole Cover page matter ----- Center Indent (Ctrl + e)

Student Names on Cover page: -

1. All the copies should have all the student names in the preferred order (i.e Team Leader, Team Member1 and Team member2)
2. Only self name in the individual copies.

Tab: - Every paragraph shall be starting with a single Tab.





Page No.s:- Shall be starting after the contents page.

Arrangement of Chapters:

The following is suggested format for arranging the Dissertation / Thesis matter into various chapters. However this arrangement can be changed to suit the particular project work.

1. Introduction
2. Literature Survey
3. Theoretical Analysis
4. Experimental Investigations
5. Experimental Results
6. Discussion of Results
7. Summary, Conclusion and Recommendations
(Original Contribution to be Highlighted)
8. Reference / bibliography
9. Appendices (if any)

The arrangement of paragraph in a Chapter :

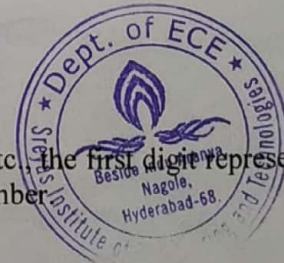
Each topic Title in a chapter should be properly numbered for example: 2.1, 2.2. etc., (Bold and Upper case) where, the first digit represents the Chapter number, and second digit, the topic Title number.

Sub-topic titles, if any, may be indicated as 1.1.1, 1.1.2, etc. (Bold and title case) i.e. the first digit representing the chapter, the second representing the topic title and the third representing the sub-topic title.

Photographs and Tables:

The photographs and tables occurring in a chapter may be serially numbered as Fig: 1.1, 1.2 etc., along with suitable CAPTION where the first digit represents the chapter, the second digit represents figure number.

The photograph may be represented as: plate 1.1, 1.2 etc. the first digit representing chapter and the second representing the photograph number



Graphs :

The graph should clearly indicate the points which are used for drawing the curve or curves along with the error bars. The axes (X, Y and Z) should have CAPTIONS



RESEARCH AND DEVELOPMENT CELL
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
KUKATPALLY, HYDERABAD – 500 085, A.P. INDIA
ACADEMIC REGULATIONS AND GUIDELINES FOR THE CANDIDATES OF B.TECH
/ M.TECH / M.S / RESEARCH PROGRAMMES
(effective from Academic Year 2008-2009)[†]

ANNEX-D

General Outline for Thesis Write-Up

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD – 500 085, A.P., INDIA
Month, Year

D.1 Size and copies:

The Dissertation should be submitted in A4 size paper and 3 / 5 copies of the thesis and synopsis are required to be submitted to the Incharge of Technical Seminar / MiniProjects / Projects duly signed by supervisor and Head of the Department. The thesis submission form should be filled and submitted along with the necessary fee and enclosures.

D.2 Paper, Typing & Format:

Bond paper should be used for the preparation of the Thesis. Typing should be done on the 12 font size letter, Bookman old style.

The lay-out should provide **1 ½ inch** on the left side, **1 inch** on top and bottom **1 inch** on the right side.

Fresh **Para** should commence after five spaces. Double spacing shall be provided throughout the dissertation / thesis. The page number shall be indicated at the top middle of the each page.

D.3 Binding:

Mini Project / Technical Seminar:

The Dissertation / thesis shall be Spiral bound, using cover of **white color**. The bound front cover should indicate in **the First Page** with the following:

1.(Title)
2.(Name of the candidate)
3.(Name of the Faculty in which the thesis is being submitted
(e.g. Faculty of Electronics & Communications Engg.))
4. Bottom

Two plain blank papers should be provided at the beginning and at the end.



Main Project:

The Dissertation / thesis shall be properly bound, using hard cover of **white color**. The bound front cover should indicate in **Navy Blue Embossed** letter with the following:

- 5.(Title)
- 6.(Name of the candidate)
- 7.(Name of the Faculty in which the thesis is being submitted (e.g. Faculty of Mechanical Engg.))
- 8. Bottom

Two plain blank papers should be provided at the beginning and at the end.

D.4 Third page:

Third page should contain the following:

- 1.(Title)
- 2. Thesis / Dissertation / Technial Seminar submitted in partial fulfillment of the requirement for the award of the degree of Ph.D./ /M. Phil./ M.S/B.Tech.
by
.....
(Name of the candidate)
Reg.No:
- 3. Bottom

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD – 500 085, A.P., INDIA
Month, Year**

D.5 Fourth page:

The fourth page should contain a certificate signed by the Student and Supervisor in the Following format.

CERTIFICATE

This is to certify that the thesis / dissertation / Technical Seminar entitledth at is being submitted by Sri / Smt. / Ms..... in partial fulfillment for the award of Ph.D. / M. Phil / M.S / B.Tech in to the Jawaharlal Nehru Technological University is a record of bonafide work carried out by him / her under our guidance and supervision.

The results embodied in this thesis have not been submitted to any other University or Institute for the award of any degree or diploma.

**Signature of Student
Name and Roll No**

**Signature of the Supervisor
Name and Designation**



Handwritten initials 'SN' on the right margin.

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD – 500 085, A.P., INDIA
Month, Year**

D.6 Fifth page:

The fifth page should contain a certificate signed by the Head/Incharge of Department in the following format.

CERTIFICATE

This is to certify that the thesis / dissertation / Technical Seminar entitled
.....
.....that is being submitted by Sri / Smt. /
Ms..... in partial fulfillment for the award of Ph.D.
/ M. Phil / M.S / B. Tech in to the Jawaharlal Nehru Technological
University is a record of bonafide work carried out by him / her at our
organization/institution

**Signature of Head of the Department
Name and Designation**

D.7 Sixth page:

The sixth page may include the 'Acknowledgement'.

D.8 Seventh page:

The seventh page may contain an abstract of the Dissertation / Thesis / Technical Semina
The candidate may emphasize here his/her contributions. The reader/reviewer should b
able to get the gist of the work in the abstract itself, without having to go into the mai
body of the text. In short, the abstract should be stand alone and self explanatory of the
entire work.



D.9 Pages 7TH, 8TH

In these pages the candidate must provide a table of contents, list of tables, list of figures, and photographs and notation.

NOTE: All the above pages are to be numbered in Roman numerals of lower case and the rest of the thesis should have English numerical numbers.

D.10 Number of pages:

In case the number of pages in the thesis (excluding the initial pages) exceeds 250, the candidate should get it printed on both sides of paper.

D.11 Arrangement of Chapters:

The following is suggested format for arranging the Dissertation / Thesis matter into various chapters. However this arrangement can be changed to suit the particular Research work.

1. Introduction
2. Literature Survey
3. Theoretical Analysis
4. Experimental Investigations
5. Experimental Results
6. Discussion of Results
7. Summary, Conclusion and Recommendations
(Original Contribution to be Highlighted)
8. Reference / bibliography
9. Appendices (if any)

D.12 The arrangement of paragraph in a Chapter :

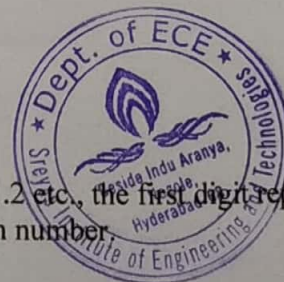
Each topic Title in a chapter should be properly numbered for example: 2.1, 2.2. etc., (Bold and Upper case) where, the first digit represents the Chapter number, and second digit, the topic Title number.

Sub-topic titles, if any, may be indicated as 1.1.1, 1.1.2, etc. (Bold and title case) i.e. the first digit representing the chapter, the second representing the topic title and the third representing the sub-topic title.

D.13 Photographs and Tables:

The photographs and tables occurring in a chapter may be serially numbered as Fig: 1.1, 1.2 etc., along with suitable CAPTION where the first digit represents the chapter, the second digit represents figure number.

The photograph may be represented as: plate 1.1, 1.2 etc., the first digit representing chapter and the second representing the photograph number.



D.14 Graphs :

The graph should clearly indicate the points which are used for drawing the curve or curves along with the error bars. The axes (X, Y and Z) should have CAPTIONS.

7

D.15 Bibliography or References:

The following format may be used for the Bibliography / References:

For Book

Sl. No	Author (s)	Book	Publisher	Year	Pages (PP)
--------	------------	------	-----------	------	------------

For Journal / Proceedings

Sl. No	Author (s)	Paper	Journal	Vol. No,	Year	Pages (PP)
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(A typical Specimen of Cover Pages is Attached)





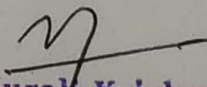
SREYAS

INSTITUTE OF ENGINEERING AND TECHNOLOGY
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Summary of domain wise projects

DOMAIN	A.Y. 2018-19	A.Y. 2017-18	A.Y. 2016-17	A.Y. 2015-16
Embedded Systems	23	25	27	22
Signal Processing & Communications	03	04	02	-
VLSI	11	06	05	04
Microwave Engineering	02	04	06	01
Antennas and wave Propagation	09	08	-	-
Wireless Networks	09	02	14	04
Image Processing	20	05	01	-


Dr. N. Murali Krishna
Head of the Department
Electronics & Communication Engineering
Sreyas Institute of Engineering and Technologies
Beside Indu Aranya, Nagole, Hyderabad-500 068.



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Department of Electronics and Communication Engineering

SCHEDULE FOR IV B.TECH I SEMESTER MINI-PROJECTS
IV B.Tech I-Sem A.Y:2018-19

Batch: 2015-19

18-05-18

S. No	Date	Day	Event	Remarks
1	30-04-18	Monday	Last date for Project Batches Registration	
2	08-05-18	Friday	Last date for Approval of Project titles along with the letters from industry	
3	21-05-18	Monday	Last date for Project Abstract Submission	
4	22-05-19	Tuesday	Title Approval and Guide Allocation	
5	24-05-18	Thursday	PRC-I for ECE - C & ECE - D	Project title to be defended
6	25-05-18	Friday	PRC-I for ECE - A & ECE - B	
7	28-05-18	Monday	Literature Survey document submission	
8	07-06-18	Thursday	ECE - A & ECE - B PRC-II	Status of the Project & Guidance
9	08-06-18	Friday	ECE - C & ECE - D PRC-II	
10	14-06-18	Thursday	Phase - II documentation based on the completion of the Project to be submitted to the guides	
11	27-06-18	Wednesday	Project Completion Certificate submission	
12	29-06-18	Friday	Final PRC for ECE - A & ECE - C	
13	30-06-18	Saturday	Final PRC for ECE - B & ECE - D	
14	09-07-18	Monday	Project Document submission	Final Document

Instructions to the students:

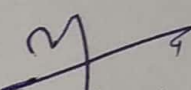
1. The dates mentioned above will be strictly followed.
2. In case many students are writing supplementary exam on a particular day; the day may be varied based on the information provided by the exam branch.
3. Absence for the PRC's will not be entertained except for emergency situations, for which the information has to be sent to the Project Coordinator and Head of the department.
4. In the remaining cases, a student can avail leave with prior information to the Project Coordinator and the Head of the department and the same has to be filed.
5. 20% of the marks based on the guide's recommendations, 10% on the attendance, 10% based on the Project Coordinator recommendations, 10% by the discretion of the Head of the Department and the remaining 50% will be from PRC. No other recommendations will be entertained.

Note:

1. All the students are instructed to coordinate with the concerned section Project coordinator for smooth conduction of the Projects
2. Students can contact your concerned Project Coordinator for any queries.

Project Coordinators

1. IV ECE – A – A. Praveena
2. IV ECE – B – D. Bindu
3. IV ECE – C – V. A. Shankar Ponnappally
4. IV ECE – D – K. Sonali Swaroop


Head of the Department – ECE

Dr. N. Murali Krishna

Head of the Department

Electronics & Communication Engineering

Sreyas Institute of Engineering and Technologies

Beside Indu Aranya, Nagole, Hyderabad-500 060.



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Campus Address : Baside Indu Azhaya, CSI, Basalinguda, Nagole, Hyderabad, 500008 | Phone : 040-6582 4415



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

2015 Batch IV B.Tech I Sem R15 ECE - A Mini-Project Batches

A - 1	BL's	%	A - 2	BL's	%	A - 3	BL's	%	A - 4	BL's	%
15VE1A0401	0	69.76	15VE1A0402	0	69.37	15VE1A0432	0	69.16	16VE5A0411	0	69.02
15VE1A0450	0	58.93	15VE1A0406	0	58.9	15VE1A0427	0	56.48	15VE1A0409	1	60.51
15VE1A0447	1	59.52	15VE1A0452	1	67.82	16VE5A0402	1	58.04	16VE5A0403	1	57.32
15VE1A0448	6	53.13	15VE1A0405	7	49.1	15VE1A0437	7	53.7	15VE1A0456	7	51.43

A - 5	BL's	%	A - 6	BL's	%	A - 7	BL's	%	A - 8	BL's	%
16VE5A0410	0	68.89	15VE1A0441	0	68.24	15VE1A0413	0	66.45	15VE1A0459	0	66.06
15VE1A0440	1	66.39	15VE1A0444	1	69.22	15VE1A0445	1	59.76	15VE1A0442	0	62.33
16VE5A0408	1	57.91	15VE1A0418	2	58.18	15VE1A0434	2	55.52	15VE1A0458	2	55.13
15VE1A0454	9	46.15	15VE1A0411	4	54.06	15VE1A0428	4	53.19	15VE1A0433	4	56.48

A - 9	BL's	%	A - 10	BL's	%	A - 11	BL's	%	A - 12	BL's	%
15VE1A0420	0	65.49	15VE1A0412	0	65.04	15VE1A0439	0	64.36	15VE1A0416	0	64.33
15VE1A0451	0	62.3	15VE1A0414	0	61.64	15VE1A0421	0	61.43	15VE1A0404	0	61.07
15VE1A0408	3	58.15	15VE1A0410	3	60.06	15VE1A0427	3	53.94	15VE1A0429	3	55.19
15VE1A0438	11	47.07	15VE1A0426	17	40.33	15VE1A0430	6	54.51	15VE1A0407	5	52.75

A - 13	BL's	%	A - 14	BL's	%	A - 15	BL's	%
15VE1A0425	0	63.58	15VE1A0436	0	62.66	15VE1A0403	0	62.57
15VE1A0431	0	60.96	15VE1A0453	0	60.33	15VE1A0423	0	59.37
15VE1A0446	3	55.37	15VE1A0449	3	59.19	15VE1A0455	3	56.33
16VE5A0407	5	51.02	15VE1A0415	6	54.18	15VE1A0419	6	49.52

Dr. N. Murali Krishna
 Head of the Department
 Electronics & Communication Engineering
 Sreyas Institute of Engineering and Technology
 Baside Indu Azhaya, Nagole, Hyderabad-500 008.



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Conseiling Code
SRYAS

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

2015 Batch IV B.Tech I Sem R15 ECE - B Mini-Project Batches

B - 1	BL's	%	B - 2	BL's	%	B - 3	BL's	%	B - 4	BL's	%
15VE1A0474	0	59.43	15VE1A0495	0	76.87	15VE1A0467	0	74.69	15VE1A04A3	0	73.58
15VE1A0465	8	47.04	15VE1A04A4	0	56.87	15VE1A0487	0	56.03	15VE1A0496	0	60.54
			15VE1A0483	1	58.66	15VE1A0484	1	61.22	15VE1A0492	1	61.52
			15VE1A04B2	8	46.18	15VE1A0497	9	47.43	15VE1A04B0	12	48.96

B - 5	BL's	%	B - 6	BL's	%	B - 7	BL's	%	B - 8	BL's	%
15VE1A04B9	0	70.42	15VE1A04A5	0	69.43	15VE1A04B5	0	68.9	16VE5A0417	0	68.85
15VE1A04A6	0	65.97	16VE5A0421	0	63.15	15VE1A0468	0	62.75	16VE5A0414	0	62.55
15VE1A0482	1	62.72	16VE5A0416	1	54.81	15VE1A04B8	2	57.43	16VE5A0415	2	53.4
15VE1A04A9	5	58.24	16VE5A0422	6	52.55	15VE1A0461	15	40.24	15VE1A04B1	13	42.39

B - 9	BL's	%	B - 10	BL's	%	B - 11	BL's	%	B - 12	BL's	%
15VE1A0464	0	68.57	15VE1A0476	0	68.48	15VE1A0470	0	68.36	15VE1A0485	0	67.61
15VE1A04A0	0	64.54	15VE1A0490	0	64.36	15VE1A0477	0	63.94	15VE1A0491	0	63.16
15VE1A04A8	1	63.01	15VE1A0462	2	61.16	15VE1A0499	2	55.58	15VE1A04B7	2	57.97
15VE1A0489	6	47.52	15VE1A0473	4	52.72	15VE1A0488	3	57.01	14VE1A0420	6	28.93

B - 13	BL's	%	B - 14	BL's	%	B - 15	BL's	%	B - 16	BL's	%
15VE1A0493	0	67.25	16VE5A0423	0	66.3	15VE1A04B3	0	66.24	15VE1A0471	0	65.22
16VE5A0418	0	65.79	15VE1A04A7	1	62.63	15VE1A0486	0	64.72	15VE1A04B4	1	57.37
15VE1A0494	1	58.63	16VE5A0419	4	53.96	15VE1A04A2	1	58.96	15VE1A04B6	7	47.67
15VE1A0480	7	50.24	15VE1A04A1	7	47.76	15VE1A0498	3	42.87	15VE1A0463	7	49.82

B - 17	BL's	%
15VE1A0466	0	77.46

Dr. N. Murali Krishna
 Head of the Department
 Electronics & Communication Engineering
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Counselling Code
SRYS

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

2015 Batch IV B.Tech I Sem R15 ECE - C Mini-Project Batches

C - 1	BL's	%	C - 2	BL's	%	C - 3	BL's	%	C - 4	BL's	%
15VE1A04D6	0	78.21	15VE1A04E5	0	77.43	15VE1A04E9	0	72.21	15VE1A04C8	0	72.09
15VE1A04E8	1	65.49	15VE1A04F9	1	58.81	15VE1A04J0	1	66.75	16VE5A0409	1	60.04
16VE5A0425	1	57.4	15VE1A04C4	2	58.3	15VE1A04F8	2	59.34	16VE5A0424	3	54.98
15VE1A04G1	4	54.45	15VE1A04G9	4	54.06	16VE5A0433	4	53.45	15VE1A04D5	5	54.12

C - 5	BL's	%	C - 6	BL's	%	C - 7	BL's	%	C - 8	BL's	%
15VE1A04G6	0	69.16	15VE1A04F5	0	68.06	15VE1A04G5	0	67.25	15VE1A04F2	0	66.81
15VE1A04E4	1	58.72	15VE1A04E0	1	66.42	15VE1A04D8	1	62.45	16VE5A0428	0	56.64
15VE1A04C3	4	53.46	15VE1A04C6	4	54.42	15VE1A04D2	4	54.51	15VE1A04H3	2	66.66
16VE5A0436	9	49.79	15VE1A04C1	10	45.91	15VE1A04E1	12	43.58	15VE1A04G4	12	43.07

C - 9	BL's	%	C - 10	BL's	%	C - 11	BL's	%	C - 12	BL's	%
15VE1A04F1	0	66.45	15VE1A04F4	0	65.76	16VE5A0434	0	65.32	16VE5A0429	0	63.74
15VE1A04D3	0	61.01	15VE1A04C2	0	60.72	16VE5A0426	0	59.19	16VE5A0432	0	58.81
15VE1A04D9	3	53.73	15VE1A04F0	3	55.28	15VE1A04F3	3	54.45	15VE1A04H0	3	54.03
15VE1A04H6	8	51.43	15VE1A04F6	18	34.99	15VE1A04G2	7	53.37	15VE1A04C5	7	48.81

C - 13	BL's	%	C - 14	BL's	%	C - 15	BL's	%	C - 16	BL's	%
16VE5A0427	0	63.45	16VE5A0404	0	63.02	15VE1A04G3	0	62.87	15VE1A04H7	0	62.69
15VE1A04H5	0	58.57	15VE1A04C7	0	57.7	15VE1A04G0	0	56.96	15VE1A04G7	0	61.76
15VE1A04D4	3	51.34	15VE1A04D1	3	61.94	14VE1A04H6	3	41.47	16VE5A0430	2	57.49
14VE1A04E9	7	28.8	15VE1A04H1	6	53.88	15VE1A04G8	6	49.61	15VE1A04E6	6	53.76

C - 17	BL's	%	C - 18	BL's	%	C - 19	BL's	%	C - 20	BL's	%
15VE1A04E2	0	62.66	16VE5A0431	0	62.51	15VE1A04C9	0	62.03			
15VE1A04H9	2	58.06	15VE1A04H8	2	57.7	15VE1A04F7	0	61.88			
15VE1A04D0	6	52.03	15VE1A04H2	5	57.34	15VE1A04E3	19	36.51			

Dr. N. Mujahid Krishna
 Head of the Department
 Department of Engineering
 Electronics & Communication Technologies
 Sreyas Institute of Engineering and Technology
 Beside Indu Aranya, Nagole, Hyderabad-500 068.



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

2015 Batch IV B.Tech I Sem R15 ECE - D Mini-Project Batches

D - 1	BL's	%	D - 2	BL's	%	D - 3	BL's	%	D - 4	BL's	%
16VE1A0437	0	79.66	15VE1A04K3	1	69.49	15VE1A04N2	0	62.75	15VE1A04L1	0	63.88
15VE1A04M0	0	63.55	15VE1A04J6	2	54.24	15VE1A04P1	0	57.85	15VE1A04L0	1	63.07
16VE5A0448	1	61.62	15VE1A04L9	5	50.9	15VE1A04J9	2	60.15	15VE1A04K5	2	55.52
15VE1A04N3	18	34.51				15VE1A04P0	12	39.79	15VE1A04M5	5	50.87

D - 5	BL's	%	D - 6	BL's	%	D - 7	BL's	%	D - 8	BL's	%
15VE1A04J8	0	71.34	15VE1A04J5	0	70.9	15VE1A04K1	0	69.7	15VE1A04J7	0	67.76
15VE1A04N8	0	62.69	15VE1A04L2	1	67.04	15VE1A04N9	0	62.63	15VE1A04L7	1	67.19
15VE1A04K7	2	57.82	15VE1A04L3	2	56.21	15VE1A04L6	2	53.25	15VE1A04M2	2	56.75
15VE1A04L4	12	42.18	15VE1A04N7	5	54.15	15VE1A04N6	11	45.31	15VE1A04P9	5	49.55

D - 9	BL's	%	D - 10	BL's	%	D - 11	BL's	%	D - 12	BL's	%
15VE1A04M1	0	66.9	15VE1A04N4	0	66.72	15VE1A04K4	0	66.06	15VE1A04M8	0	65.7
16VE5A0441	2	53.91	15VE1A04M6	1	58.57	15VE1A04Q0	0	61.25	16VE5A0440	1	58.09
16VE5A0412	4	54.04	16VE5A0444	2	56.72	16VE5A0447	2	52.47	15VE1A04M4	3	57.37
15VE1A04L5	10	48.18	14VE1A04P8	6	30.53	15VE1A04P3	9	48.42	15VE1A04K2	6	52.09

D - 13	BL's	%	D - 14	BL's	%	D - 15	BL's	%	D - 16	BL's	%
15VE1A04M3	0	65.37				16VE5A0438	0	64.85	15VE1A04K0	0	64.84
15VE1A04N5	0	60.51	16VE5A0443	1	58.34	15VE1A04M7	0	59.97	16VE5A0445	1	57.66
15VE1A04M9	3	54.51	15VE1A04N0	3	55.82	16VE5A0446	3	51.15	15VE1A04K6	4	53.94
15VE1A04N1	9	52.27	15VE1A04J1	7	53.97	15VE1A04J2	9	46.39	15VE1A04P6	8	46.3

D - 17	BL's	%	D - 18	BL's	%	D - 19	BL's	%	D - 20	BL's	%
16VE5A0442	0	64.51	15VE1A04P2	0	75.85	16VE5A0439	0	77.45			
15VE1A04P4	0	59.13	15VE1A04K8	0	73.7						
15VE1A04K9	4	50.69	15VE1A04P7	0	65.31						
16VE5A0406	8	49.11	15VE1A04J4	0	62.57						

Dr. N. Murali Krishna

Head of the Department
Electronics & Communication Engineering

Sreyas Institute of Engineering and Technology,
Beside Indu Aranya, Nagole, Hyderabad-500 068.



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

2015 Batch IV B.Tech I Sem R15 ECE - A Mini-Project Titles & Guides

Batch No	Roll No	Name of the Student	Title of the Project	Internal Guide	Location of the Project	External Guide
A - 1	15VE1A0401	A Sony	Detection of Dental carries based on digital image processing and Arduino system	Dr. Sandeep Kumar	IETE	N.Sridhar R&D Engineer
	15VE1A0450	S Abhilash Goud				
	15VE1A0447	Pothuganti Divya Jhansi				
	15VE1A0448	R S P Shanmukhanath				
A - 2	15VE1A0402	Archana Bathula	An error notification UART serial communication module based on VHDL	Y. Satish Kumar	IETE	N.Sridhar R&D Engineer
	15VE1A0406	Ch Akash				
	15VE1A0452	Shravya Mandhapuram				
	15VE1A0405	B Krishna Mohan Goud				
A - 3	15VE1A0432	Malyala Srinija	An FPGA based Advanced Encryption standard Rijndael Algorithm for all Key Lengths	Ch. S. V. Maruthi Rao	IETE	N.Sridhar R&D Engineer
	15VE1A0424	K Revanth Yadav				
	16VE5A0402	Andhe Shivakumar				
	15VE1A0437	Murari Kumar Agarwal				
A - 4	16VE5A0411	Boga Dileep	Drunken Driving Avoidance System	V. Taj Kiran	NSIC	G.Jayavarman
	15VE1A0409	Ch Varshitha Reddy				
	16VE5A0403	Anipindi Durgaprasad				
	15VE1A0456	Veena Chitkuri				
A - 5	16VE5A0410	Beigum Sai Phanindra	Design and Configuration of Virtual Local Area Networks	B. Sreenivasu	BSNL	A.Vijay kumar
	15VE1A0440	Neela Sai Rohith				
	16VE5A0408	B Dattatreya Suresh				
	15VE1A0454	S Sandeep Goud				
A - 6	15VE1A0441	Nelanti Mounika	Brain Tumor Classification using back propagation neural network	Dr. Murali Krishna	IETE	N.Sridhar R&D Engineer
	15VE1A0444	Pasham Likhitha				
	15VE1A0418	Jabishetti Shrivani				
	15VE1A0411	Dasadi Akhilandeshwari				
A - 7	15VE1A0413	Didugu Bhavani	FPGA implementation of Convolution encoder and viterbi decoder	G. Ramachandra Kumar	IETE	N.Sridhar R&D Engineer
	15VE1A0445	Pasupunoori Ganesh				
	15VE1A0434	Maragani Nagaraju				
	15VE1A0428	Koppolu Kavyasri				





A - 8	15VE1A0459	Voruganti Sai Kumar	Implementation of VOIP on Wi-Fi backbone	G. Vijay Goud	BSNL	T.Ramesh Chary
	15VE1A0442	Oruganti Kusuma Priya				
	15VE1A0458	Vinayak				
	15VE1A0433	Mamidi Sanghavi				
A - 9	15VE1A0420	Juluri Sharath Kumar	Design and Implementation of error detection and correction using BCH codes	Dr. John Moses	IETE	N.Sridhar R&D Engineer
	15VE1A0451	Sandhi Pravalika				
	15VE1A0408	Cheruku Prahasith Reddy				
	15VE1A0438	Murki Shashank				
A - 10	15VE1A0412	Deshmukh Kavya	Arduino based four legged robot control using bluetooth	A. Sowjanya	IETE	K.Venkanna R&D Engineer
	15VE1A0414	Edunoori Sathna				
	15VE1A0410	D Sri Sai Satyanarayana				
	15VE1A0426	Kolan Sai Anvesh Reddy				
A - 11	15VE1A0439	Nama Shardhul Kumar	Design and Implementation of IOT based Smart Health care monitoring system	B. Spandana	ECIL	Dr.T.Thirumalai DGM
	15VE1A0421	K H V Durga Sai Jahnavi				
	15VE1A0427	Konakalla Sandeep				
	15VE1A0430	Malchalam Vijay Kumar				
A - 12	15VE1A0416	G Anirudh Reddy	Robotic Vehicle controlled by hand gesture	D. Bindu	IETE	K.Venkanna R&D Engineer
	15VE1A0404	Bheemidi Pranitha				
	15VE1A0429	M Sathwikotham				
	15VE1A0407	Ch S Srinivas				
A - 13	15VE1A0425	Keravelly Sowjanya	Biometric Based Security System	Sk. Md. Rafi	ECIL	Dr.T.Thirumalai DGM
	15VE1A0431	M Shailaja Reddy				
	15VE1A0446	Polishetti Sravan Kumar				
	16VE5A0407	Asthapuram Srinivas				
A - 14	15VE1A0436	Mudhagonda Bharathi	A Tactical information of unmanned vehicles using vehiculat ADHOC networks	G. D. Basavaraj	IETE	N.Sridhar R&D Engineer
	15VE1A0453	Soma Bhavana				
	15VE1A0449	Rai Hemalatha				
	15VE1A0415	G Harshini				
A - 15	15VE1A0403	B Aditya Hruday	Design implementaion of 32-bit RISC CPU based on MIPS on FPGA	C. Yamuna Reddy	IETE	N.Sridhar R&D Engineer
	15VE1A0423	Kandula Tarun Teja				
	15VE1A0455	Tummala Subha Sri				
	15VE1A0419	Jilla Nikhil Kumar				

Dr. N. Muraji Krishna

Head of the Department
Electronics & Communicing Engineering
Sreyas Institute of Engineering and Technologies
Beside Indu Aranya, Nagole, Hyderabad-500 068.



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

2015 Batch IV B.Tech I Sem RI5 ECE - B Mini-Project Titles & Guides

Batch No	Roll No	Name of the Student	Title of the Project	Internal Guide	Location of the Project	External Guide
B - 1	15VE1A0474	Dayanand Sai Dikshit	SRAM Memory Controller with AHB Interface	G. Ramachandra Kumar	RCI	C.Ramesh Reddy Scientist 'F'
	15VE1A0495	Navya Anumula	Wireless Notice board using RF	K. Mahesh	IETE	B.Venkanna R&D Engineer
B - 2	15VE1A04A4	Ragiphani Vamshi				
	15VE1A0483	K Shrivaya Chandra	IOT and RFID based E-Passport system using Arduino	B. Amarender Reddy	ECIL	Dr.T.Thirumalai DGM
B - 3	15VE1A04B2	Syed Ahmed Alisha				
	15VE1A0467	B Sai Anirudh	4D Movie Chair	Hemalatha Dalmia	IETE	Raviteja R&D Engineer
B - 4	15VE1A0487	K Vamshi Reddy				
	15VE1A0484	K Sai Priya Reddy	GPRS based SCADA implementation for multisensory security with SMS alerts	A. Praveena	ECIL	Vijay Chandra
B - 5	15VE1A0497	P Chandra Shekar				
	15VE1A04A3	Rachakonda Rashi	Realization of FIR Filters using Matlab	A. Praveena	BEL	B.Vinatha DGM
B - 6	15VE1A0496	Neha Pande				
	15VE1A0492	Mantri Lalit Kumar	Design and Simulation of Band Pass Filter for X-Band Radar	J. Panduranga Rao	RCI	M.Veerendra Scientist 'C'
B - 7	15VE1A04B0	S H R K Akhil				
	15VE1A04B9	Varala Ravali Devi	Aqua Smart Drone with automatic fishing technique	Neha Anjum	IETE	Ch.Raviteja R&D Engineer
B - 8	15VE1A04A6	S Pavan Kalyan				
	15VE1A0482	Kakkireni Pranavi				
15VE1A04A9	Shamala Karthik					
15VE1A04A5	Rangineni Sai Sri					
16VE5A0421	K Tejaswini					
16VE5A0416	G Vinay Kumar					
16VE5A0422	Kalluri Mounika					
15VE1A04B5	Thadakamalla Akhil					
15VE1A0468	Batharaju Rajani					
15VE1A04B8	Vadla Mounika					
15VE1A0461	Adla Sahithi Reddy					
16VE5A0417	G Ajay Kumar					
16VE5A0414	Dumpala Sai Prasad					
16VE5A0415	Gajam Srikanth					



15VE1A04B1	S V Surya Deekshit	Implementation of Aadhar card based EVM	K. Sonali Swaroop	IETE	Ch.Raviteja R&D Engineer
15VE1A04A0	P Leela Krishnaveni				
15VE1A04A8	Sama Shreya Reddy				
15VE1A0489	K Sai Rama Krishna				
15VE1A0476	Golla Deeksha				
15VE1A0490	K Greeshma Reddy	PID design of Position Servo Controller for Missile Applications	Ch. S. V. Maruthi Rao	RCI	B.Jawaharlal Scientist
15VE1A0462	A I Manasa Reddy				
15VE1A0473	Chintala Srinivas				
15VE1A0470	C J Shashank				
15VE1A0477	G Sanath Kumar Reddy				
15VE1A0499	Pisike Sai Kiran	Fire Detection Robot using ARM7	T. Suman	ECIL	Teja & N.Ekambaram
15VE1A0488	K Naga Sushma				
15VE1A0485	Kashojjala Sindhuja				
15VE1A0491	M Navneeth Kumar				
15VE1A04B7	Vaddapelly Sai Krishna				
14VE1A0420	G NandhaKumar Charya	Smart Eco Car	Y. Sravan Kumar	IETE	B.Venkanna R&D Engineer
15VE1A0493	Mugithi Pravalika				
16VE5A0418	Gujja Sai Kumar				
15VE1A0494	Nandhyala Shilpa				
15VE1A0480	Jonnadula Mounisha				
15VE1A0465	B Priyanka	Satellite Technology for Advanced Railway signalling Arduino	K. Narsimha	IETE	N.Sridhar R&D Engineer
16VE5A0423	Katari Ganesh				
15VE1A04A7	Salagrama Sai Teja				
16VE5A0419	Gutti Satyam				
15VE1A04A1	R Hareesh Kumar				
15VE1A04B3	T Bhavani	Bomb Disposal Robot	V. Swathi	IETE	B.Venkanna R&D Engineer
15VE1A0486	Kasireddy Sowmya				
15VE1A04A2	R Sai Kumar Reddy				
15VE1A0498	Peapala Nikhil				
15VE1A0471	Challa Priya				
15VE1A04B4	T Vineeth Kumar Reddy	Design and Simulation of Directional Coupler for X-Band Radar	Dr. V. A. Sankar Ponnappalli	RCI	K.Balajyothi Scientist
15VE1A04B6	T Meghavardan Reddy				
15VE1A0463	Amradhi Rakesh Reddy				
15VE1A0466	Baddam Harshitha				
15VE1A0464	Anthathi Keerthana				
B - 9		Windmill Power generation	Brahmaiah. Sk	IETE	B.Venkanna R&D Engineer
B - 10					
B - 11					
B - 12					
B - 13					
B - 14					
B - 15					
B - 16					
B - 17					
B - 18					



Dr. N. Murali Krishna
Head of the Department
Electronics & Communication Engineering
Sreyas Institute of Engineering and Technology
Beside Indu Aranya, Nagole, Hyderabad-500 068.

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
2015 Batch IV B.Tech I Sem R15 ECE - C Mini-Project Titles & Guides

Batch No	Roll No	Name of the Student	Title of the Project	Internal Guide	Location of the Project	External Guide
C - 1	15VE1A04D6	Janagam Srilekha	RFID based Access Control using Arduino	D. Bindu	NSIC	Jayavardhan Professor
	15VE1A04E8	Mallela Abhigna				
	16VE5A0425	M Kalanjali				
C - 2	15VE1A04G1	N Abhiram Teja	Signle Chip FM Radio	M. Divya	NSIC	Jayavardhan Professor
	15VE1A04E5	Lenkala Akhila Reddy				
	15VE1A04F9	Narsing Lokesh				
C - 3	15VE1A04C4	Bolavoni Bhima	Wireless data Encryption and Decryption	B. Spandana	NSIC	Jayavardhan Professor
	15VE1A04G9	R Saikrishna				
	15VE1A04E9	Mamidi Tejaswini				
C - 4	15VE1A04J0	V Nikhil Chakravarthi	Smart Helmet	A. Sowjanya	IETE	B.Venkanna R&D Engineer
	15VE1A04F8	Naalla Srilekha				
	16VE5A0433	M Bhuvaneshwari				
C - 5	15VE1A04C8	E Rachana Reddy	IOT enabled Wireless sensor network	A. Praveena	ECIL	Dr.T.Thirumalai DGM
	16VE5A0409	Bantu Naresh				
	16VE5A0424	Korra Ashok				
C - 6	15VE1A04D5	Jaminipiyata Srinidhi	Broken Railway track detection system through wireless network	G. D. Basavaraj	NSIC	Jayavardhan Professor
	15VE1A04G6	P Hrishikesh Kaushik				
	15VE1A04E4	K DurgakameshwarRao				
C - 7	15VE1A04C3	B HarshavardhanReddy	Wireless Heart Rate monitoring system using RF Technologies	Y. Satish Kumar	NSIC	Jayavardhan Professor
	16VE5A0436	Nichakolla Bheemesh				
	15VE1A04F5	M Harshith Netha				
C - 8	15VE1A04E0	Karipe Samyuktha	RF Controlled Spy Robot with Night Vision Camera	C. Yamuna Reddy	IETE	B.Venkanna R&D Engineer
	15VE1A04C6	Deshidi Rajapriyanka				
	15VE1A04C1	Akoju Charanchandu				
C - 9	15VE1A04G5	Pasupuleti Manisha	Safety device for unmanned level crossing	Neha Anjum	NSIC	Jayavardhan Professor
	15VE1A04D8	M Keertana Kaushik				
	15VE1A04D2	G G Kavi Chandra				
C - 10	15VE1A04E1	Kasa Madhukanth	Investigate the Code through mobile device for opening a Secret	M/ Tsi Vivan	NSIC	Jayavardhan
	15VE1A04F2	Mendu Saketh Reddy				
	16VE5A0428	Makam Yugandhar				





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Batch No	Roll No	Name of the Student	Title of the Project	Internal Guide	Location of the Project	External Guide
D - 1	16VE5A0437	P Sampath Reddy	Touch Screen and Zigbee based restaurant ordering system	G. Ramachandra Kumar	IETE	K.Venkanna R&D Engineer
	15VE1A04M0	Naroju Shiva Kumar				
	16VE5A0448	Yerakala Sai Kumar				
D - 2	15VE1A04N3	S Saailesh	Integrated Mine Safety Monitoring & Alerting System	K. Mahesh	IETE	
	15VE1A04K3	G Nandanikhil				
	15VE1A04J6	Chittepu Prashanthi				
D - 3	15VE1A04L9	M Kavya Sai Madhavi	GPS + GSM based advanced vehicle tracking system	G. Vijay Goud	IETE	B.Venkanna R&D Engineer
	15VE1A04N2	Roshni Chawla				
	15VE1A04P1	T M Vamshi Krishna				
D - 4	15VE1A04J9	D Gnaneshwari	Raspberry pie controlled SMS update notification (SUN) system	K. Mahesh	IETE	B.Venkanna R&D Engineer
	15VE1A04P0	S Rohith Kumar Goud				
	15VE1A04L1	K Hamilton Sudhakar				
D - 5	15VE1A04L0	Katagouni Praveena	Smart Hoverboard with automatic and manual transmission	C. Yamuna Reddy	IETE	Ch.Raviteja R&D Engineer
	15VE1A04K5	G Manikanteshwar				
	15VE1A04M5	Panthula Saikiran				
D - 6	15VE1A04J8	Devkathie Bhavani	Design and Development of Embedded Live and Motion detection system in Military Applications	K. Sonali Swaroop	IETE	B.Venkanna R&D Engineer
	15VE1A04N8	Somanaboina Rakesh				
	15VE1A04K7	Gunda Sai Tarun				
D - 7	15VE1A04L4	Kondrikala Uftej	Automatic Temperature Control in Vacuum brazing furnace using PLC	Sk. Md. Rafi	BHEL	D.Venkataramana
	15VE1A04J5	CV L P Sai Mounica				
	15VE1A04L2	K Lakshmi Prasanna				
D - 8	15VE1A04L3	Keetha Avinash	Density based Traffic signal system using Arduino	V. Swathi	IETE	Ch.Raviteja
	15VE1A04N7	S Krishna Sai				
	15VE1A04K1	E Sowmya				
n - o	15VE1A04N9	S Sai Chaitanya	Automatic Temperature controlling of a furnace using cascade control	V Sravan Kumar	BHEL	
	15VE1A04L6	K Devendar Reddy				
	15VE1A04N6	S Nikhilar Reddy				



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Sl. No.	Name of the Candidate	Project Title	Supervisor Name	Institution	Grade
D - 10	16VE5A0412	Byroji Bhavana			D.Venkataramana
	15VE1A0415	K Harish Kumar			Ch.Raviteja R&D Engineer
	15VE1A04N4	Samala Harish			
	15VE1A04M6	P Supriya			
D - 11	16VE5A0444	S Rakesh			
	14VE1A04P8	V Hari Haran Reddy			
	15VE1A04K4	G Sowjanya			
	15VE1A04Q0	V Chandan Swaraj			
	16VE5A0447	V Chandrashekar			
D - 12	15VE1A04P3	T Saketh			
	15VE1A04M8	Peravalli Rohini			
	16VE5A0440	Prasad Aravind			
	15VE1A04M4	Panakanti Divya			
D - 13	15VE1A04K2	G Siddhartha			
	15VE1A04M3	Palakonda Sanjana			
	15VE1A04N5	S Meghana Reddy			
	15VE1A04M9	Podduturi Anish			
	15VE1A04N1	Rapaka Abhishek			
D - 14	16VE5A0443	Srigada Rahul			
	15VE1A04N0	Poleboina Shruthi			
	15VE1A04J1	A Hemanth Reddy			
D - 15	16VE5A0438	Pandiri Vennela			
	15VE1A04M7	Patakoti Rishika			
	16VE5A0446	T Pravalika			
	15VE1A04J2	A Vishal Reddy			
D - 16	15VE1A04K0	Dornala Harshini			
	16VE5A0445	T Yugandar			
	15VE1A04K6	G V Rithika Pillay			
D - 17	15VE1A04P6	Thati Vinay Kumar			
	16VE5A0442	Repaka Vineeth			
	15VE1A04P4	T Sai Deekshitha			
	15VE1A04K9	Kadaveru Akhilesh			
	16VE5A0406	Arutla Manisha			
D - 18	15VE1A04P2	T Mounika Reddy			
	15VE1A04K8	K Vishnu Murthy Bhat			
	15VE1A04P7	T Ramyatha Reddy			
	15VE1A04J4	Challagundla Omsri			
D - 19	16VE5A0439	Polishetti Divya			



Dr. N. S. Murali Krishna
 Head, Department
 of Communication Engineering
 Jayas Institute of Engineering and Technologies
 Beside Indu Aranya, Nagole, Hyderabad-500 068.



No. : 3329

The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 07.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. A. Sony	15VE1A0401	ECE	SRYS
2. S. Abhilash Goud	15VE1A0450	ECE	SRYS
3. P. Divya Jhansi	15VE1A0447	ECE	SRYS
4. R. S.P.Shanmukhanath	15VE1A0448	ECE	SRYS

Project Title : Detection of dental caries based on digital image processing & Arduino system

Project Guide : N. Sreedhar

Duration : 1 Month

K. Tejassu
(R&D Engineer)





No. : 3311

The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 03.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. B. Archana	15VE1A0402	ECE	SRYS
2. Ch. Akash	15VE1A0406	ECE	SRYS
3. M. Shravya	15VE1A0452	ECE	SRYS
3. B. Krishna Mohan Goud	15VE1A0405	ECE	SRYS

Project Title : An error notification UART serial communication module based on VHDL

Project Guide : N. Sreedhar

Duration : 1 Month

Srujana
(R&D Engineer)





No. : 3365

The Institution of Electronics and Telecommunication Engineers
Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 23.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. M. Srinija	15VE1A0432	ECE	SRYS
2. K. Revanth	15VE1A0424	ECE	SRYS
3. A. Shiva Kumar	16VE5A0402	ECE	SRYS
4. Murari Agarwal	15VE1A0437	ECE	SRYS

Project Title : An FPGA based advanced encryption standard-rijndael algorithm for all key lengths

Project Guide : N. Sreedhar

Duration : 1 Month

Srijana
(R&D Engineer)





No. : 3330

The Institution of Electronics and Telecommunication Engineers
Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 07.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. N. Mounika	15VE1A0441	ECE	SRYS
2. P. Likhitha	15VE1A0444	ECE	SRYS
3. J. Sravani	15VE1A0418	ECE	SRYS
4. D. Akhilandeshwari	15VE1A0411	ECE	SRYS

Project Title : Brain tumor classification using back propagation neural network

Project Guide : N. Sreedhar

Duration : 1 Month

K. Tejali
(R&D Engineer)





No. : **3348**

The Institution of Electronics and Telecommunication Engineers
Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

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www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 09.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. D. Bhavani	15VE1A0413	ECE	SRYS
2. P. Ganesh	15VE1A0445	ECE	SRYS
3. M. Nagaraju	15VE1A0434	ECE	SRYS
3. K. Kavyasri	15VE1A0428	ECE	SRYS

Project Title : FPGA implementation of convolutional encoder and viterbi decoder

Project Guide : N. Sreedhar

Duration : 1 Month

Srujana
(R&D Engineer)



Office of the Principal,
Regional Telecom Training Centre,
An ISO 9001:2008 Certified Institution
Gachibowli, Hyd-32. Ph.No:040-23000221 / 232.
www.rttchyd.bsnl.co.in, email: rttchyd@bsnl.co.in
Lr.No: RTTC-HYD/BD/Mini-Prj/Corr/Appr/2018/01



भारत संचार निगम लिमिटेड
(भारत सरकार का उपक्रम)
BHARAT SANCHAR NIGAM LIMITED
(A Govt. of India Enterprise)

Dated at Hyderabad the 08-05-2018

To
The Professor & Head of the Department,
Department of Electronics & Communication Engineering (ECE),
Sreyas Institute of Engineering & Technology,
Beside Indu aranya, Nagole,
Hyd -500 068.

Sir,

Sub: Approval to accommodate students for **B.Tech Mini Project work** at **RTTC-Hyderabad** –Reg.

Ref: Your letter regarding permission of students for Mini Project Work –Reg

* * * * *

It is to inform you that the following bonafide students of your college have registered and permitted for pursuing the B.Tech Mini Project work at Regional Telecom Training Centre –Hyd., for the batch commenced from 07-05-2018. The details are furnished below.

The Title of the project is “Implementation of VOIP on WIFI backbone”.

- | | | |
|------------------------|--------------|---------------------|
| 1. Mr. V. Sai Kumar | B.Tech (ECE) | Roll No: 15VE1A0459 |
| 2. Mr. Vinayak | B.Tech (ECE) | Roll No: 15VE1A0458 |
| 3. Ms. Mamidi Sanghavi | B.Tech (ECE) | Roll No: 15VE1A0433 |
| 4. Ms. O. Kusuma Priya | B.Tech (ECE) | Roll No: 15VE1A0442 |

This is for your information and necessary action please.

Divisional Engineer

O/o Principal, RTTC, Hyderabad-32

DIVISIONAL ENGINEER (Trans & Swg)

इलेक्ट्रिकल प्रशासन एवं संचार विभाग

O/o SENIOR PRINCIPAL

श्री. डी. ए. के. ए. ए. श्री. इंद्रप्रकाश - 32

R.T.T.C., B.S.N.L., HYD - 32

Copy to:

Above Student / Students for information please.





No. : 3309

The Institution of Electronics and Telecommunication Engineers Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 02.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. J. Sharath Kumar	15VE1A0420	ECE	SRYS
2. C. Prahasith Reddy	15VE1A0408	ECE	SRYS
3. M. Shashank	15VE1A0438	ECE	SRYS
3. S. Pravalika	15VE1A0451	ECE	SRYS

Project Title : Design and Implementation of error detection and correction using BCH codes

Project Guide : N. Sreedhar

Duration : 1 Month

Srujana

(R&D Engineer)





No. : 3336

The Institution of Electronics and Telecommunication Engineers
Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 08.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. Deshmukh Kavya	15VE1A0412	ECE	SRYS
2. Edunoori Sathna	15VE1A0414	ECE	SRYS
3. Damaraju Sri Sai Satyanarayana	15VE1A0410	ECE	SRYS
4. Kolan Sai Anvesh Reddy	15VE1A0426	ECE	SRYS

Project Title : Arduino based four legged robot control using bluetooth

Project Guide : B.Venkanna

Duration : 1 Month

Srujana

(R&D Engineer)





No. : 3315

The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 04.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. G. Anirudh Reddy	15VE1A0416	ECE	SRYS
2. B. Pranitha	15VE1A0404	ECE	SRYS
3. M. Sathwikotham	15VE1A0429	ECE	SRYS
4. Ch.S. Srinivas	15VE1A0407	ECE	SRYS

Project Title : Robotic vehicle controlled by hand gesture

Project Guide : B.Venkanna

Duration : 1 Month

Srujana
(R&D Engineer)





इलेक्ट्रॉनिक्स कारपोरेशन आफ इण्डिया लिमिटेड

Electronics Corporation of India Limited

CIN U32100TG1967GOI001149

(भारत सरकार का उद्यम) / (A Govt. of India Enterprise)
कम्प्यूटर शिक्षा प्रभाग / COMPUTER EDUCATION DIVISION

ECIT



ISO 9001:2008

Date: 11/05/2018.

To,
Head of the Department,
Sreyas Institute Of Engineering And Technology,
Hyderabad.

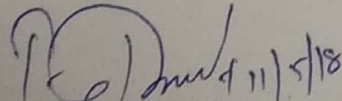
Dear Sir,

Ms. K. SOWJANYA (15VE1A0425), Ms. M. SHAILAJA (15VE1A0431),
Mr. P. SRAVAN KUMAR (15VE1A0446) & Mr. A. SRINIVAS (16VE5A0407)
students of SREYAS INSTITUTE OF ENGINEERING AND TECHNOLOGY are
assigned to do a Internship / mini project in "EMBEDDED SYSTEMS" in
Computer Education Division, ECIL for a period of one month as a part of their
Internship / Mini project fulfillment for the award of the Engineering Degree
certificate.

Thanking You,

Yours truly,

For ELECTRONICS CORPORATION OF INDIA LIMITED


T. THIRUMALAI

DGM: CED
Dr. T. THIRUMALAI
DGM, CED, ECIL,
HYDERABAD-500 062



अतिथि गृह काम्प्लेक्स, ई सी आई एल, हैदराबाद, तेलंगाना, भारत.
Guest House Complex, ECIL, Hyderabad - 500 062. T.S., INDIA.
दूरभाष / Tel. 2712 5864, 2712 2816, टेली फैक्स / Tele Fax : +91-040-2712 6017
वेब साइट / Web Site : www.ecil.co.in, ई-मेल / E-mail : headced@ecil.co.in



No. : 3351

The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org
www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 10.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. M. Bharathi	15VE1A0436	ECE	SRYS
2. S. Bhavana	15VE1A0453	ECE	SRYS
3. R. Hemalatha	15VE1A0449	ECE	SRYS
4. G. Harshini	15VE1A0415	ECE	SRYS

Project Title : A tactical information of unmanned vehicles using vehicular ADHOC networks

Project Guide : N. Sreedhar

Duration : 1 Month

Srujana
(R&D Engineer)



No. : 3346



The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org
www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 08.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. B. Adithya Hruday	15VE1A0403	ECE	SRYS
2. K. Tarun Teja	15VE1A0423	ECE	SRYS
3. T. Subha Sri	15VE1A0455	ECE	SRYS
3. J. Nikhil Kumar	15VE1A0419	ECE	SRYS

Project Title : Design 32-bit RISC CPU based on MIPS based on FPGA

Project Guide : N. Sreedhar

Duration : 1 Month

Srujana
(R&D Engineer)



No. : 3347 B2



The Institution of Electronics and Telecommunication Engineers Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 08.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. Navya Anumula	15VE1A0495	ECE	SRYS
2. Ragiphani Vamshi	15VE1A04A4	ECE	SRYS
3. Kandagatla Shravya Chandra	15VE1A0483	ECE	SRYS
4. Syed Ahmed Alisha	15VE1A04B2	ECE	SRYS

Project Title : Wireless notice board using RF

Project Guide : B.Venkanna

Duration : 1 Month

Srujana
(R&D Engineer)





इलेक्ट्रॉनिक्स कॉर्पोरेशन ऑफ इण्डिया लिमिटेड
Electronics Corporation of India Limited

CIN U32100TG1967GOI001149

(भारत सरकार का उद्यम) / (A Govt. of India Enterprise)
कम्प्यूटर शिक्षा प्रभाग / COMPUTER EDUCATION DIVISION



ISO 9001:2008

Date: 05/05/2018.

To,
Head of the Department,
Sreyas institute of engineering and technology,
Hyderabad.

Dear Sir,

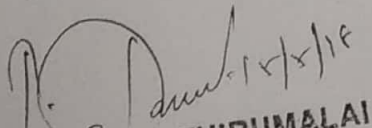
Mr. B. SAI ANIRUDH (15VE1A0467), Ms. K. SAI PRIYA REDDY (15VE1A0484), Mr. K. VAMSI REDDY (15VE1A0487) & Mr. P. CHANDRA SHEKAR (15VE1A0497) students of Sreyas institute of engineering and technology are assigned to do a mini project in "EMBEDDED SYSTEMS" in Computer Education Division, ECIL for a period of one month (i.e 04/05/2018 to 04/06/2018) as a part of their project fulfillment for the award of the Engineering Degree certificate.

Project title:

Thanking You,

Yours truly,

For ELECTRONICS CORPORATION OF INDIA LIMITED


T. THIRUMALAI
DGM, CED, ECIL,
DGM: HYDERABAD-500 062

अतिथि गृह कॉम्प्लेक्स, ई सी आई एल, हैदराबाद, तेलंगाणा, भारत.
Guest House Complex, ECIL, Hyderabad - 500 062. T.S., INDIA.
दूरभाष / Tel. 2712 5864, 2712 2816, टेली फैक्स / Tele Fax : +91-040-2712 6017
वेब साइट / Web Site : www.ecil.co.in, ई-मेल / E-mail : headced@ecil.co.in

No. : 3358

The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org



PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 11.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. Rachakonda Rashi	15VE1A04A3	ECE	SRYS
2. Mantri Lalit Kumar	15VE1A0492	ECE	SRYS
3. Neha Pande	15VE1A0496	ECE	SRYS
4. Suravajhala HRK Akhil	15VE1A04B0	ECE	SRYS

Project Title : 4D Movie Chair

Project Guide : CH.V.Ravi Teja

Duration : 1 Month

Srujana
(R&D Engineer)





इलेक्ट्रॉनिक्स कारपोरेशन ऑफ इण्डिया लिमिटेड

Electronics Corporation of India Limited

CIN U32100TG1967GOI001149

(भारत सरकार का उद्यम) / (A Govt. of India Enterprise)

कम्प्यूटर शिक्षा प्रभाग / COMPUTER EDUCATION DIVISION

ECIT



ISO 9001:2008

Date: 09/05/2018.

To,
Head of the Department,
Sreyas institute of engineering and technology ,
Hyderabad.

Dear Sir,

Ms. V. RAVALI DEVI (15VE1A04B9), Ms. K. PRANAVI (15VE1A0482),
Mr. S. KARTHIK (15VE1A04A9) & Mr. S. PAVAN KALYAN
(15VE1A04A6) students of Sreyas institute of engineering and technology are
assigned to do a mini project in "EMBEDDED SYSTEMS" in Computer Education
Division, ECIL for a period of one month as a part of their project fulfillment for
the award of the Engineering Degree certificate.

Project title:

Thanking You,

Yours truly,

For ELECTRONICS CORPORATION OF INDIA LIMITED

Dr. Y. P. Malai
Dr. Y. P. MALAI
DGM, CED, ECIL,
HYDERABAD-500 062



अतिथि गृह कॉम्प्लेक्स, ई सी आई एल, हैदराबाद, तेलंगाणा, भारत.

Guest House Complex, ECIL, Hyderabad - 500 062. T.S., INDIA.

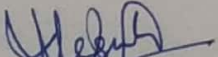
दूरभाष / Tel. 2712 5864, 2712 2816, टेली फैक्स / Tele Fax : +91-040-2712 6017

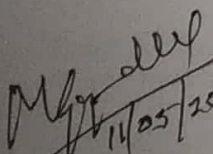
वेब साइट / Web Site : www.ecil.co.in, ई-मेल / E-mail : headced@ecil.co.in




NO.RCI/HRD/Project_work/415/Form/
Government of India, Ministry of Defence
Defence Research & Development Org (DRDO)
RESEARCH CENTRE IMARAT
Dr APJ Abdul Kalam Missile Complex
Vignyana Kancha, Hyderabad - 500 069
Phone No: 24306635/24306384
Fax No : 24306014
Email ID : Hrdg@rcilab.in

FORWARDING OF APPLICATION FOR PROJECT TRAINEES (STUDENTS)

1. Name of the Student (s) : A SAHITHI REDDY
B RAJANI
T AKHIL
2. Relationship with DRDO : V MOUNIKA 6114
RELATIVE OF SHRI M RAMBABU, TO 'A'
3. College / Institution / University : SREYAS INSTITUTE OF ENGG. & TECHNOLOGY
4. B.E /B.Tech/M.E/M.Tech : B.TECH
5. Branch : ELECTRONICS & COMMUNICATION ENGINEERING.
6. Duration of Training : From 14 MAY 2018 to 25 JUNE 2018.
7. Title of the Project : DESIGN & SIMULATION OF X-BAND FILTER.
8. Allotment of Directorate : DRSS
9. Guide Name & Designation : M VEERENDRA, SC 'C'
10. Guide Signature : 


11/05/2018


(Technology Director / Project Director)
बी.आर.के.रेड्डी / B.R.K.K. / Project Director
उत्कृष्ट वैज्ञानिक / Outstanding Scientist
तकनीकी निदेशक, डि आर एस एस / Technology Director, DRSS
अनुसंधान केन्द्र इमारत / Research Centre Imarat
डॉ ए पी जे अब्दुल कलाम प्रयोगशाला / Dr APJ Abdul Kalam Missile Complex
हैदराबाद / Hyderabad-500 069.



No. : ¹³⁸ 3339

The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 08.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. G. Ajay Kumar	16VE5A0417	ECE	SRYS
2. D. Sai Prasad	16VE5A0414	ECE	SRYS
3. G. Srikanth	16VE5A0415	ECE	SRYS
4. S. Deekshith	15VE1A04B1	ECE	SRYS

Project Title : Aqua smart drone with automatic fishing technique

Project Guide : CH.V.Ravi Teja

Duration : 1 Month

Srujana

(R&D Engineer)





B9

No. : 3344

The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

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PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 08.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. A. Keerthana	15VE1A0464	ECE	SRYS
2. P. Krishnaveni	15VE1A04A0	ECE	SRYS
3. S. Shreya Reddy	15VE1A04A8	ECE	SRYS
4. K. Sai Ramakrishna	15VE1A0489	ECE	SRYS

Project Title : Implementation of Aadhar card based EVM

Project Guide : N. Sreedhar

Duration : 1 Month

Srujana
(R&D Engineer)





RESEARCH CENTRE IMARAT
Dr. APJ Abdul Kalam Missile complex
Govt. of India, Ministry of Defence



Hyderabad
Date: 09th May 2018

To,
The Head of the department,
Sreyas institute of engineering and technology,
Nagole, Hyderabad.
Telangana.

Sub:- Mini project - internship for 4th B.Tech 1st semester (Regular)-regarding.
Title:- 'PID Design of Position Servo controller for missile application'.

Sir,

I'm here by accepting the following students for the mini project with the title mentioned above.

s.no	Name of the student	Roll no
1	G.Deeksha	15VE1A0476
2	k.Greeshma reddy	15VE1A0490
3	A.l.manasa reddy	15VE1A0462
4	Ch.srinivas	15VE1A0473

B. Jawaharlal

Scientist

Control System Lab(CSL)

Research Centre Imarat

Ministry of Defence, DRDO

Vigyan Kanchara.

Pin: 500069.

B. JAWAHARLAL
SCIENTIST
RCI, DRDO
MINISTRY OF DEFENCE
Vigyan Kanchara, Hyderabad-69.



इलेक्ट्रॉनिक्स कारपोरेशन आफ इण्डिया लिमिटेड
Electronics Corporation of India Limited

CIN U32100TG1967GOI001149

(भारत सरकार का उद्यम) / (A Govt. of India Enterprise)
कम्प्यूटर शिक्षा प्रभाग / COMPUTER EDUCATION DIVISION

B-11

ECIT



ISO 9001:2008

Date: 05/05/2018.

To,
Head of the Department,
Sreyas institute of engineering and technology,
Hyderabad.

Dear Sir,

Mr. C. J. SHASHANK (15VE1A0470), Mr. G. SANATH KUMAR REDDY (15VE1A0477), Ms. K. NAGA SUSHMA (15VE1A0488) & Mr. P. SAI KIRAN (15VE1A0499) students of Sreyas institute of engineering and technology are assigned to do a mini project in "EMBEDDED SYSTEMS" in Computer Education Division, ECIL for a period of one month (i.e 04/05/2018 to 04/06/2018) as a part of their project fulfillment for the award of the Engineering Degree certificate.

Project title:

Thanking You,

Yours truly,

For ELECTRONICS CORPORATION OF INDIA LIMITED


T. THIRUMALAI

DGM: CED

Dr. T. THIRUMALAI
DGM, CED, ECIL,
HYDERABAD-500 062



अतिथि गृह कॉम्प्लेक्स, ई सी आई एल, हैदराबाद, तेलंगाणा, भारत.
Guest House Complex, ECIL, Hyderabad - 500 062. T.S., INDIA.
दूरभाष / Tel. 2712 5864, 2712 2816, टेली फैक्स / Tele Fax : +91-040-2712 6017
वेब साइट / Web Site : www.ecil.co.in, ई-मेल / E-mail : headced@ecil.co.in



B-12 No. : 3331

The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 07.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. K. Sindhuja	15VE1A0485	ECE	SRYS
2. M. Navneeth Kumar	15VE1A0491	ECE	SRYS
3. Sai Krishna	15VE1A04B7	ECE	SRYS
4. Govardhanam Nanda Kumar	14VE1A0420	ECE	SRYS

Project Title : Smart ECO Car

Project Guide : B.Venkanna

Duration : 1 Month

K. Tejassi
(R&D Engineer)





No. : 3355

The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 11.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. M. Pravalika	15VE1A0493	ECE	SRYS
2. G. Sai Kumar	16VE5A0418	ECE	SRYS
3. N. Shilpa	15VE1A0494	ECE	SRYS
4. J. Mounisha	15VE1A0480	ECE	SRYS
5. B. Priyanka	15VE1A0465	ECE	SRYS

Project Title : Satellite technology on advanced railway signalling

Project Guide : N. Sreedhar

Duration : 1 Month

Srujana
(R&D Engineer)





No. : 3342

The Institution of Electronics and Telecommunication Engineers
Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 08.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. M. Pravalika	15VE1A0493	ECE	SRYS
2. G. Sai Kumar	16VE5A0418	ECE	SRYS
3. N. Shilpa	15VE1A0494	ECE	SRYS
4. J. Mounisha	15VE1A0480	ECE	SRYS

Project Title : Satellite technology on advanced railway signalling

Project Guide : N. Sreedhar

Duration : 1 Month

Srujana

(R&D Engineer)





The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org

B14

No. : 3353

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 10.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. K. Ganesh	16VE5A0423	ECE	SRYS
2. S. Sai Teja	15VE1A04A7	ECE	SRYS
3. G. Satyam	16VE5A0419	ECE	SRYS
4. R. Harish	15VE1A04A1	ECE	SRYS

Project Title : Bomb disposal robot

Project Guide : B. Venkanna

Duration : 1 Month

Srujana
(R&D Engineer)





NO.RCI/HRD/Project_work/415/Form/
 Government of India, Ministry of Defence
 Defence Research & Development Org (DRDO)
 RESEARCH CENTRE IMARAT
 Dr APJ Abdul Kalam Missile Complex
 Vignyana Kancha, Hyderabad - 500 069
 Phone No: 24306635/24306384
 Fax No : 24306014
 Email ID : Hrdg@rcilab.in

FORWARDING OF APPLICATION FOR PROJECT TRAINEES (STUDENTS)

- 1. Name of the Student (s) : T BHAVANI
K SOWMYA
P NIKHIL
R SAKUMAR REDDY
- 2. Relationship with DRDO : RELATIVE OF SHRI VIKAS PATEL, SC 'C' ⁶⁷⁴³
- 3. College / Institution / University : SREYAS INSTITUTE OF ENGG. & TECHNOLOGY
- 4. B.E /B.Tech/M.E/M.Tech : B TECH
- 5. Branch : ELECTRONICS & COMMUNICATION ENGG.
- 6. Duration of Training : From 14 MAY 2018 to 25 JUNE 2018.
- 7. Title of the Project : DESIGN & SIMULATION OF COUPLER
- 8. Allotment of Directorate : DRSS
- 9. Guide Name & Designation : K BALAJYOTHI , SC 'E'
- 10. Guide Signature : K. Balaji

[Handwritten Signature]
 11/05/2018

[Handwritten Signature]
 (Technology Director / Project Director)
 बी.आर.के. रेड्डी/B.R.K. REDDY
 उत्कृष्ट वैज्ञानिक/Outstanding Scientist
 तकनीकी निदेशक, डि आर एस एस/Technology Director, DRSS
 अनुसंधान केन्द्र इमारत/Research Centre Imarat
 डॉ ए पी जे अब्दुल कलाम प्रशोधन संस्थान/Dr APJ Abdul Kalam Missile Complex
 हैदराबाद/Hyderabad-500 069.

B-16

No. : 3354



**The Institution of Electronics and Telecommunication Engineers
Hyderabad Centre**

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 10.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. C. Priya	15VE1A0471	ECE	SRYS
2. T. Vineeth	15VE1A04B4	ECE	SRYS
3. T. Meghvardhan	15VE1A04B6	ECE	SRYS
4. A. Rakesh	15VE1A0463	ECE	SRYS

Project Title : Windmill power generation

Project Guide : B. Venkanna

Duration : 1 Month

Srujana
(R&D Engineer)



PHONE : 040-24583040
24583151
FAX : 040-24583154



GOVERNMENT OF INDIA
MINISTRY OF DEFENCE
DEFENCE RESEARCH &
DEVELOPMENT LAB
KANCHANBAGHP P.O
HYDERABAD- 500 058

NO.DRDL/DOHR/Internship/2018

st-
DATED 21 May, 2018

To

The Principal,
Sreyas Institute of Engg. & Tech,
Tattiannaram (V), G.S.I. Bandlaguda,
Nagole, Hyderabad - 500 068.

SUB : 2ND SUMMER INTERNSHIP PROGRAMME TO III B.E / B.TECH (ALL BRANCHES) STARTING FROM 25TH MAY, 2018 AT DRDL AUDITORIUM

Based on your request letter, the Competent Authority has approved the Summer Internship in respect of the following students of your College / Institution:

1. B. Harshitha 15VE1A0466
- ✓ 2. A. Keerthana 15VE1A0464

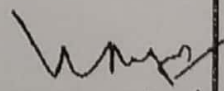
The Internship is scheduled from **25th May, 2018.**

Please instruct above students to report to DRDL Auditorium, DRDL, Hyderabad on **24th May, 2018 (one day before start of the Programme) at 1000 Hrs** for registration.

INSTRUCTIONS FOR STUDENTS :

1. Carry valid College / Institution Student ID Card & Two photographs.
2. Carry Aadhar card copy.
3. Mobile phones are strictly not allowed.
4. Accommodation and Transportation is not provided.
5. Make own arrangement for lunch.
6. Bring own note book and pen.
7. All expenses are to be borne by students themselves during the internship.
8. Strict discipline and safety has to be maintained during the stay in DRDL.
9. Timings are to be strictly adhered.
10. Detailed schedule will be given on 25th May, 2018.

We wish all the best to the students and take the opportunity to learn and gain knowledge from the Internship Programme.


(Dr. K. Rajender Rao)
Scientist F
Director, Human Resources
for Director, DRDL



NO.RCI/HRD/Project_work/415/Form/
Government of India, Ministry of Defence
Defence Research & Development Org (DRDO)
RESEARCH CENTRE IMARAT
Dr APJ Abdul Kalam Missile Complex
Vignyana Kancha, Hyderabad - 500 069
Phone No: 24306635/24306384
Fax No : 24306014
Email ID : Hrdg@rcilab.in

FORWARDING OF APPLICATION FOR PROJECT TRAINEES (STUDENTS)

1. Name of the Student (s) : T BHAVANI
K SOWMYA
P NIKHIL
R SAKUMAR LEDDY
2. Relationship with DRDO : RELATIVE OF SHRI VIKAS PATEL, SC 'C' 67L13
3. College / Institution / University : SREYAS INSTITUTE OF ENGG. & TECHNOLOGY
4. B.E /B.Tech/M.E/M.Tech : B TECH
5. Branch : ELECTRONICS & COMMUNICATION ENGG.
6. Duration of Training : From 14 MAY 2018 to 25 JUNE 2018.
7. Title of the Project : DESIGN & SIMULATION OF COUPLER
8. Allotment of Directorate : DRSS
9. Guide Name & Designation : K BALAJYOTHI , SC 'E'
10. Guide Signature : K. Balaji Jyothi

11/05/2018

(Technology Director / Project Director)

की आर.के. रेड्डी/B.R.K. REDDY
अ.प्र.वे. प्रौद्योगिकी/Outstanding Scientist
संशोधन विभाग, डी.आर.ए.ए.ए. प्रौद्योगिकी निदेशक, DRSS
संशोधन केंद्र, इमारत, रिसर्च सेंटर इमारत
डॉ. ए.पी.जे. अब्दुल कलाम मिसाइल कम्प्लेक्स
हैदराबाद/Hyderabad-500 069.

Scanned by CamScanner

THE NATIONAL SMALL INDUSTRIES CORPORATION LIMITED

(A Govt. of India Enterprise)

TECHNICAL SERVICES CENTRE

Recognized R & D centre by Department of Science & Technology
Government of India

Kushaiguda, ECIL PO, HYDERABAD 500062
Ph: 040-27121422, 27126646, Fax: 27122303.

24-05-2018

NSIC/TSC/HYD/TRG-ELC/2018-19

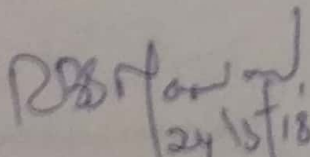
WHOM SO EVER IT MAY CONCERN

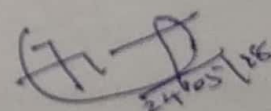
This is to inform you that the following students are Undergoing "Mini project-Single chip FM Radio circuit" in Electronics Department/Module since 04-05-2018. The Course Completion Date is 03-06-2018. Due to some technical problem the project cannot be executed. Instead of that project they can go on with "Innovative Hidden things locator using wireless technology".

S.NO	NAMES	ROLL NO.
1	LAKHILA	15VE1A04E5
2	N.LOKESH	15VE1A04F9
3	B.BHEEMA	15VE1A04C4
4	G.SAI KRISHNA	15VE1A04G9

Their stay with the organization is purely temporary, and this letter is issued on their request.

Yours truly,


(R. Balasubramanian)
Dy. General Manager


ELECTRONICS MODULE HEAD
(G. JAYAVARMAN)

THE NATIONAL SMALL INDUSTRIES CORPORATION LIMITED

(A Govt. of India Enterprise)

TECHNICAL SERVICES CENTRE

Recognized R & D centre by Department of Science & Technology
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Kushaiguda, ECIL PO, HYDERABAD 500062
Ph: 040-27121422, 27126646, Fax: 27122303.

04-05-2018

NSIC/TSC/HYD/TRG-ELC/2018-19

WHOM SO EVER IT MAY CONCERN

This is to inform you that the following students are Undergoing "Mini Project (RFid Based access control system using arduino)" in Electronics Department/Module since 04-05-2018. The Course Completion Date is 03-06-2018.

1	J.Srilekha	D/O J.Damodhar Reddy
2	M.Abhigna	D/O M.Narender Reddy
3	M.Kalanjali	D/O M.Kasi Reddy
4	N.Abhiram Teja	S/O N.Srinivasa Rao

Their stay with the organization is purely temporary, and this letter is issued on their request.

Yours truly,

RBS Manian
(R.Balasubramanian) 4/5/18
Dy.General Manager

[Signature]
04/05/18

आर. बालसुब्रमनियन/R. BALASUBRAMANIAN
उप महा प्रबंधक /Deputy General Manager
राष्ट्रीय लघु उद्योग निगम लिमिटेड
The National Small Industries Corporation Limited
भारत सरकार का उद्यम
(A Govt. of India Enterprise) under Ministry of MSME
तकनीकी सेवा केन्द्र / Technical Services Centre
हैदराबाद / Hyderabad-500062

THE NATIONAL SMALL INDUSTRIES CORPORATION LIMITED

(A Govt. of India Enterprise)

TECHNICAL SERVICES CENTRE

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Government of India

Kushaiguda, ECIL PO, HYDERABAD 500062
Ph: 040-27121422, 27126646, Fax: 27122303.

10-05-2018

NSIC/TSC/HYD/TRG-ELC/2018-19

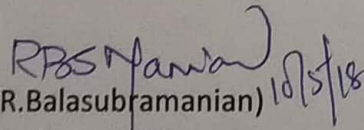
WHOM SO EVER IT MAY CONCERN

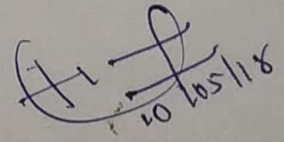
This is to inform you that the following students are Undergoing "Mini project" in Electronics Department/Module since 04-05-2018. The Course Completion Date is 03-06-2018.

S.NO	NAMES	ROLL NO.
1	AKHILA	15VE1A04E5
2	LOKESH	15VE1A04F9
3	BHEEMA	15VE1A04C4
4	SAI KRISHNA	15VE1A04G9

Their stay with the organization is purely temporary, and this letter is issued on their request.

Yours truly,


(R. Balasubramanian) 10/5/18
Dy. General Manager


ELECTRONIC MODULE HEAD
(G. JAYAVARMAN)

THE NATIONAL SMALL INDUSTRIES CORPORATION LIMITED

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Ph: 040-27121422, 27126646, Fax: 27122303.

08-05-2018

NSIC/TSC/HYD/TRG-ELC/2018-19

WHOM SO EVER IT MAY CONCERN

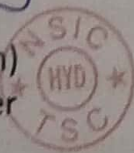
This is to inform you that the following students are Undergoing "Mini Project (WIRELESS ENCRYPTION AND DECRYPTION)" in Electronics Department/Module since 04-05-2018. The Course Completion Date is 03-06-2018.

SL.NO	NAME OF THE TRAINEE	FATHERS NAME	ROLL NO
1	Mamidi Tejaswini	Mamidi Anji Reddy	15VE1A04E9
2	V Nikhil Chakrawartha	V Chandra Sekhar	15VE1A04J0
3	Nalla Srilekha	Nalla Krishnaiah	15VE1A04F8
4	Merugu Bhuvaneshwari	Merugu Venkatesh Yadav	16VE5A0433

Their stay with the organization is purely temporary, and this letter is issued on their request.

Yours truly,

RBS Manoj
(R. Balasubramanian)
Dy. General Manager



[Handwritten signature]



No. : 3350

The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 09.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. E. Rachana Reddy	15VE1A04C8	ECE	SRYS
2. B. Naresh	16VE5A0409	ECE	SRYS
3. K. Ashok	16VE5A0424	ECE	SRYS
4. J. Srinidhi	15VE1A04D5	ECE	SRYS

Project Title : Smart Helmet

Project Guide : B. Venkanna

Duration : 1 Month

Srujana

(R&D Engineer)





इलेक्ट्रॉनिक्स कारपोरेशन आफ इण्डिया लिमिटेड

Electronics Corporation of India Limited

CIN U32100TG1967GOI001149

(भारत सरकार का उद्यम) / (A Govt. of India Enterprise)

कम्प्यूटर शिक्षा प्रभाग / COMPUTER EDUCATION DIVISION

ECIT



ISO 9001:2008

Date: 11/05/2018.

To,
Head of the Department,
Sreyas institute of engineering and technology ,
Hyderabad.

Dear Sir,

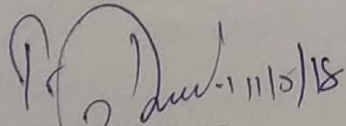
Mr. P. HRISHIKESH KAUSHIK (15VE1A04G6), Mr. KAMESH (15VE1A04E4), Mr. HARSHA VARDHAN REDDY (15VE1A04C3) & Mr. BHEEMESH (16VE5A0436) students of Sreyas institute of engineering and technology are assigned to do a mini project in "EMBEDDED SYSTEMS" in Computer Education Division, ECIL for a period of one month as a part of their project fulfillment for the award of the Engineering Degree certificate.

Project title:

Thanking You,

Yours truly,

For ELECTRONICS CORPORATION OF INDIA LIMITED


T. THIRUMALAI

DGM: CED

Dr. T. THIRUMALAI
DGM, CED, ECIL,
HYDERABAD-500 062

अतिथि गृह कॉम्प्लेक्स, ई सी आई एल, हैदराबाद, तेलंगाना, भारत.
Guest House Complex, ECIL, Hyderabad - 500 062. T.S., INDIA.
दूरभाष / Tel. 2712 5864, 2712 2816, टेली फैक्स / Tele Fax : +91-040-2712 6017
वेब साइट / Web Site : www.ecil.co.in, ई-मेल / E-mail : headced@ecil.co.in

THE NATIONAL SMALL INDUSTRIES CORPORATION LIMITED

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TECHNICAL SERVICES CENTRE

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Ph: 040-27121422, 27126646, Fax: 27122303.

11-05-2018

NSIC/TSC/HYD/TRG-ELC/2018-19

WHOM SO EVER IT MAY CONCERN

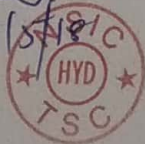
This is to inform you that the following students are Undergoing "Mini Project (BROKEN RAIL DETECTION SYSTEM THROUGH WIRELESS NETWORK)" in Electronics Department/Module since 04-05-2018. The Course Completion Date is 03-06-2018.

SL.NO	NAME OF THE TRAINEE	FATHERS NAME	ROLL NO
1	Munagapati Harshith Netha	Munagapati Srinivas	15VE1A04F5
2	Karipe Samyuktha	K Rajender	15VE1A04E0
3	Deshidi Rajapriyanka	Deshidi Vankanna	15VE1A04C6
4	Akoju Charan Chandu	A. Nagabhushana	15VE1A04C1

Their stay with the organization is purely temporary, and this letter is issued on their request.

Yours truly,

RBS Pawar
(R. Balasubramanian)
Dy. General Manager



G. Jayavarma
11/05/18

ELECTRONIC MODULE HEAD
(G. JAYAVARMA)

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08-05-2018

NSIC/TSC/HYD/TRG-ELC/2018-19

WHOM SO EVER IT MAY CONCERN

This is to inform you that the following students are Undergoing "Mini Project (WIRELESS HEART RATE MONITORING SYSTEM USING RF TECHNOLOGY)" in Electronics Department/Module since 04-05-2018. The Course Completion Date is 03-06-2018.

SL.NO	NAME OF THE TRAINEE	FATHERS NAME	ROLL NO
1	Pasupuleti Manisha	Pasupuleti Srinivasulu	15VE1A04G5
2	M. Keertana	M. Rajani Kanth	15VE1A04D8
3	Godithi Kavi Chandra	Godithi Dinesh Kumar	15VE1A04D2
4	Kasa Madhukanth	Kasa Udaya Shankar	15VE1A04E1

Their stay with the organization is purely temporary, and this letter is issued on their request.

Yours truly,

RBS Manwan
(R. Balasubramanian)
Dy. General Manager



Handwritten signature/initials

No. : 3313

The Institution of Electronics and Telecommunication Engineers
Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

Phone: 040-2709 5128, 2709 8025, 2709 7175

E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org



PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 03.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. M. Saketh Reddy	15VE1A04F2	ECE	SRYS
2. M. Yugender	16VE1A0428	ECE	SRYS
3. Sk Afthab	15VE1A04H3	ECE	SRYS
4. P. Madhuri	15VE1A04G4	ECE	SRYS

Project Title : RF-controlled spy robot with night vision camera

Project Guide : B. Venkanna

Duration : 1 Month

Srujana
(R&D Engineer)



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10-05-2018

NSIC/TSC/HYD/TRG-ELC/2018-19

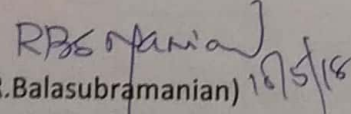
WHOM SO EVER IT MAY CONCERN

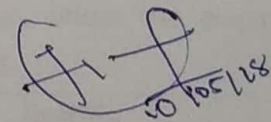
This is to inform you that the following students are Undergoing "Mini project" in Electronics Department/Module since 04-05-2018. The Course Completion Date is 03-06-2018.

S.NO	NAMES	ROLL NO.
1	G.ARCHITHA	15VE1A04D3
2	M.ANJALA	15VE1A04F1
3	K.ROHAN REDDY	15VE1A04D9
4	VAMSHI	15VE1A04H6

Their stay with the organization is purely temporary, and this letter is issued on their request.

Yours truly,


(R. Balasubramanian) 10/5/18
Dy. General Manager



ELECTRONIC MODULE HEAD
(G. JAYAVARMA)



No. : 3340

The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

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E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 08.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. Sai Sumanth	16VE5A0434	ECE	SRYS
2. Lalith Yadav	16VE5A0426	ECE	SRYS
3. Sindhu	15VE1A04F3	ECE	SRYS
4. Tejaswini	15VE1A04G2	ECE	SRYS

Project Title : DTMF based humanless boat control for oceanic applications

Project Guide : CH.V.Ravi Teja

Duration : 1 Month

Srujana

(R&D Engineer)





No. : 3341

The Institution of Electronics and Telecommunication Engineers Hyderabad Centre

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www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 08.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. M. Praveena	16VE5A0429	ECE	SRYS
2. M. Bhavani	16VE5A0432	ECE	SRYS
3. S. Navya Sri	15VE1A04H0	ECE	SRYS
4. D. Shiva	15VE1A04C5	ECE	SRYS

Project Title : Sensor based E-bikes for college campus

Project Guide : CH.V.Ravi Teja

Duration : 1 Month

Srujana
(R&D Engineer)



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08-05-2018

NSIC/TSC/HYD/TRG-ELC/2018-19

WHOM SO EVER IT MAY CONCERN

This is to inform you that the following students are Undergoing "Mini Project (SAFETY GUARD FOR THE BLIND WITH VOICE FEEDBACK)" in Electronics Department/Module since 04-05-2018. The Course Completion Date is 03-06-2018.

SL.NO	NAME OF THE TRAINEE	FATHERS NAME	ROLL NO
1	Sheela Aishwarya	Sheela. Devendher Kumar	15VE1A04H5
2	Maddikuntla Manasa	Maddikuntla Srinivas	16VE5A0427
3	Gundlapelli Mukesh Goud	Gundlapelli.Dasu Goud	15VE1A04D4
4	Kyama Vamshi Krishna	Kyama Prabhakar	14VE1A04E9

Their stay with the organization is purely temporary, and this letter is issued on their request.

No. : 3357



The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

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www.ietehyd.org

rs

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 11.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. Dhrudeep Parikh	15VE1A04C7	ECE	SRYS
2. A. Gayathri	15VE1A04D1	ECE	SRYS
3. S. Sripada	15VE1A04H1	ECE	SRYS
4. A. Shireesha	16VE5A0404	ECE	SRYS

Project Title : Electronic Guide for super bazars

Project Guide : B. Venkanna

Duration : 1 Month

Srujana
(R&D Engineer)





No. : 3356

The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

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www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 10.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. N. Agasthya Preetham	15VE1A04G0	ECE	SRYS
2. P. Manish	15VE1A04G3	ECE	SRYS
3. V. Manish	14VE1A04H6	ECE	SRYS
4. R. Sai Rahul	15VE1A04G8	ECE	SRYS

Project Title : A low power modified pipelined LFSR architecture

Project Guide : N. Sreedhar

Duration : 1 Month

Srujana
(R&D Engineer)



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14-05-2018

NSIC/TSC/HYD/TRG-ELC/2018-19

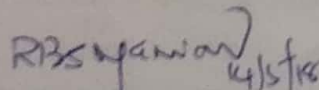
WHOM SO EVER IT MAY CONCERN

This is to inform you that the following students are Undergoing "Mini Project (MEMS CONTROLLED ELECTRICAL APPLIANCES)" in Electronics Department/Module since 04-05-2018. The Course Completion Date is 03-06-2018.

SL.NO	NAME OF THE TRAINEE	FATHERS NAME	ROLL NO
1	Tanikella Tanishka H.C	Tanikella Srihari	15VE1A04H7
2	Pullabatla Avinash Prasad	Pullabatla Guru Prasad	15VE1A04G7
3	Mami Dala Shirisha	Mamidala Mallesh	16VE5A0430

Their stay with the organization is purely temporary, and this letter is issued on their request.

Yours truly,


(R. Balasubramanian)
Dy. General Manager





No. : 3314

The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

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www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 03.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. G. Tirupathi	15VE1A04D0	ECE	SRYS
2. K. Ganesh	15VE1A04E2	ECE	SRYS
3. V. Subash	15VE1A04H9	ECE	SRYS

Project Title : Bluetooth controlled home automation

Project Guide : B. Venkanna

Duration : 1 Month

Srujana
(R&D Engineer)





No. : **3338**
The Institution of Electronics and Telecommunication Engineers
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www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 08.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

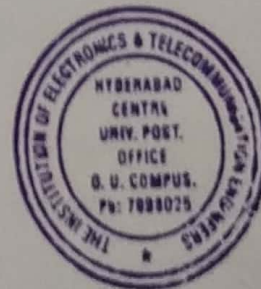
Name	Roll No.	Branch	College
1. M. Srinivas	16VE5A0431	ECE	SRYS
2. S. Sai Sree	15VE1A04H2	ECE	SRYS
3. U. Divya	15VE1A04H8	ECE	SRYS

Project Title : Flash flood intimation over wireless network

Project Guide : CH. V. Ravi Teja

Duration : 1 Month

Srujana
(R&D Engineer)



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NSIC/TSC/HYD/TRG-ELC/2018-19

08-05-2018

WHOM SO EVER IT MAY CONCERN

This is to inform you that the following students are Undergoing "Mini Project (ZONE WISE SPEED CONTROL SYSTEM FOR VEHICLE)" in Electronics Department/Module since 04-05-2018. The Course Completion Date is 03-06-2018.

SL.NO	NAME OF THE TRAINEE	FATHERS NAME	ROLL NO
1	E Sai Kiran	Prakash Rao	15VE1A04C9
2	N Arpitha	N Vinay Raj	15VE1A04F7
3	Kunchala Sai Lohinath Rao	Kunchala Mallikarjun Rao	15VE1A04E3

Their stay with the organization is purely temporary, and this letter is issued on their request.

Yours truly,

RBS Marian
(R. Balasubramanian)
Dy. General Manager



HS



D-1 No. : 3332

**The Institution of Electronics and Telecommunication Engineers
Hyderabad Centre**

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

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E-mail : hyderabad.iete@gmail.com, ietehyd@gmail.com, info@ietehyd.org

www.ietehyd.org

RS

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 07.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. P. Sampath	16VE5A0437	ECE	SRYS
2. Y. Sai Kumar	16VE5A0448	ECE	SRYS
3. N. Shiva Kumar	15VE1A04M0	ECE	SRYS
4. S. Sailesh	15VE1A04N3	ECE	SRYS

Project Title : Touch Screen & ZIGBEE based Restaurant ordering system

Project Guide : B.Venkanna

Duration : 1 Month

K. Tejassi
(R&D Engineer)





No. : 3364

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www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 21.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. G. Nanda Nikhil	15VE1A04K3	ECE	SRYS
2. M. Kavya	15VE1A04L9	ECE	SRYS
3. Ch. Prashanthi	15VE1A04J6	ECE	SRYS

Project Title : Integrated mines safety monitoring and alerting systems

Project Guide : CH.V.Ravi Teja

Duration : 1 Month

Srujana
(R&D Engineer)



0-3
No. 3323

The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

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www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: .05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. Roshni Chawla	15VE1A04N2	ECE	SRYS
2. D. Gnaneshwari	15VE1A04J9	ECE	SRYS
3. T. Vamshi Krishna	15VE1A04P1	ECE	SRYS
4. S. Rohith Kumar Goud	15VE1A04P0	ECE	SRYS

Project Title : GPS+GSM based advanced vehicle tracking system

Project Guide : B.Venkanna

Duration : 1 Month

K. Tejasri
(R&D Engineer)





D3 .
No. : 3496

**The Institution of Electronics and Telecommunication Engineers
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www.ietehyd.org

Ref.No. IETE/R&D/STMINIPROJ/2018

Date: 27.06.2018

CERTIFICATE

This is to certify that the project work entitled "GPS GSM based advanced vehicle tracking and locking system" submitted by Roshni Chawla (15VE1A04N2), D. Gnaneshwari (15VE1A04J9), T. Vamshi Krishna (15VE1A04P1) and S. Rohith Kumar Goud (15VE1A04P0) students of "Sreyas Institute of Engineering and Technology" in partial fulfillment for the award of the degree of B.Tech in Electronics and Communication Engineering record of bonafide work carried out by them, in our organization during 2nd May, 2018 to 2nd June, 2018. This report is not submitted for any other degree or diploma.

Their attendance and performance during the period was good.

B. Venkanna

(B. Venkanna)
Project Guide

(Dr.D.Linga Reddy)
Honorary Secretary,
IETE, Hyderabad Centre.

(Dr.G.Laxminarayana)
Chairman,
IETE, Hyderabad Centre.





No. : 3366

The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

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PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 25.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. G. Manikanteshwar	15VE1A04K5	ECE	SRYS
2. P. Sai Kiran	15VE1A04M5	ECE	SRYS
3. K. Praveena	15VE1A04L0	ECE	SRYS
4. K. Hamilton	15VE1A04L1	ECE	SRYS

Project Title : Anti-collision glasses for blind

Project Guide : N.Sreedhar

Duration : 1 Month

Srujana
(R&D Engineer)



D-4

No. : 3625



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Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

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E-mail : hyderabad.iete@gmail.com, ictchyd@gmail.com, info@ictchyd.org
www.ictchyd.org

Ref.No. IETE/R&D/STMINIPROJ/2018

Date: 04.07.2018

CERTIFICATE

This is to certify that the project work entitled "Anti-collision glasses for blind" submitted by G. Manikanteshwar (15VE1A04K5), P. Sai Kiran (15VE1A04M5), K. Praveena (15VE1A04L0) and K. Hamilton (15VE1A04L1) students of "Sreyas Institute of Engineering and Technology" in partial fulfillment for the award of the degree of B.Tech in Electronics and Communication Engineering record of bonafide work carried out by them, in our organization during 2nd May, 2018 to 2nd June, 2018. This report is not submitted for any other degree or diploma.

Their attendance and performance during the period was good.

Sreedhar
(N. Sreedhar)
Project Guide

(Dr.D.Linga Reddy)
Honorary Secretary,
IETE, Hyderabad Centre.

[Signature]
CHAIRMAN
IETE HYD. CENTRE

(Dr.G.Laxminarayana)
Chairman,
IETE, Hyderabad Centre.





D-5

No. : 3337

**The Institution of Electronics and Telecommunication Engineers
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www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 08.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. D. Bhavani	15VE1A04J8	ECE	SRYS
2. S. Rakesh	15VE1A04N8	ECE	SRYS
3. G. Sai Tarun	15VE1A04K7	ECE	SRYS

Project Title : Smart Hover Board with automatic & manual transmission

Project Guide : CH. V. Ravi Teja

Duration : 1 Month

Srujana
(R&D Engineer)





D-6 No. : 3318

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PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 05.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. Ch. V. Sai Mounica	15VE1A04J5	ECE	SRYS
2. S. Krishna Sai	15VE1A04N7	ECE	SRYS
3. K. Avinash	15VE1A04L3	ECE	SRYS
4. K. Lakshmi Prasanna	15VE1A04L2	ECE	SRYS

Project Title : Design and development of embedded live & motion detection system in military application IPC 2148

Project Guide : B.Venkanna

Duration : 1 Month

K. Tejassri
(R&D Engineer)





D-8

No. : 3349

The Institution of Electronics and Telecommunication Engineers

Hyderabad Centre

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

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PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 09.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. D. Chandana	15VE1A04J7	ECE	SRYS
2. L. Anusha	15VE1A04L7	ECE	SRYS
3. N. Upender	15VE1A04M2	ECE	SRYS
4. V. Karthik	15VE1A04P9	ECE	SRYS

Project Title : Density based traffic signal system using arduino

Project Guide : CH.V.Ravi Teja

Duration : 1 Month

Srujana
(R&D Engineer)



No. 3951

**The Institution of Electronics and Telecommunication Engineers
Hyderabad Centre**

Adjacent: Director, P G Admissions Office, O.U. Campus, Hyderabad-500 007, T.S, India

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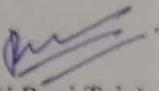
Ref.No. IETE/R&D/STMINIPROJ/2018

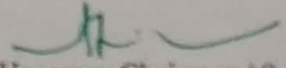
Date: 28.06.2018

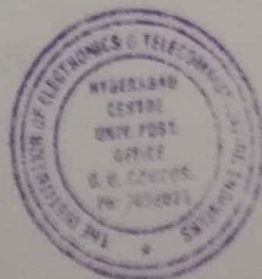
CERTIFICATE

This is to certify that the project work entitled "Density Based Traffic Signal System using Arduino" submitted by D. Chandana (15VE1A04J7), L. Anusha (15VE1A04L7), N. Upender (15VE1A04M2) and V. Karthik (15VE1A04P9) students of "Sreyas Institute of Engineering and Technology" in partial fulfillment for the award of the degree of B.Tech in Electronics and Communication Engineering record of bonafide work carried out by them, in our organization during 5th May, 2018 to 5th June, 2018. This report is not submitted for any other degree or diploma.

Their attendance and performance during the period was good.


(CH.V.Ravi Teja)
Project Guide


Honorary Chairman / Secretary
IETE, Hyderabad Centre.





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www.ietehyd.org

D-10

No. : 3326

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 07.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. S. Harish	15VE1A04N4	ECE	SRYS
2. P. Supriya	15VE1A04M6	ECE	SRYS
3. S. Rakesh	16VE5A0444	ECE	SRYS
4. V. Hari Haran Reddy	14VE1A04P8	ECE	SRYS

Project Title : Hand talk assistance using hand glove and controlling vehicle

Project Guide : CH.V.Ravi Teja

Duration : 1 Month

K. Teja
(R&D Engineer)





D-11
No. : 3361

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www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 14.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. G. Sowjanya	15VE1A04K4	ECE	SRYS
2. V. Chandan Swaraj	15VE1A04Q0	ECE	SRYS
3. V. Chandra Shekar	16VE5A0447	ECE	SRYS
4. T. Saketh	15VE1A04P3	ECE	SRYS

Project Title : The smart segway bike with inbuilt solar automatic mode integrated with mobile/tablet using internet

Project Guide : CH.V.Ravi Teja

Duration : 1 Month

Srujana

(R&D Engineer)





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www.ietchyd.org

D-12 No. : 3322

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PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 05.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. P. Divya	15VE1A04M4	ECE	SRYS
2. Prasad Aravind	16VE5A0440	ECE	SRYS
3. P. Rohini	15VE1A04M8	ECE	SRYS
4. Siddardh	15VE1A04K2	ECE	SRYS

Project Title : Biometric attendance system over IOT

Project Guide : B.Venkanna

Duration : 1 Month

K. Tejasvi
(R&D Engineer)



D-13

No. : 3316



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www.ietehyd.org

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PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 04.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. P. Sanjana	15VE1A04M3	ECE	SRYS
2. S. Meghana	15VE1A04N5	ECE	SRYS
3. P. Anish	15VE1A04M9	ECE	SRYS
4. R. Abhishek	15VE1A04N1	ECE	SRYS

Project Title : Artificial Intelligence A.I table mate with intruder image capturing technique using smart phone WIFI

Project Guide : CH.V. Ravi Teja

Duration : 1 Month

Srujana
(R&D Engineer)



D-15

No. : 3324



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www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 07.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. P. Vennela	16VE5A0438	ECE	SRYS
2. P.Rishika	15VE1A04M7	ECE	SRYS
3. T. Pravalika	16VE5A0446	ECE	SRYS
4. A. Vishal	15VE1A04J2	ECE	SRYS

Project Title : Women safety device with GPS tracking & alerts

Project Guide : CH.V.Ravi Teja

Duration : 1 Month

K. Tejasee
(R&D Engineer)





D-16

No. : **3327**

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PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: 07.05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. D. Harshini	15VE1A04K0	ECE	SRYS
2. G. V. Rithika	15VE1A04K6	ECE	SRYS
3. T. Vinay	15VE1A04P6	ECE	SRYS
4. T. Yugandhar	16VE5A0445	ECE	SRYS

Project Title : Smart Watch Operated vehicle with Image Capturing System

Project Guide : CH.V.Ravi Teja

Duration : 1 Month

K. Tejasi
(R&D Engineer)





D-17

No. : **3325**

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www.ietehyd.org

PROJECT ALLOTMENT LETTER

Ref: IETE/R&D/MINIPROJ/2018

Date: .05.2018

R&D Sub-committee of IETE, Hyderabad Centre is allotted the Mini Project work in our organization to the following student as per the details given below.

Name	Roll No.	Branch	College
1. R. Vineeth	16VE5A0442	ECE	SRYS
2. T. Sai Deekshitha	15VE1A04P4	ECE	SRYS
3. A. Manisha	16VE5A0406	ECE	SRYS
4. K. Akhilesh	15VE1A04K9	ECE	SRYS

Project Title : Programmable energy meter with bills estimation

Project Guide : B.Venkanna

Duration : 1 Month

K. Tejasi
(R&D Engineer)





D-19

भारत डायनामिक्स लिमिटेड
BHARAT DYNAMICS LIMITED
(भारत सरकार का उपक्रम A Government of India Enterprise)

रक्षा मंत्रालय Ministry of Defence

कंचनबाग Kanchanbagh :: हैदराबाद Hyderabad - 500 058

दूरभाष Tel.No. : 24587917

परियोजना कार्य आबंटन-पत्र

PROJECT WORK ALLOTMENT LETTER

वर्ष तथा पंजीकरण संख्या Year & Registration No : 2018-19/ 113

Name of the Student : Polisbetti Divya Age: 20
Son/Daughter of Shri : P. Basappa Smt. P. Basvareela
Full Permanent Address: 10-4-253/1, Birammalguda, Sagur road, Hyd
Pin. Code 500079 Cell: 9948042700
College Institute : Sreyas Institute of Engineering & Tech
Qualification : B.Tech Branch: ECE Sem: IV-I College ID No. 16VE5A0439
Duration of the Project : 1 month From: 18/5/18 To: 17/6/18
Guide : K. Venkateshwar Rao
: DGM Desig. /Dept: E&D ST.No. _____
Fee Amount Rs. : 2000 Bank Name: Andhra DDNo: 2303 Date 18/5/18
Reference : Change (Internal Audit)
Identification marks : A mole on the left cheek

Copy to:

- 1) Project Guide : Guiding the student for doing Project Work
- 2) Incharge CISF (Security) : Arranging Temporary entry passes for above period.

Note No: 243

Checked by: [Signature]

Student Sign: P. Divya

Instructions for student

- 1) Project work should be completed within the period. No extension will be granted.
- 2) You are permitted to go up to the project guide allotted.
- 3) Cameras, Cell Phone, CDs are not permitted.
- 4) You should submit the report and obtain certificate on the date of completion of project work.

[Signature]
Mgr (P&A) App. Trgs. & Legal

वी. प्रवीन कुमार
V. PRAVEEN KUMAR
प्रबंधक (का. एवं प्रशा.) शिक्षु प्रशि एवं विधि
Manager (P&A) App. Trgs. & Legal