

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

4-05-2018

То

The Principal Sreyas Institute of Engineering and Technology Nagole Hyderabad – 500068 Respected Sir,

Sub: Grant of permission to organize a Workshop on Emerging Trends in VLSI & Signal Processing, - a request-regarding.

In order to enlighten the current trends in Electronics and Communication Engineering among the faculty community we would like to organize a Workshop on Emerging Trends in VLSI & Signal Processing on 07-05-2018 to 12-05-2018. Herewith we have attached the budget for your kind perusal.

Kindly permit us to organize the event.

Thanking you,

Yours sincerely, Som 4 B. Sreenivasu TMEN Head OF the Department Electronics and Communication Engineering Electronics STITUTE OF ENgineering SREVAS INSTITUTE OF ENgole, Hydro esides indu Aranya, Nagole, Hyd e-mail: ece hod@sreyas.ac.II

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering

4-05-2018

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Budget:

Sl. No	Particulars	Amount (INR)		
1	Brochure printing	1006.00		
2	Stationary (Scribbling pad and pen) (33 Nos)	1200.00		
3	TA and DA for Resource persons (6 Nos)	20000.00		
4	Banner	1600.00		
5	Refreshment and Lunch (33 persons) Tea, Snacks and Working lunch (INR 100/- per person per day)	12000.00		
6	Certificate printing	1000.00		
7	Miscellaneous	2000.00		
	TOTAL	38800.00		

Head of the Department Electronics and Communication Engineering B Sheeniyasu HEAD OF THE DEPARTMENT Electronics & Communication Eng Sheeniyasu Sheeniyasu Electronics & Communication Eng Sheeniyasu Sheeniy



SREYAS INSTITUTE OF ENGINEERING AND TECHNOLOGY (Approved by AICTE, New Delhi | Affiliated to JNTU, Accredited by NAAC, Hyderabad) Beside Indu Aranya, GSI, Bandlaguda, Nagole, Hyderabad, 500068

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

ONE-WEEK WORKSHOP

EMERGING TRENDS IN VLSI & SIGNAL PROCESSING 7TH TO 12TH MAY 2018

Contact: Mr. Sk Mohammed Rafi Assistant Professor, ECE 98493 25350

HEAD OF THE DEPARTMENT Electronics & Communication Energy SHEYAS INSTITUTE OF ENGL & TECH Decrues Indu Aranya, Nagole, Hyd-68 Sherife Sce.hou @sreyas.ac.in Convener: Mr.B. SREENIVASU HOD,ECE 95022 51564



(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN: 500068

Department of Electronics and Communication Engineering

One-week Workshop on Emerging Trends in VLSI & Signal processing

Date: 07-05-2018 to 12-05-2018

Schedule of One-week Workshop on Emerging Trends in VLSI									
Day-1 (07/May/2018)									
	Registration and Inauguration: 9.00 AM to 9.30 AM								
Sl.	Topics to be covered	Time	Name of the Resource Person						
No.	T								
1	Inauguration	9.30 Am to 10.00 AM							
1	VLSI Design Flow	10.00 AM to 11.30AM	Mr. MANOJ M						
2	Architectural level	11.40 AM to 01.30 PM	Scientist – E						
	Optimization Techniques:		Research Centre Imarat						
	Pipelining, Parallelism		Hyderabad						
	and Retiming	Dready (01 20 DM 40 02 0							
3		Break (01.30 PM to 02.0 02.00 PM to 4.00 PM							
3	Verilog/VHDL	02.00 PM to 4.00 PM	Mr. MOHAMMED RAFI						
	Programming Hands-on		SHAIK						
			Assistant Professor, ECE, SIET						
		Day-2 (08/May/2018)							
4	Applications of Adaptive	09.00 AM to 11.00 AM	Dr. B. PRABHAKAR						
	Signal Processing		Associate Professor						
			JNTUH College of Engineering						
			Jagtial, Nachupally, Telangana						
		Tea Break							
5	Artificial Intelligent in	11.10 AM to 01.00 PM	Dr. B. PRABHAKAR						
	Signal Processing		Associate Professor						
			JNTUH College of Engineering						
			Jagtial, Nachupally, Telangana						
	Lunch	Break (01.00 PM to 02.0	0 PM)						
6	Hands-on Training on	02.00 PM to 04.00 PM	Mr. CH S V MARUTHI RAO						
	Xilinx Spartan 3E-FPGA -		Associate Professor, ECE, SIET						
	based implementation								
		Day-3 (09/May/2018)							
7	Research issues in Signal	09.00 AM to 11.00 AM							
	Processing domain		Deputy General Manager						
			BEL, Hyderabad						
		Tea Break							
8	Recent Trends in VLSI	11.00 AM to 01.00 PM	Mr. A. APPLE RAJU						
	domain and Research		Deputy General Manager						
	Issues		BEL, Hyderabad						
	Lunch	Break (01.00 PM to 02.0	· · · · ·						



(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN: 500068

9	Hands-on Training in	02.00 PM to 04.00 PM	Ms. A. SOWJANYA
	Implementation of various		Assistant Professor, ECE, SIET
	Filters, System		
	Identification, Adaptive		
	Filtering using MATLAB		
10		Day-4 (10/May/2018)	
10	Fourier Series and	09.00 AM to 11.00 AM	Dr. SANDEEP KUMAR
	Dirichlet's Conditions		Associate Professor
	from Scratch and		ECE, SIET
	Research Advances in		
	Signal Processing Domain	Tee Devel	
11		Tea Break	
11	Speech Processing and its	11.10 AM to 01.00 PM	Dr. SANDEEP KUMAR
	Research Scopes	Duest (01 00 DM 40 02 0	Associate Professor, ECE, SIET
10	1	Break (01.00 PM to 02.0	Dr. SANDEEP KUMAR
12	Demonstration on various	02.00 PM to 04.00 PM	
	Speech Processing		Associate Professor, ECE, SIET
	Concepts	Day-5 (11/May/2018)	
13	Nanoelectronics	09.00 AM to 11.00 AM	Dr. SHAIK FAROOZ
15	Nanoelectronics	09.00 ANI 10 11.00 ANI	Associate Professor, ECE, SIET
		Tea Break	
14	Power Reduction in	11.10 AM to 01.00 PM	Dr. SHAIK FAROOZ
17	Spintronic Devices		Associate Professor, ECE, SIET
		Break (01.00 PM to 02.0	
15	Case Study: Real Time	02.00 Pm to 04.00 PM	Dr. SHAIK FAROOZ
10	Digital Signal Processing	02.0011110011.001111	Associate Professor, ECE, SIET
	for Electronic Skin		
	Implementation		
		Day-6 (12/May/2019)	
16	Single Electron Devices	09.00 AM to 11.00 AM	Ms. HEMLATHA DALMIA
			Assistant Professor, ECE, SIET
		Tea Break	
17	Biomedical Signal	11.10 AM to 01.00 PM	Ms. HEMLATHA DALMIA
	Processing		Assistant Professor, ECE, SIET
		Break (01.00 PM to 02.0	
18	Case Study: Mixed Signal	02.00 PM to 03.00 PM	Ms. HEMLATHA DALMIA
	Design Flow		Assistant Professor, ECE, SIET
		Valedictory	

Roso)

HOD/ECE



(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN: 500068

Department of Electronics and Communication Engineering

A REPORT OF

ONE-WEEK

WORKSHOP ON

EMERGING TRENDS IN VLSI& SIGNAL PROCESSING

(7thMay 2018 to 12thMay, 2018)

One-week workshop on "Emerging Trends in VLSI" has been conducted by Departments of ECE, Sreyas Institute of Engineering and Technology from07th May to 12thMay, 2018. A Total of 33 faculties have registered for this program. Mr. Manoj M, Scientist – E, Research Centre Imarat, \setminus Hyderabad was the Chief Guest and other dignitaries for the inaugural function were Dr. Suresh Akella, Principal and A. Hriday Reddy, Vice Chairman.

DAY 1:

After inaugural session chief guest addressed the gathering about the one-week faculty development program on E.

Mr. Manoj briefed out concepts such as VLSI design Technology, design flow and architectural level optimization techniques such as pipelining, parallelism and retying. Post lunch session was hands-on Verilog/VHDL programming.

DAY 2:

Day 2 started with the concepts of Applications of adaptive signal processing and after a small tea break he explained about Artificial intelligence in signal processing. Afternoon complete session was hands-on training on Xilinx Spartan #E-FPGA-based implementation.

DAY 3:

Third day was very interesting research issues in signal processing domain were discussed and after a small tea break discussion was on recent trends in VLSI domain and research issues. Afternoon session was hands on training on implementation of various filters, system identification, adaptive filtering using MATLAB.



(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN: 500068

DAY 4:

This day discussion was all about Fourier series and Dirichlet's conditions from scratch and research avenues in signal processing domain, after a small tea break he discussed research scopes in speech processing. Post lunch he demonstrated various speech processing concepts.

DAY 5:

The fifth day forenoon session was started with concepts of Nano-electronics and also discussed about Power reduction in spintronic devices. After lunch various case studies were discussed.

DAY 6:

Day 6 talk was on the concepts such as single electron devices and Biomedical signal processing were discussed in forenoon session and afternoon session various case studies were discussed.

In the valedictory session speaker was facilitated and a vote of thanks was presented by workshop coordinator.

Head of the Department Electronics and Communication Engineering HEAD OF THE DEPARTMENT Electronics & Communication Enge SREYAS INSTITUTE OF ENGG & TECH. Besides indu Aranya, Nagole, Hyd-68 Fe-mail: ece.hod@sreyas.ac.in



(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN: 500068



Faculty members of the department along with the resource person during the session in Emerging Trends in VLSI and Signal Processing



ONE-WEEK

WORKSHOP ON

EMERGING TRENDS IN VLSI & SIGNAL PROCESSING

(7th May 2018 to 12thMay, 2018)

Expenditure Statement:

4

SI. No	Particulars	Amount (INR)
1	Banner and Brochure Printing	950.00
2	Certificate Printing	950.00
3	Scribbling pad and Pen	1100.00
4	Honorarium to Mr. Manoj M	5000.00
5	Honorarium to Dr. B. Prabhakar	5000.00
6	Honorarium to Mr. A. Appal Raju	5000.00
7	Tea, Snacks and Lunch	11480.00
	TOTAL	Rs.29480

HOD, ECE

B. Sreenivasu YEAD OF THE DEPARTMENT octronics & Communication Engg. YAS INSTITUTE OF ENGG. & TECH Besides indu Aranya, Nagole, Hyd- S e-mail: eco 1





4

N

SREYAS INSTITUTE OF ENGINEERING AND TECHNOLOGY

DEPARTMENT OF ECE

Workshop on Emerging Trends in VLSI & Signal Processing ATTENDANCE SHEET

S.NONAME OF THE PARTICPANT 7-May-18 8-May-18 9-May-18 10-May-18 11-May-18 12-May												
	(FN)	(AN)		T				1		lay-18	t	lay-18
1 Dr MD RASHID	Rent	(AIN)	(FN)	(AN)	(FN)	(AN)	(FN)	(AN)	(FN)	(AN)	(FN)	(AN)
2 Dr A KISHORE REDDY	TAK 2	the	Renso	Kens	Bus	first	Raus	Rent	Ruz	Rut	Rust	King
3 Dr M SUSHANTH BABU	City	an	da	there	du	the	the	typ	1 Aug	tup	tw	the
4 Mr J PANDU RANGA RAO	the	thi	an	Al	dig	1 dil	and	the	an	di-	84	B
5 Mr P PRAVEEN RAJU	the second second		olf.	- LH	JA	JA	1 pm	JP4	Sille	121	the	n
6 Mr MAHESH KUSUMA	Payu.	Rey .	Rajy.		Rajy -	and the second s	Paju-	Pajy-	Refy?	Raju.	Bagy.	Bajy.
7 Mrs HEMLATHA DALMIA	Bine	Fahr	KAIC	Farm	Filly	AM	Killa	Fring	Bach	Km	Km	Nin
8 Mr MOHAMMED RAFI	ANG	1	1 Alle	110	the	the	the-	111-	the	the	the	ARE
9 Mr G D BASAVARAJ	M	MH	MAI	Mn	MAR	Mike	Min	Jamp.	mil	MA	nin	10
10 Mr K NARSIMHA	10 - D	17	m l	27	0	M	N		n	h	12-1	Pr
10 MI K NAKSIMHA 11 Ms M DIVYA	Mal	Alg	Ney	Ale	The	April	her	hat	Ma	1la	ner.	M
12 Mrs P CHAITANYA	Divya		12101	apros		Bug	Diva	Divy	DIVY	River	Divy	Ding
	Mailonya	clastone	cho Pronipo	Vatarya	Waitprop	chattonija	chaitanya	Waitania	Unitarye	chairania.	Usitanus	chartonys
13 Ms VUPPALA SWATHI 14 Mrs S PALLAVI	Who	Vino	Ven	4 pers	Yan	and the second division of the second divisio	VING	Vala	Agni	Gla	Vich	fin
	Sh	Spa	Ste	SP2	Sh	Sch	Sth	Sh	Sh	Sin	an	8m
15 Mr G RAMACHANDRA		Gler	Lek,	Cille	Geh	GK	C.M.	KIL	Cith	Cur	an	anha
16 Mrs NEHA ANJUM	ME	Ne	12 m	RY	1st	The	Pro-	D	Mo	M	A	Ashn
17 Mrs SHAIK ASHA BEE	AR	AND	Ada	AST	AST	ASE	AKE	Astat	AST	AT	AND	ASD
18 Mrs A SOWJANYA	Bau	HS	MS.	€n.	AL	AS	7-1	He	Ay	A	fre	M
19 Mrs D BINDU		Bull	Bindu	Rijon	Bindy	Rinder	Rindu	Bindu	Rindu	Bludy	Bindy	Bindy
20 Mrs C YAMUNA	Hanunga	your	yana	Youmana	Hanne	Haming	yamina	4 amina	yanura	yances	yanni	yamura
21 Mr K HARISH VARRMA	and	(ARS)	an	ant	as	AUG	ast	ast	QUN	QUL	Cult	QH
22 Mr R SURESH	ANDIN	Suver	Surat	Supert	-Suren	Superh	- Sunpil	Suren	Sweith	Surell	Sureil	Surver
23 Mr B PARSHURAMULU	44	(RU	Ry	Pr	Pu	FY	Spy	(Ky	Ru	Ry	Pre	Qu
24 Ms P SRI PALLAVI	Sur	Spr	Son	Sim	Ry	Sin	Est.	Gur	Sin	Sin	0	0.
25 Ms R SIRISHA	aly-	By-	Ber	Set	Ry	By	-Ref-	Ky	No.	Sin	ð.	l. I i
25 Mrs V JYOTHJ	Juelt		Type	Jup	The	The	Juo	Juo	Tuo	Juo	Jun	Sie Dert Jup
27 Mrs K TEJASWI	Leja	Teia	Tija	Tyja	Tein	Tila	Tela	Teía	Teia	TUD	Teia	TEIA
28 Mr B NAVEEN	var.	nue,	Nag.	have	have	none	nave	have	wie.	har	hang	have
29 Ms B SUSHANTHI	Suguet	Sugt	Kent	Abert	Rey	Rusz	Reeler	Roy	Put	Rent	Reett	lout
30 Ms T REVATHI	Repothi	Remathi	Brothe		Rowth	Baratti	Reighti	Reworthi	Routhi		Revathi	Reathi
31 Ms Y RADHIKA	Radhika	Radhiba	Radhika	Radhik	Raditike	eadhikg.	Rathity	Radhika	Ranhika	Pathika.	Raihila	Rodhika
32 Mr G ANIL KUMAR	AWT	Anot	Auil	Anil	-Anil	Anil	Anil	Anil	Busneen	FONDIN	ALT I	Anil
33 Ms M JHANSI	Fiert	Try	Front	Tout	Jay	- Tarl	· Tat	TEAR	FARE	EPART	T	
	0	U	- Creek	- Gran	05	June	Joseph	Out	ics & Comi	numbertion	England	Jent
								Judes	Huu Arany	j@srevas.a	cin	
								e-ma	II: ece.not		- week	

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	:	B. Naveen
2.	Designation	ċ	
3.	Education Qualification with Specialization	*	
4.	Department	•	ELE
5.	Institution	•	SIET
6.	Address for Correspondence with email and Mobile Number	:	Noveen. D@ Soreyas. ac in
7.	No. of refresher courses/workshops attended		
Place:	Nogole		

Date: 07-05-18

leen Signature of the applicant

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	: K. Tejosu?
2.	Designation	: Asst. Parob
3.	Education Qualification with Specialization	: Mitech
4.	Department	: ECE
5.	Institution	: SIET
6.	Address for Correspondence with email and Mobile Number	: Tejoswi. K@ Seeyos.in
7.	No. of refresher courses/workshops attended	
Place:	128010	

Date: 07-05-18

eju Signature of the applicant

.

SREYAS Institute of Engineering & Technology (Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	:	J. Fandy vanga vao
2.	Designation	:	J. Pandy Vanga Vao Assoc. prof.
3.	Education Qualification with Specialization	•	H. Jech
4.	Department	2	ECE
5.	Institution	:	SIET
6.	Address for Correspondence with email and Mobile Number	:	Fandurangarab. 50 sneyas. ac. in
7.	No. of refresher courses/workshops attended		Ap
Place:	Hyd		
Date:	-Hgd 07/5/2018		

Signature of the applicant

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	:	Dr. Chuslanth Babu
2.	Designation	1920	Dr. Chuslanth Babu. professor
3.	Education Qualification with Specialization	:	ph.D
4.	Department	•	ECE
5.	Institution	:	SS EC
6.	Address for Correspondence with email and Mobile Number	*	or. shushanthbabuQsveyas.ac.in
7.	No. of refresher courses/workshops attended		-
Place:	Hyd		
Date:	07/5/2018		



.

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	•	Dr. Rashid mahamored.
2.	Designation	:	profescor
3.	Education Qualification with Specialization	å	ph.O
4.	Department	•	ELE
5.	Institution	*	CLET
6.	Address for Correspondence with email and Mobile Number	4 9	Dr. Rachid nahamood Steppe. ac. in
7.	No. of refresher courses/workshops attended		F
Place:	Hyd		
Date:	0715/2018.		

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	2.5	A. Kishole reddy.
2.	Designation	2	Assoc. Prot
3.	Education Qualification with Specialization	:	Murech
4.	Department	ż	ECE
5.	Institution	•	SLET
6.	Address for Correspondence with email and Mobile Number	· !	A. Kishike reddy O sreyos. ac."in
7.	No. of refresher courses/workshops attended		-
Place:	etyd		
Date:	01/5/2018		

.

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	:	R. Shirisha
2.	Designation	•	Asst. Arub
3.	Education Qualification with Specialization	*	Asst. Prob M. Tech
4.	Department	:	ECE
5.	Institution	•	SIET
6.	Address for Correspondence with email and Mobile Number		Starisha. r@ greyas.
7.	No. of refresher courses/workshops		
	attended		
Place:	Norgole		

Date: 7/8/2018

Signature of the applicant

action

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	:	shiaike Asha bee
2.	Designation		Asst. Professor
3.	Education Qualification with Specialization	•	M. Tech
4.	Department	:	ECE

- 5. Institution
- Address for Correspondence with 6. email and Mobile Number

: SIET

: asha bee. 5 @ gmail: com

7. No. of refresher courses/workshops attended

.

Place: Hyderabad Date: 7/5/2015

.

.....

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	: P. Chraithanyra Neha Anjum
2.	Designation	Asst. Prot
3.	Education Qualification with Specialization	M.Tech
4.	Department	ECE
5.	Institution	SIET
	institution	
6.	Address for Correspondence with email and Mobile Number	: nehaonjum@gmail.com
7.	No. of refresher courses/workshops attended	
Place:	Hyderabad	
Date:	7 5 2018	

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	: S. Pallavi
2.	Designation	Asst. Professor
3.	Education Qualification with Specialization	: M.Tech
4.	Department	: ECE
5.	Institution	: SIET
6.	Address for Correspondence with email and Mobile Number	: pallowi.s@ sreyas.ac.ir
7.	No. of refresher courses/workshops attended	
Place:	Hydevabad	
Date:	7/5/2018	

Pallani Signature of the applicant

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	: V. Jyothi
2.	Designation	: V. Jyothi : Asst. Pr.S
3.	Education Qualification with Specialization	M.Tech
4.	Department	ECE
5.	Institution	SIET
6.	Address for Correspondence with email and Mobile Number	: Jyoth: N@ 87eyos.
7.	No. of refresher courses/workshops attended	-
Place:	Nagole	
	7/5/2018	

Signature of the applicant

ac.in

.

SREYAS Institute of Engineering & Technology (Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	:	R. Suresh
2.	Designation		R. Swesh Asst. Prot
3.	Education Qualification with Specialization	*	M. Tech
4.	Department	•	2 + C + L
5.	Institution	:	SIET
6.	Address for Correspondence with email and Mobile Number	•	Suresh R@ Sreyal, ac. in
7.	No. of refresher courses/workshops attended		
Place:	Hyd		
Date:	7/5/18		
			S

e

SREYAS Institute of Engineering & Technology (Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	•	Hemlertha. Datomia Asst. Prot
2.	Designation	:	Asst. Poot
3.	Education Qualification with Specialization	:	M- Tech
4.	Department	:	E. C. E
5.	Institution	•	SIGT
6.	Address for Correspondence with email and Mobile Number	•	Hemalatha. D@ Soeyas. ac. in
7.	No. of refresher courses/workshops attended		
Place:	Hyd		
Date:	7/5/18		

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	: Mahesh . K
2.	Designation	: Asst. Prof
3.	Education Qualification with Specialization	: M. Tech
4.	Department	: 2. 0-2
5.	Institution	SIET
~		

6. Address for Correspondence with email and Mobile Number

: Mahesh. K@ Stegas. ac. in

7. No. of refresher courses/workshops attended

715/18

Place: 14yd

Date:

Signature of the applicant

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	:	p. Prouleer	Rogu
2.	Designation		P. Prouleer Asst. Poot	0
3.	Education Qualification with Specialization	•	M. Tech	
4.	Department	÷	2. C. E	
5.	Institution		SIGT	
		•		
6.	Address for Correspondence with email and Mobile Number	•	Provision . D@	Steyas. ac. in
7.	No. of refresher courses/workshops attended			
Place:	Hyd			

Date: 7-15/18

.

P. Rocuert Signature of the applicant

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	:	A. Saujonya
2.	Designation	:	A. Saujonya A88t Porob
3.	Education Qualification with Specialization	:	m. Tech
4.	Department	÷	ECE
5.	Institution	•	SIET
6.	Address for Correspondence with email and Mobile Number	•	Sowjanya. a @ Saeyas. acin
7.	No. of refresher courses/workshops attended		р.
Place:	Nagole		

Date: 07-05-18

oujanja Signature of the applicant

.

SREYAS Institute of Engineering & Technology (Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	:	D. Bindu
2.	Designation	•	Asst. prot
3.	Education Qualification with Specialization		mitech
4.	Department	•	ELE
5.	Institution	•	SIET
6.	Address for Correspondence with email and Mobile Number	•	bindund @ severas, acrim
7.	No. of refresher courses/workshops attended		Emain,
Place:	Nagole		
	07-05-18		

4

SREYAS Institute of Engineering & Technology (Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	•	P. Sri Pallavi
2.	Designation	:	Asst. Prof
3.	Education Qualification with Specialization	•	M. Lech
4.	Department	ġ.	下 ひ モ
5.	Institution	•	SIET
6.	Address for Correspondence with email and Mobile Number	•	9966811797
7.	No. of refresher courses/workshops attended		r
Place:	7/5/18 Magole		
Date:	\$ 07/5/18		

.

SREYAS Institute of Engineering & Technology (Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	•	B. Parshusamulu.
2.	Designation		Arest-Phot.
3.	Education Qualification with Specialization	200	Mrech.
4.	Department	•	ELE
5.	Institution	•	SIET
6.	Address for Correspondence with email and Mobile Number	0167 •	Parshuramulu. 59@ gmail. com
7.	No. of refresher courses/workshops attended	-	
Place:	Hyderabad		
Date:	thyduabad OHS 2018.		

B. Pag Signature of the applicant

e

SREYAS Institute of Engineering & Technology (Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	: (- Kamuna
2.	Designation	:	Asst-playersor
3.	Education Qualification with Specialization	:	Mrech.
4.	Department	:	ECE
5.	Institution	:	SIET
6.	Address for Correspondence with email and Mobile Number	:	yamuna. C@ bleyasaciin 9848028812
7.	No. of refresher courses/workshops attended		
Place:	Hy duabad		
Date:	Hyduabad D71812018		

Signature of the applicant

.

SREYAS Institute of Engineering & Technology (Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	Mohammed. Ratti
2.	Designation	Asst. Prof
3.	Education Qualification with Specialization	M. Lech, VLSI
4.	Department	EC-E
5.	Institution	SIET
6.	Address for Correspondence with email and Mobile Number	: 99 mohammedrattie grait com
7.	No. of refresher courses/workshops attended	-
Place:	Hyderabad 715/18	
Date:	715/18	

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	:	G. D. Basava Raj
2.	Designation		Asst. Prof
3.	Education Qualification with Specialization		M. tech
4.	Department	:	E-C.E
5.	Institution	:	SIET
6.	Address for Correspondence with email and Mobile Number	•	70130 08702
7.	No. of refresher courses/workshops		-
	attended		
Place:	Hydelabad 07/05/18		
Date:	09105118		

Signature of the applicant

1

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	:	K. Narsimha
2.	Designation	:	Asst. Prof
3.	Education Qualification with Specialization		M. tech
4.	Department	•	ECE
5.	Institution	•	STET
6.	Address for Correspondence with email and Mobile Number	:	7382491153
7.	No. of refresher courses/workshops attended		-
Place:	Hyderabad 07 15/18		
Date:	07/5/18		

K, Nog Signature of the applicant

.

SREYAS Institute of Engineering & Technology (Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

and the second

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	:	p. chaitanya
2.	Designation	•	P. Chaitanya A88t. p9106
3.	Education Qualification with Specialization	:	M. Tech
4.	Department	\$ •	ECE
5.	Institution	2	SIET
6.	Address for Correspondence with email and Mobile Number	а <u>т</u> .	Chattanya.p @ Soreyas.ac.in
7.	No. of refresher courses/workshops attended		
Place:	Nagula		
Date:	07-05-18		

chaitu Signature of the applicant

.

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	:	m. Divya
2.	Designation		ASSt. prob
3.	Education Qualification with Specialization	÷	Mitech
4.	Department	•	ECE
5.	Institution	•	SIET
6.	Address for Correspondence with email and Mobile Number	•	9848788993
7.	No. of refresher courses/workshops attended		Para.
Place:	Nagole		
Date:	Nagole 07-05-18		

Divya. Signature of the applicant

4

...

SREYAS Institute of Engineering & Technology (Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	:	V. Swathi
2.	Designation		Agge.ponob
3.	Education Qualification with Specialization	•	mitech
4.	Department	•	ECE
5.	Institution	•	SIET
6.	Address for Correspondence with email and Mobile Number	540 -	Swathi. Vuppala @ Saeyos. ac. in
7.	No. of refresher courses/workshops attended		~
Place:	Nogole 07-05-18		
Date:	07-05-18		

Swathi Signature of the applicant

.

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	•	G. Ramachandra Kyman
2.	Designation	1	G. Ramachandra Kymey Asst-professor
3.	Education Qualification with Specialization	:	MTech Et
4.	Department	:	ECE
5.	Institution	•	SIET
6.	Address for Correspondence with email and Mobile Number	:	ramachandra.g. @ Breyas.ac.in
7.	No. of refresher courses/workshops attended	-	
Place:	Hyderabad		
Date:	Hyderabad. 07/5/2018-		

Signature of the applicant

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Registration Form

1	Name	;	K. Horlsh Worng.
2.	Designation	*	Asst. prof.
3.	Education Qualification with Specialization	:	M. Tech (Embedded System)
4.	Department	•	ECE
5.	Institution	:	Steyas institute of Engineering & Technology.
6.	Address for Correspondence with email and Mobile Number	:	harlshvanna Sreyas.ac.in
7.	No. of refresher courses/workshops attended		
Place:	Hydelabad.		

Signatur

the applicant

Date: 07/05/2018.

ą

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

- 1. Course Content
- 2. Course Material

3. Hospitality Arrangements

- 4. Practical Sessions
- 5. Most interesting part of the workshop

namo Electronics

6. Any Suggestions

Name of the Participant

Signature of the Participant

SPET Name of the Organization

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

1. Course Content

43

2. Course Material

3. Hospitality Arrangements

- 4. Practical Sessions
- 5. Most interesting part of the workshop

namo Electronics

6. Any Suggestions

Name of the Participant Rate

Signature of Participant

Name of the Organization

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

- 1. Course Content
- 2. Course Material

3. Hospitality Arrangements

- 4. Practical Sessions
- 5. Most interesting part of the workshop

nano Electronice

6. Any Suggestions

by. Rachid Mahawood Name of the Participant

Signature of the Participant

Name of the Organization

Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

1.	Course Content	Excellent/Good/Poor
2.	Course Material	Excellent/Good/Poor
3.	Hospitality Arrangements	Excellent/Good/Poor
4.	Practical Sessions	Excellent/Good/Poor
5.	Most interesting part of the workshop	Nano Electronics

6. Any Suggestions

Kitejoswi Name of the Participant

Signature of the Participant

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

- 1. Course Content
- 2. Course Material

3. Hospitality Arrangements

4. Practical Sessions

5. Most interesting part of the workshop

6. Any Suggestions

Name of the Participant

Signature of the Participant

S)ET Name of the Organization

Speech perocepsing

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

1. Course Content

÷

- 2. Course Material
- 3. Hospitality Arrangements
- 4. Practical Sessions
- 5. Most interesting part of the workshop

Speech processing

6. Any Suggestions

Name of the Participant

Signature of the Participant

Name of the Organization

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

- 1. Course Content
- 2. Course Material
- 3. Hospitality Arrangements
- 4. Practical Sessions
- 5. Most interesting part of the workshop

6. Any Suggestions

R. Shivisha Name of the Participant

Signature of the Participant

SIET Name of the Organization Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor

Nano electronicy



Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

- 1. Course Content
- 2. Course Material
- 3. Hospitality Arrangements
- 4. Practical Sessions
- 5. Most interesting part of the workshop

6. Any Suggestions

Sharbe Asherbee Name of the Participant

Signature of the Participant

SIET Name of the Organization Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor

Power Redection

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

- 1. Course Content
- 2. Course Material

3. Hospitality Arrangements

4. Practical Sessions

5. Most interesting part of the workshop

6. Any Suggestions

Neha Anjum Name of the Participant

Signature of the Participant

SLET Name of the Organization Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor

Nano electronics



Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

- 1. Course Content
- 2. Course Material
- 3. Hospitality Arrangements
- 4. Practical Sessions
- 5. Most interesting part of the workshop

6. Any Suggestions

S. Pallav! Name of the Participant

Signature of the Participant

SIET Name of the Organization Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor

Verilog programming

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

- 1. Course Content
- 2. Course Material
- 3. Hospitality Arrangements
- 4. Practical Sessions
- 5. Most interesting part of the workshop

6. Any Suggestions

V. Tyoth' Name of the Participant

Signature of the Participant

SIET Name of the Organization

Nono electroniz



Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

Excellent/Good/Poor

Excellent/Good/Poor

Excellent/Good/Poor

Excellent/Good/Poor

Speech Processing

- 1. Course Content
- 2. Course Material
- 3. Hospitality Arrangements
- 4. Practical Sessions
- 5. Most interesting part of the workshop
- 6. Any Suggestions

Name of the Participant

: R. Suresh

Signature of the Participant

8

SIET

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

ŕ

- 1. Course Content
- 2. Course Material
- 3. Hospitality Arrangements
- 4. Practical Sessions
- 5. Most interesting part of the workshop

6. Any Suggestions

Name of the Participant

: Hemba tha. Dalmia

Excellent/Good/Poor

Excellent/Good/Poor

Excellent/Good/Poor

Excellent/Good/Poor

Speech Processing

Signature of the Participant

7

SLET

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

1. Course Content

÷

- 2. Course Material
- 3. Hospitality Arrangements
- 4. Practical Sessions
- 5. Most interesting part of the workshop
- 6. Any Suggestions

Name of the Participant

P. Poalen Payy

Signature of the Participant

P. Poaul

SIET

Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor

Veorlog Poogsamme



Hyderabad | PIN:500068

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

Excellent/Good/Poor

Excellent/Good/Poor

Excellent/Good/Poor

Excellent/Good/Poor

Nono Electronics

- 1. Course Content
- 2. Course Material
- 3. Hospitality Arrangements
- 4. Practical Sessions
- 5. Most interesting part of the workshop

6. Any Suggestions

Name of the Participant

Mahesh. K

Signature of the Participant

SIET

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

1.	Course Content	Excellent/Good/Poor
2.	Course Material	Excellent/Good/Poor
3.	Hospitality Arrangements	Excellent/Good/Poor
4.	Practical Sessions	Excellent/Good/Poor
5.	Most interesting part of the workshop	Nano Electronica

6. Any Suggestions

÷

A. Sow Jonyp Name of the Participant

Signature of the Participant

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

1. Course Content

÷

- 2. Course Material
- 3. Hospitality Arrangements
- 4. Practical Sessions
- 5. Most interesting part of the workshop

6. Any Suggestions

D. Birdy Name of the Participant

Signature of the Participant

SIET Name of the Organization

porogrammente Venilog



Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

- 1. Course ContentExcellent/Good/Poor2. Course MaterialExcellent/Good/Poor3. Hospitality ArrangementsExcellent/Good/Poor4. Practical SessionsExcellent/Good/Poor
- 5. Most interesting part of the workshop

Nano Electronics

6. Any Suggestions

÷

P. Sri Name of the Participant

Signature of the Participant



(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

- 1. Course Content
- 2. Course Material

3. Hospitality Arrangements

4. Practical Sessions

5. Most interesting part of the workshop

6. Any Suggestions

Name of the Participant

B. Payshyzampila.

Signature of the Participant

B. Pag

Name of the Organization

SIET

Hyderabad | PIN:500068

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

1. Course Content

e

2. Course Material

3. Hospitality Arrangements

4. Practical Sessions

5. Most interesting part of the workshop

Nano Electronice

6. Any Suggestions

Name of the Participant

C-Yamuma.

Signature of the Participant

C.Y

Name of the Organization

SLET

Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

1.	Course Content	Excellent/Good/Poor
2.	Course Material	Excellent/Good/Poor
3.	Hospitality Arrangements	Excellent/Good/Poor
4.	Practical Sessions	Excellent/Good/Poor
~		

5. Most interesting part of the workshop

Speech process.

6. Any Suggestions

ę

Name of the Participant

Signature of the Participant

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

1. Course Content

÷

- 2. Course Material
- 3. Hospitality Arrangements
- 4. Practical Sessions
- 5. Most interesting part of the workshop

6. Any Suggestions

G. D. Basava Ray Name of the Participant

Signature of the Participant

Name of the Organization

case study

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

Excellent/Good/Poor

Excellent/Good/Poor

Excellent/Good/Poor

Excellent/Good/Poor

- 1. Course Content
- 2. Course Material

3. Hospitality Arrangements

4. Practical Sessions

5. Most interesting part of the workshop

Nano Electronics

6. Any Suggestions

K. Marsimha Name of the Participant

Signature of the Participant

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

1.	Course Content	Excellent/Good/Poor
2.	Course Material	Excellent/Good/Poor
3.	Hospitality Arrangements	Excellent/Good/Poor
4.	Practical Sessions	Excellent/Good/Poor
5.	Most interesting part of the workshop	Namo electronics

6. Any Suggestions

ę,

P. Chaitanya Name of the Participant

Signature of the Participant

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

1. Course Content	Excellent/Good/Poor
2. Course Material	Excellent/Good/Poor
3. Hospitality Arrangements	Excellent/Good/Poor
4. Practical Sessions	Excellent/Good/Poor
5. Most interesting part of the workshop	Case Studies

6. Any Suggestions

÷

M · Divya Name of the Participant

Divya Signature of the Participant



(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

- 1. Course Content
- 2. Course Material
- 3. Hospitality Arrangements
- 4. Practical Sessions
- 5. Most interesting part of the workshop

6. Any Suggestions

 $\bigvee \cdot S$ Swathi Name of the Participant

Signature of the Participant

SIET Name of the Organization Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor

Verilog porogramming

(Approved by AICTE, Delhi | Affiliated to JNTUH, Hyderabad | Accredited by NAAC) Hyderabad | PIN:500068

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

- 1. Course Content
- 2. Course Material
- 3. Hospitality Arrangements
- 4. Practical Sessions
- 5. Most interesting part of the workshop

6. Any Suggestions

Name of the Participant

G. Ramachandra Kumar

Signature of the Participant

G. Kerf

Name of the Organization

SET

Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor

Hyderabad | PIN:500068

Department of Electronics and Communication Engineering

Workshop on Emerging Trends in VLSI & Signal Processing

07-05-2018 to 12-05-2018

Feedback Form

- 1. Course Content
- 2. Course Material
- 3. Hospitality Arrangements
- 4. Practical Sessions
- 5. Most interesting part of the workshop

6. Any Suggestions

Name of the Participant K. Harrish Varma.

Signature of the Participant

Name of the Organization

Steyay Institute of Engineering & Technology

Excellent/Good/Poor Excellent/Good/Poor Excellent/Good/Poor

Design process.



Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mrs K TEJASWI

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

of

SIET

Asureth Principal

PRINCIPAL SREYAS INSTITUTE OF ENGG. & TECH 9-39, Sy. No. 107, Tattiannaram (V), GSI, Bandlaguda, Nagole, Hyd-68



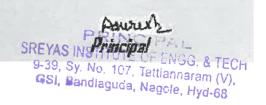
Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mr B NAVEEN

of SIET

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

5 man Head of the Department





Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Ms B SUSHANTHI

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

of

SIFT

Daurext Principa

PRINCIPA SREYAS INSTITUTE CAENGG, & TECH 9-39, Sy. No. 107, Tettlannaram (V), GST, Bandiaguda, Nagole, Hyd-68



Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Ms T REVATH!

of SIET

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

5 von un Head of the Department

Querex 9-39, Sy. No. 107, Tattiannaram (V), GSI, Bandlaguda, Nagole, Hyd-68

Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Ms Y RADHIKA

of SIET

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

2 toron in Head of the Department

SREYAS INSTITUTE OF ENGG. & TECh 9-39, Sy. No. 107, Tattismaram (V), GSI, Bandiaguda, Nagole, hyu-64



Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mr G ANIL KUMAR

of SIET

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

15 von nu Head of the Department

Awresh Principa

PRINCIPAL SREYAS INSTITUTE OF ENGG. & TECH 9-39, Sy. No. 107, Tattiannaram (V), GSI, Bandlaguda, Nagole, Hyd-68

Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Ms M JHANSI

of SIET

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

suresh RINCIPAL SREYAS INSTITUTE OF ENGG. & TECH

9-39, Sy. No. 107, Tattiannaram (V), GSI, Bandlaguda, Nagole, Hyd-68



Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Dr MD RASHID MAHMOOD of SIFT

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

9-39, Sy. No. 107, Tattiannaram (V), GSI, Bandlaguda, Nagole, Hyd-68



Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Dr A KISHORE REDDY

of SIET

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

SREYAS INSTITUTE OF ENGG. & TECh g-39, Sy. No. 107, Tattiannaram (V), gSI, Bandlaguda, Nadole, Hyd-68

Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Dr M SUSHANTH BABU

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

of

SIET

SREYAS INSTITUTE OF ENGG. & TECH 9-39, Sy. No. 107, Tattiannaram (V), GSI, Bandlaguda, Nagole, Hyd-68

Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mr J PANDU RANGA RAO of SIET

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

SREYAS INSTITUTE OF ENGG, & TECH 9-39, Sy. No. 107, Tattiannaram (V), GSI, Bandiaguda, Negole, Hyd-68



Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mr P PRAVEEN RAJU

of SIET

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

Principal



Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mr MAHESH KUSUMA

of SIET

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

15 mm Head of the Department

Daurest

9-39, Sy. ror, resummer diff (v), GSI, Bandlaguda, Nagole, Hyd-68.

Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mrs HEMLATHA DALMIA of SIFT

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

SREYAS INSTITUTE OF ENGG. & TECH 9-39, Sy. No. 107, Tattiannaram (V), GSI, Bandieguua, Nagola, Hyd-68

Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mr MOHAMMED RAFI SHAIK of SIFT

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

surech PRINCIPAL SREYAS INSTITUTE OF ENGG. & TECH 9-39, Sy. No. 107, Tattiannaram (V), GSI, Bandlaguda, Nagole, Hyd-68



Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mr G D BASAVARAJ

of SIET

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

9-39, Sy. No. 107, Tattiannarem GSI. Bandlaguda, Nugale, Hye-ig



Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mr K NARSIMHA

of SIET

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

SREYAS INSTITUTE OF ENGG. & TECH 9-39, Sy. No. 107, Tattiannaram (M), GSI, Bandlaguda, Nagole, Hyd-68

Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Ms M DIVYA

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

2 - a man Head of the Department

of

SIFT

Deurexh

SREYAS INSTITUTE OF ENGG. & TECH 9-39. Sy. No. 107, Tattiannaram (V), GSI, Bandlaguda, Nagole, Hyd-6a

Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mrs P CHAITANYA

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

of

SIFT

9-39, Sy. No. 107, Tattiannaram (V). GSI, Bandlaguda, Nagole, Hyd-68

Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Ms VUPPALA SWATHI of SIFT

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

Sauresh Principa

PRINCIPAL SREYAS INSTITUTE OF ENGC. & TECH 9-39, Sy. No. 107. Tattlannaram (V). GSI, Bandiaguos, Nagola, Hyd-68



of

SREYAS INSTITUTE OF ENGINEERING AND TECHNOLOGY (Approved by AICTE, New Delhi | Affiliated to JNTU, Accredited by NAAC, Hyderabad) Beside Indu Aranya, GSI, Bandlaguda, Nagole, Hyderabad, 500068

Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mrs S PALLAVI

SIFT has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department





Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mr G RAMACHANDRA KUMAR of SIET

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

NGG. & TECH 9-39, Sy. No. 107, Tellisinarum (V), GSI, Bandlaguda, Nagole, Hyd-68

Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mrs NEHA ANJUM

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

of

SIFT

Querexh

SREYAS INSTITUTE OF ENGG. & TECH 9-39, Sy. No. 107, Tattiannaram (V), GSI, Bandlaguda, Nagole, Hyd-63



Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mrs SHAIK ASHA BEE

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

of

SIET

Awres Principa

PRINCIPAL SREYAS INSTITUTE OF ENGG. & TECH 9-39, Sy. No. 107, Tattiannaram (V), GSI, Bandlaguda, Nagole, Hyd-68



of

SIET

SREYAS INSTITUTE OF ENGINEERING AND TECHNOLOGY (Approved by AICTE, New Delhi | Affiliated to JNTU, Accredited by NAAC, Hyderabad) Beside Indu Aranya, GSI, Bandlaguda, Nagole, Hyderabad, 500068

Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mrs A SOWJANYA

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

Aurus

SREYAS INSTITUTE OF ENGG. & TECH 9-39, Sy. No. 107, Tattiannoram (V), GSI, Bandlaguda, Nagole, Hyd-68



Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mrs D BINDU

of SIET

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

SREYAS INSTITUTE OF ENGG. & TECH 9-39, Sy. No. 107, Tattiannaram (V), GSI, Bandlaguda, Nagole, Hyd-68



Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mrs C YAMUNA

of SIFT

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

Asurech

SREYAS INSTITUTE OF ENGO. & TECH 9-39, Sy. No. 107, Tattiannaram (V), GSI, Bandiaquine, Magnie, Hyd-68



Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mr K HARISH VARRMA

of SIET

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

SREYAS INSTITUTE OF ENGG. & TECH PRINCIPA

9-39, Sy. No. 107, Tattiannaram (V). GSI, Bandlaguda, Nagole, Hyd-Sa



Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mr R SURESH

of SIET

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

SREYAS INSTITUTE OF ENGG. & TECH 9-39, Sy. No. 107, Tattiannaram (V), GSI, Bandlaguda, Nagole, Hyd-68



Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mr B PARSHURAMULU

of SIET

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

SREYAS INS ENGG. & TECH 9-39, Sy. No. 107, Tatliannaram (V), GSI, Bandiaguda, Nagole, Hyd-68



Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Ms P SRI PALLAVI

of <u>ster</u> has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Ms R SIRISHA

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

of

SIFT

9-39, Sy. No. 107, Tattiannaram (V), ASI, Bandlaguda, Nagole, Hyd-68



Certificate of Participation

This is to certify that Mr/Ms/Dr/Prof Mrs V JYOTHI

has participated One-week Workshop on Emerging Trends in VLSI and Signal Processing organized by the department of Electronics and Communication Engineering held at Sreyas Institute of Engineering and Technology from 07th to 12th May 2018.

Head of the Department

of

SIFT

America

SREYAS INSTITUTE OF ENGG. & TECH 9-39, Sy. No. 107, Tattiannoram (M GSI, Bandlaguda, Megole, Kyd.60