

FACULTY DEVELOPMENT PROGRAMME (FDP)

DEEP LEARNING AND IT'S APPLICATIONS

(10-12-2018 to 14-12-2018)

Organized by

SREYAS INSTITUTE OF ENGINEERING AND TECHNOLOGY

Preamble:

Deep learning methods are now prevalent in the area of machine learning, and are now used invariably in many research areas. The influx of research articles in this area demonstrates that these methods are remarkably successful at a diverse range of applications, namely self driving cars, new kinds of video games, Artificial Intelligence, Automation, object detection and recognition, surveillance tracking etc.

Major Course Contents:

This FDP aims at providing introductory concepts of deep learning in the areas of Computer Vision and NLP. The FDP covers important core-concepts in deep learning such as CNNs, RNNs, GANs, VAEs. All the concepts taught in the FDP are complemented with hands on training using state of the art deep learning frameworks.

Faculty conducting this programme:

The programme will be conduted by the faculty members of Sreyas Institute of Engineering and Technology. Speakers from industries are also expected to deliver as part of the course.

Speakers:

Dr.N.Venkatrao(TKR Engineering College) Venkateshwar Reddy Ramesh Paka(Industry)

Registration Fee Particulars:

Faculty and Research Scholars	Rs. 1500/-
Industry Participants	Rs. 2000/-

Organized at:

The programme will be coordinated and conducted by Department of Computer Science and Engineering .Sreyas institute Engineering and Technology, Hyderabad.

Accommodation:

All the selected participants will have to make their own arrangements for their accommodation. No TA will be paid for the participants.

How to apply:

A filled in form of application in the prescribed format duly signed and sponsored by appropriate authorities (along with demand draft) should reach the coordinator by speed-post. It is also mandatory to send scanned application form and demand draft through e-mail to cse.hod@sreyas.ac.in selection will be intimated only through mail.

Selection Criteria:

Selection will be done based on first-come-first-serve basis to a maximum number of 50 (fifty). Additionally 10 participants from industry are allowed to participate. The list of selected participants will be intimated through e- mail. In case a candidate is not selected, the DD will be sent back. Candidates will be issued certificates on successful completion of the course. Reservations are followed for selecting candidates as per GOI norms.

Important dates:

	Last date (Application & DD)	5 th December, 2018		
	Selection List by E- mail	09th December ,2018		
	Duration	10 th December to 14 th December 2018		

About SIET:

Sreyas Institute of Engineering & Technology was established in the year 2011 under the agencies of Sreyas Educational Society. The Institute endeavors to impact high quality, competency based technical education in Engineering and Technology to students with skills and abilities to face challenging needs of industry and society. The institute is approved by all Indian council for Technical Education(AICTE), New Delhi and affiliated to JNTUH

About CSE Department:The Department of Computer Science and Engineering Established in the year 2011. The major objective of Department is to impact high quality Education and research. The department of CSE is an intellectually challenging discipline.Our Students undergo entrepreneurship and inventiveness workshops and coding sessions which strive to be the most innovative in the world. The infrastructure is absolutely world-class with opportunities to build practical skills



APPLICATION FORM $\label{eq:faculty} \mbox{ FACULTY DEVELOPMENT PROGRAMME (FDP) } \\ \mbox{ ON }$

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1. Name	:						
2. Designation	:		SPONSORSHIP CERTIFICATE				
3. Institution	:		Dr. /Mr. /Ms is an				
4. Email	:		employee of our Institute/Organization and is hereby sponsored to participate in the FDP on "Deep				
5. DD No:	Bank:	Date:	Learning and it's Applications" during 10 th December to 14 th December 2018 at Sreyas Institute of Engineering and Technology, Hyderabad				
Amount:			3,7,7				
6. Address for Correspondence:							
			Signature of Head of the Institution (with seal)				
7. Educational	Qualification:		Address for correspondence				
8. Subjects ta	ught so far:		Post your application form with DD to				
10. Experience	, ,	·	Dr.Rohit Raja, Professor Department of Computer Science and Engineering Sreyas Institute of Engineering and Technology Hyderabad-500068, Telangana, India				
Teaching:	Research:	Industry:					
Declaration			E-mail the scanned copies of filled-in and duly signed application form along with DD to cse.hod@sreyas.ac.in				
The information	•	to the best of my					
-	-	abide by the rules and the course for the					
		responsibility to inform to attend the course.					
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	Ар	plicant					

Coordinators

Dr. Rohit Raja

Professor

Dept of C.S.E.

Sreyas Institute of Engineering and Technology

Nagole, Hyd-500068

Telangana State, India.

G.Sravan Kumar

Assoc.Prof

Dept of C.S.E.

Sreyas Institute of Engineering and Technology

Nagole, Hyd-500068

Telangana State, India.



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PURPOSE OF FDP

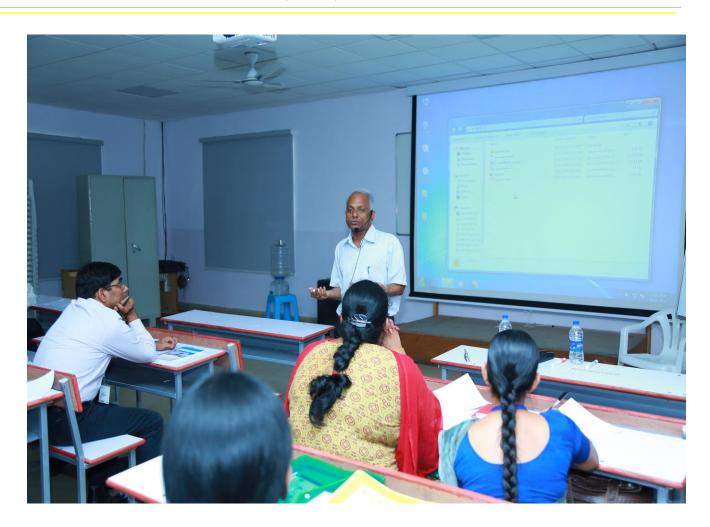
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RESOURCE PERSONS PROFILE

Dr. N. Venkat Rao

Highlights:

- ❖ NBA and NAAC work Expertise
- ❖ APSET, 2012 Qualified
- **❖** Team Work Skills
- ❖ Edited one book entitled "RECENT TRENDS IN WIRELESS NETWORKS".

Educational Qualifications:

Ph.D from Vikrama Simhapuri University, SPSR Nellore, A.P-2016.

Professional Experience: 10 Years

- September 17th to till date working as Professor, TKREC, Hyderabad.
- September, 2010 to 26.08.16 worked as Research Scholar, Vikrama Simhapuri University, Nellore, A.P
- June, 2007 to June 2008 worked as Asst. Professor, NBKRIST, Vidyanagar.
- June 2006 to June 2007 worked as Lecturer, DRW College, Gudur.
- June 2005 to June 2006 worked as Programmer, ASCET.





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Venkateshwar Reddy

Assoc Professor

Mr. Venkateshwar Reddy received his Bachelor of Science (Computer Science) from Osmania University, Hyderabad in 1997, a Master of Science (Computer Science) from Osmania University, and Hyderabad in 2000, Master of Science (Statistics) from Osmania University, Hyderabad in 2007 and Master of Technology (CSE) from Osmania University, Hyderabad in 2011. His initial academic appointment was at Hyderabad Presidency P.G. Centre, Hyderabad in 2000. He moved to the Department MCA at St. Anthony's P.G. College in 2003, served as Assistant Professor from 2003 to 2005, Later Moved to Department of MCA at Lokamanya Tilak P.G. College in 2005 as Assistant Professor and served as Associate Professor and also as Head of Department from 2005 to 2011. In September 2011, he joined the Department of Computer Science & Engineering at Anurag Group of Institutions, Hyderabad as Associate Professor. He was awarded as outstanding performance in Advanced Certification in Artificial Intelligence and Machine Learning Organized by Outreach Division of IIIT-Hyderabad in Association with Talentsprint from Jan'2019 to April'2019. He was also qualified FET (Faculty Eligibility Test) conducted by JNTUH in 2012.

Research Interests

Mr. Venkateshwar Reddy research interests revolve around Machine Learning and Artificial Intelligence and its impacts to applications ranging from transportation, business management, health care, agriculture, scientific and market analysis, natural disasters and biometric applications to engineering design and science exploration. Due to the wide availability of huge amounts of data in Web and the imminent need for turning such data into useful information and knowledge, and approaches for discovering useful patterns. Another area of interest would be related to design and implement new systems in Neural Networks, Image Processing and Parallel Algorithms to better respond to diversity in both hardware and application demands.



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Teaching Interests

Venkateshwar Reddy has taught undergraduate classes C and Data Structures, Probability & Statistics, Computer Organization, Discrete Mathematics, Operation Research, Formal Language and Automata Theory, Database Management Systems, Design Analysis and Algorithms, Artificial Intelligence, Machine Learning, Digital Image Processing, Computer Graphics, Data Warehousing & Data Mining, Geographical Information Systems and COBOL.

Education

- 2008-2011 M. Tech(CSE) from U.C.E(A), Osmania University, Andhra Pradesh
- J 2005-2007 Master of Science in Statistics, PGRRCDE College, Osmania University
- 1998-2000 Master of Science in Computer Science, Sai-Sudhir P.G. College, Osmania University
- J 1994-1997 Bachelor of Computer Science, Pragati MahaVidyalaya, Osmania University





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3. Ramesh Paka(Industry)

Ramesh Pakam is a Software Developer who has more than 6 years of experience in software development. He is having an extensive knowledge on both front- end and Back-end technologies, Good exposure on End to End deliverables. He started his career as Associate Software Developer for CGI Inc., worked as a java developer for 3.6 years while at CGI Inc, he was part of Banking and Financial Business unit where he was working directly with some of the prominent Financial Institutions of Canada like TD Bank, Scotia Bank.

Later he joined in Cognizant as Senior Full Stack Developer in Networking Domain where he worked with Verizon for 1 year. He is currently working in GAP Inc., Multinational Clothing company as a Senior Business System Analyst as Cloud Enablement Developer for their existing E-com Platform

His technical expertise includes Java, Angular/Angular Js, Spring Frame work (Spring Boot, Spring Web, Spring Security, Spring JDBC, Spring Cloud), REST, Microservices, SQL, Jenkins, Pivotal Cloud Foundry, Azure (Devops, Data Factory).

