

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

**CLASS TIME TABLE**

**Course: II Year B.Tech – I Semester**  
**Room No:IB-314**

**Section: ECE-A**

**Academic Year: 2020-21**  
**W.e.f: 18.10.2021**

	1	2		3	4		5	6	7
<b>Day/ Time</b>	<b>9.00- 10.00</b>	<b>10.00- 10.50</b>	10.50- 11.00	<b>11.00- 11.50</b>	<b>11.50- 12.40</b>	<b>L U N C H  B R E A K</b>	<b>1.20- 2.10</b>	<b>2.10- 3.00</b>	<b>3.00- 3.50</b>
<b>MON</b>	PTSP	SS	<b>B R E A K</b>	NATL	DSD		←-----BS LAB-----→		
<b>TUE</b>	EDC	EDC		DSD	PTSP		NATL	NATL	LIBRARY
<b>WED</b>	SS	SS		PTSP	EDC		←----- EDC/DSD LAB -----→		
<b>THU</b>	DSD	EDC		NATL	SS		PTSP	PTSP	MENTORING
<b>FRI</b>	NATL	EDC		SS	DSD		←----- EDC/DSD LAB -----→		
<b>SAT</b>	COI	SS		NATL	PTSP		EDC	DSD	SPORTS

Course code	Short Name of the Course	Course Name	Name of the faculty
<b>EC301PC</b>	<b>EDC</b>	Electronic Devices and Circuits	Mrs.M.Pavani
<b>EC302PC</b>	<b>NATL</b>	Network Analysis and Transmission Lines	Mr. G. D Basavraj
<b>EC303PC</b>	<b>DSD</b>	Digital System Design	Ms. Ch.Neetha
<b>EC304PC</b>	<b>SS</b>	Signals and Systems	Dr.J .Panduranga Rao
<b>EC305ES</b>	<b>PTSP</b>	Probability Theory and Stochastic Process	Mr. B.Sreenivasu
<b>EC306PC</b>	<b>EDC LAB</b>	Electronic Devices and Circuits Lab	Mrs. M.Pavani, Mrs. Sd. Shameem
<b>EC307PC</b>	<b>DSD LAB</b>	Digital System Design Lab	Ms. Ch.Neetha, Mr. Y. Varaprasad
<b>EC308ES</b>	<b>BS LAB</b>	Basic Simulation Lab	Mr. G. D Basavaraj , Mr. K. Harshavardhan Reddy
<b>*MC309</b>	<b>CoI</b>	Constitution of India	Ms. D. Jyoshna
<b>CLASS IN-CHARGE</b>			Mrs. M. Pavani

**Time Table Coordinator**

**HOD**

**Principal**

**Department of Electronics & Communication Engineering**  
**CLASS TIME TABLE**

**Course: II Year B.Tech – I Semester**  
**Room No:IB-315**

**Section: ECE-B**

**Academic Year: 2021-22**  
**W.e.f: 18.10. 2021**

	1	2		3	4	L U N C H  B R E A K	5	6	7	
<b>Day/ Time</b>	<b>9.00- 10.00</b>	<b>10.00- 10.50</b>	10.50- 11.00	<b>11.00- 11.50</b>	<b>11.50- 12.40</b>			<b>1.20- 2.10</b>	<b>2.10- 3.00</b>	<b>3.00- 3.50</b>
<b>MON</b>	EDC	DSD	B R E A K	PTSP	PTSP			SS	NATL	NATL
<b>TUE</b>	DSD	SS			NATL		EDC	←----- EDC/DSD LAB -----→		
<b>WED</b>	NATL	PTSP			DSD		SS	←-----BS LAB-----→		
<b>THU</b>	SS	SS			PTSP		DSD	EDC	EDC	MENTORING
<b>FRI</b>	CoI	SS			PTSP		EDC	DSD	NATL	LIBRARY
<b>SAT</b>	PTSP	←----- EDC/DSD LAB -----→					NATL	EDC	SPORTS	

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EC301PC	EDC	Electronic Devices and Circuits	Mrs.M.Pavani
EC302PC	NATL	Network Analysis and Transmission Lines	Mr. G. D Basavraj
EC303PC	DSD	Digital System Design	Ms. Ch.Neetha
EC304PC	SS	Signals and Systems	Dr.J .Panduranga Rao
EC305ES	PTSP	Probability Theory and Stochastic Process	Mr. B.Sreenivasu
EC306PC	EDC LAB	Electronic Devices and Circuits Lab	Mrs. M.Pavani, Mrs. Sd. Shameem
EC307PC	DSD LAB	Digital System Design Lab	Ms. Ch.Neetha, Mr. Y. Varaprasad
EC308ES	BS LAB	Basic Simulation Lab	Mr. G. D Basavaraj , Mr. K. Harshavardhan Reddy
*MC309	CoI	Constitution of India	Ms. D. Jyoshna
<b>CLASS IN-CHARGE</b>			Mr. G. D Basavraj

**Time Table Coordinator**

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