

### DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

### EXPERTS FEEDBACK ON INNOVATIVE METHODS IN TEACHING AND LEARNING PROCESS

Please fill out this feedback form so we ensure quality methodologies in teaching and learning process

Email ID: Pavan1.M@Sreyas.ac.in

NAME OF THE EXPERT: M. Pavan1

Innovative Method: One minute paper.

Parameters/Ratings	Excellent	Good	Fair	Poor
How would you rate this innovative method?	✓			
How satisfied were you with the clear goals?		✓		
How satisfied were you with appropriate usage?		✓		
How satisfied were you with implementation?	✓			
How satisfied were you with outcomes?		✓		
How satisfied were you with the timeliness?	✓			
Would you recommend our innovative method to others?	✓ YES/NO			
Please provide any additional comments or suggestions.	Good innovative teaching methods			



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

INNOVATIONS BY THE FACULTY IN TEACHING & LEARNING

EXPERT REVIEW ANALYSIS & ACTION TAKEN REPORT

Academic Year: 2024-2025

Year: IV

Semester: II

Section:

Faculty Name:

Gravanthi/Praveen

Subject & Topic:

Processor Architecture

Methodology:

One minute paper.

Analysis Report Over Experts

Expert Review 1: Observations:

- 1.
2. Easy to understand the methodology.
- 3.

Expert Review 2: Observations:

- 1.
2. Increased the creativity knowledge.
- 3.

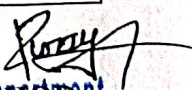
Expert Review 3: Observations:

- 1.
2. Helped in the Analysis of subject.
- 3.

Result of Satisfactory: Excellent / Very Good/ Good/ Satisfactory/ Not Satisfactory

Action Taken Report:

Good subject for the various students

  
Head of the Department  
Electronics & Communication Engineering  
Sreyas Institute of Engineering and Technology  
Beside Indu Aranya, Nagole, Hyderabad-500068.



# Sreyas Institute of Engineering and Technology

## An Autonomous Institution

Approved by AICTE, Affiliated to JNTUH

Accredited by NAAC-A Grade, NBA (CSE, ECE & ME) & ISO 9001:2015 Certified

### DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

#### INNOVATIONS BY THE FACULTY IN TEACHING & LEARNING STUDENTS FEED BACK ANALYSIS REPORT & ACTION TAKEN REPORT

Academic Year: 2024-25 Year: III Semester: II Section:

Faculty Name: B. Sravanthi Subject & Topic: SOCA.

Methodology: One minute paper.

Total number of students : 45

Number of students present : 35

Number of students absent : 10

Maximum Score :

Obtained Score : 80%.

% of Satisfactory : Very Good

Result of Satisfactory: Excellent/ Very Good/ Good/ Satisfactory/Not Satisfactory

Levels of Satisfactory: <50% = Not Satisfactory; >50 to <60% = Satisfactory; >60 to <70% = Good;  
>70 to <80% = Very Good; & >80 to <100 = Excellent

#### Analysis Report:

1. Students are satisfied with the methodology.
- 2.
- 3.

#### Action Taken Report:

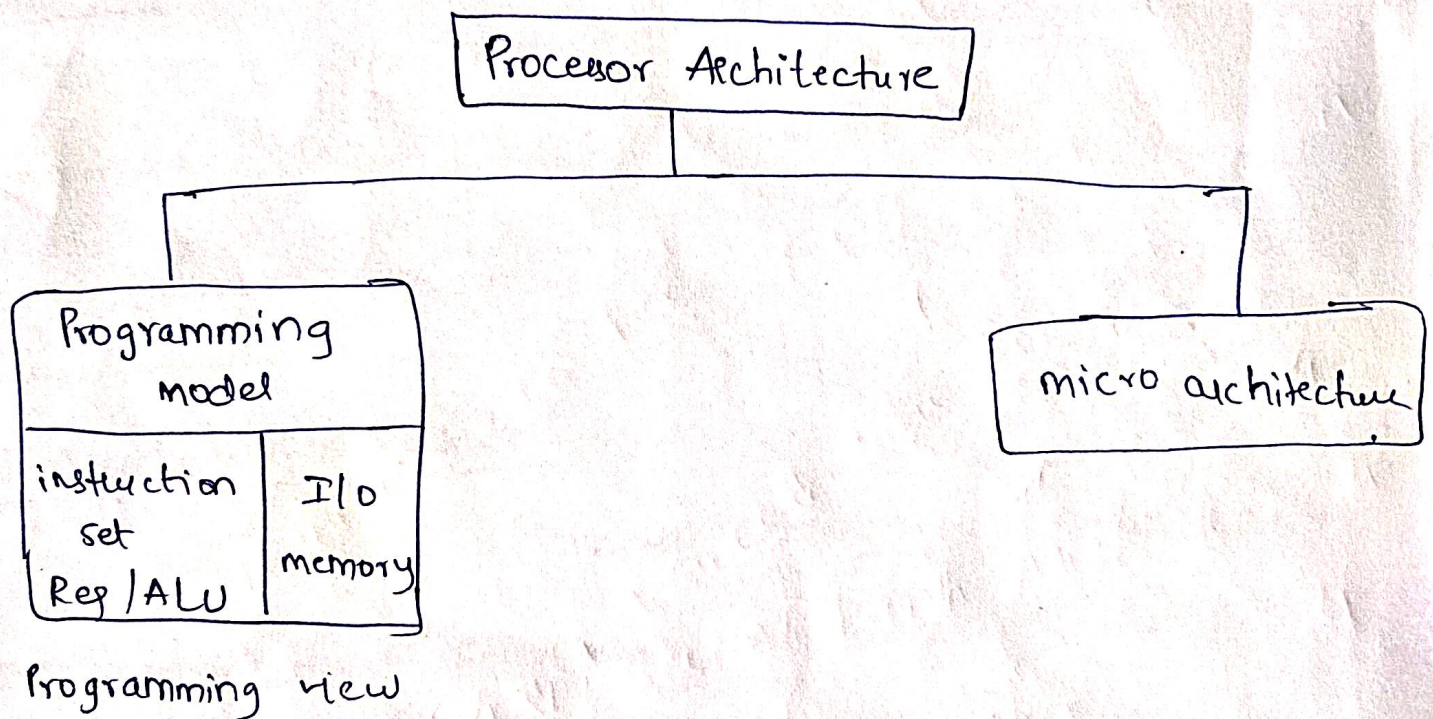
Methodology used for theoretical subjects.

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HOD



- Explain the Concept of the processor Architecture and its implementation?

A. The Processor architecture is made up of the instruction set, the memory that it runs on the control and function units that understand & execute the instruction set expresses a large number of implementation specifics.



### Instruction set Architecture

An ISA is a standard that specifies the available instructions, data types, register, hardware support for managing main memory basic characters I/O devices of a series of implementation of list,