

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

EXPERTS FEEDBACK ON INNOVATIVE METHODS IN TEACHING AND LEARNING PROCESS

Please fill out this feedback form so we ensure quality methodologies in teaching and learning process

Email ID: maruthirao.chinnam@sreyas.ac.in

NAME OF THE EXPERT: CH. S.V. Maruthi Rao.

Innovative Method: Surprise test

Parameters/Ratings	Excellent	Good	Fair	Poor
How would you rate this innovative method?	✓			
How satisfied were you with the clear goals?		✓		
How satisfied were you with appropriate usage?		✓		
How satisfied were you with implementation?	✓			
How satisfied were you with outcomes?		✓		
How satisfied were you with the timeliness?	✓			
Would you recommend our innovative method to others?	YES/NO			
Please provide any additional comments or suggestions.	Good innovative teaching methods.			

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

INNOVATIONS BY THE FACULTY IN TEACHING & LEARNING EXPERT REVIEW ANALYSIS & ACTION TAKEN REPORT

Academic Year: 2024-25 Year: III

Semester: II

Section: B

Faculty Name: Ch. S.V. Maruthi Rao Subject & Topic: CMOS VLSI

Methodology: Surprise test

Analysis Report Over Experts

Expert Review 1: Observations:

- 1.
2. Helps to understand the topic easily.
- 3.

Expert Review 2: Observations:

- 1.
2. Increased in the creativity.
- 3.

Expert Review 3: Observations:

- 1.
2. Helped in Analysis of the subject
- 3.

Result of Satisfactory: Excellent / Very Good / Good / Satisfactory / Not Satisfactory

Action Taken Report:

It can implemented for more and more subjects

Head of the Department
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

INNOVATIONS BY THE FACULTY IN TEACHING & LEARNING
STUDENTS FEED BACK ANALYSIS REPORT & ACTION TAKEN REPORT

Academic Year: 2024-25 Year: II

Semester: II

Section: B

Faculty Name: Ch.S.V. Maruthi Rao Subject & Topic: CMOS VLSI

Methodology:

Total number of students : 65

Number of students present : 55

Number of students absent : 10

Maximum Score :

Obtained Score : 79/100

% of Satisfactory : Very Good

Result of Satisfactory: Excellent/ Very Good/ Good/ Satisfactory/Not Satisfactory

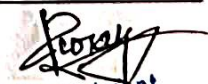
Levels of Satisfactory: <50% = Not Satisfactory; >50 to <60% = Satisfactory; >60 to <70% = Good;
>70 to <80% = Very Good; & >80 to <100 = Excellent

Analysis Report:

- 1.
2. Helps in Analysis of subject
- 3.

Action Taken Report:

Improved in the sequence of various subjects


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Quiz questions

For FET, input current $I_G =$

- a. High
- b. Low
- c. Zero
- d. Infinite

In _____ region, FET acts as voltage variable resistor

- a. Linear
- b. Ohmic
- c. Saturation
- d. Cutoff

Depletion MOSFET works in

- a. Depletion mode only
- b. Enhancement mode only
- c. Both modes
- d. None

The input impedance of E MOSFET is _____ D MOSFET

- a. Less than
- b. Greater than
- c. Equal to
- d. Less than or equal to

FET is _____ temperature stable as compared to BJT.

- a. More
- b. Less
- c. Equal
- d. None

Channel is permanently present in _____ MOSFET

- a. E MOSFET
- b. D MOSFET
- c. Both
- d. None

Enhancement MOSFET works in

- a. Depletion mode only
- b. Enhancement mode only
- c. Both modes
- d. None

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In _____ region JFET acts as an amplifier

- a. Cutoff
- b. Linear
- c. Ohmic
- d. Saturation

The current in JFET is due to _____

- a. Majority charge carriers
- b. Minority charge carriers
- c. Both
- d. None

E MOSFET starts conducting current when

- a. $V_{GS} \leq V_{th}$
- b. $V_{GS} \geq V_{th}$
- c. $V_{th} \leq 0$
- d. Any of the above

If $V_o=0$, the transistor is said to be in _____ state

- a. On
- b. Off
- c. High Impedance
- d. None

Phase shift of 180° between input and output occurs in which configuration?

- a. CB
- b. CE
- c. CC
- d. CD

In active region transistor acts as _____

- a. Amplifier
- b. Switch
- c. Rectifier
- d. None

In punch through effect, if voltage is increased rapidly, then _____ terminal disappears

- a. Emitter
- b. Base
- c. Collector
- d. Source

The current gain in CB configuration is _____.

- a. Unity
- b. less than unity

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- c. greater than unity
- d. none

Early effect occurs in _____ configuration.

_____ is transferred in transistors.

BJT is a _____ device.

Output parameters of common emitter are _____.

Current gains of CB, CE, CC are _____, _____.

The advantage of E-MOSFET is _____.

JFET works with _____ bias.

When MOSFET is used as a capacitor, the 3 operating modes are _____, _____.

The advantage of JFET over BJT is _____.

Input resistance of MOSFET is very high because _____ is isolated from channel.

The insulated material in MOSFET is made up of _____ material.

When VGS in JFET is made sufficiently negative ID is reduced to _____.

FET is _____ controlled device.

Abbreviation of JFET _____.

Abbreviation of E-MOSFET _____.



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